
802.3df 800GBASE-CR8 Annex 162C/162D Table Revisions

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Purpose

- **Annex 162C and Annex 162D table revisions to reflect 800GBASE-CR8 adoption**

100 Gbps/lane

800GbE (8x100) – Copper cable

- Copper cable
 - Align to IEEE P802.3ck D3.x Clause 162, Annex 162A, Annex 162B, Annex 162C, Annex 162D
 - With editorial license, update text, figures and tables to reflect 800GBASE-CR8 (n=8, where applicable)

Source:

https://www.ieee802.org/3/df/public/22_02/lusted_3df_01a_220203.pdf

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Annex 162C

- Annex 162C - IEEE Draft P802.3ck/D3.1

Table 162C-1—Number of PMDs supportable for each connector type

MDI types	100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	Reference
SFP112	1	—	—	162C.2.1
SFP-DD112	1,2	1	—	162C.2.2
DSFP	1, 2	1	—	162C.2.3
QSFP112	1, 2, 4	1, 2	1	162C.2.4
QSFP-DD800	1, 2, 4, 8	1, 2, 4	1, 2	162C.2.5
OSFP	1, 2, 4, 8	1, 2, 4	1, 2	162C.2.6

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Table 162C-1—Number of PMDs supportable for each connector type

MDI types	100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	800GBASE-CR8	Reference
SFP112	1	—	—	—	162C.2.1
SFP-DD112	1,2	1	—	—	162C.2.2
DSFP	1, 2	1	—	—	162C.2.3
QSFP112	1, 2, 4	1, 2	1	—	162C.2.4
QSFP-DD800	1, 2, 4, 8	1, 2, 4	1, 2	1	162C.2.5
OSFP	1, 2, 4, 8	1, 2, 4	1, 2	1	162C.2.6

Annex 162C - IEEE Draft P802.3ck/D3.1

Table 162C–2—PMD to connector signal assignments

PMD signal <PMD number>:<PMD signal>			Connector signal
100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	
0:DL0n	0:DL0n	0:DL0n	DL0n
0:DL0p	0:DL0p	0:DL0p	DL0p
1:DL0n	0:DL1n	0:DL1n	DL1n
1:DL0p	0:DL1p	0:DL1p	DL1p
2:DL0n	1:DL0n	0:DL2n	DL2n
2:DL0p	1:DL0p	0:DL2p	DL2p
3:DL0n	1:DL1n	0:DL3n	DL3n
3:DL0p	1:DL1p	0:DL3p	DL3p
4:DL0n	2:DL0n	1:DL0n	DL4n
4:DL0p	2:DL0p	1:DL0p	DL4p
5:DL0n	2:DL1n	1:DL1n	DL5n
5:DL0p	2:DL1p	1:DL1p	DL5p
6:DL0n	3:DL0n	1:DL2n	DL6n
6:DL0p	3:DL0p	1:DL2p	DL6p
7:DL0n	3:DL1n	1:DL3n	DL7n
7:DL0p	3:DL1p	1:DL3p	DL7p
0:SL0n	0:SL0n	0:SL0n	SL0n
0:SL0p	0:SL0p	0:SL0p	SL0p
1:SL0n	0:SL1n	0:SL1n	SL1n
1:SL0p	0:SL1p	0:SL1p	SL1p
2:SL0n	1:SL0n	0:SL2n	SL2n
2:SL0p	1:SL0p	0:SL2p	SL2p
3:SL0n	1:SL1n	0:SL3n	SL3n
3:SL0p	1:SL1p	0:SL3p	SL3p
4:SL0n	2:SL0n	1:SL0n	SL4n
4:SL0p	2:SL0p	1:SL0p	SL4p
5:SL0n	2:SL1n	1:SL1n	SL5n

Table 162C–2—PMD to connector signal assignments (continued)

PMD signal <PMD number>:<PMD signal>			Connector signal
100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	
5:SL0p	2:SL1p	1:SL1p	SL5p
6:SL0n	3:SL0n	1:SL2n	SL6n
6:SL0p	3:SL0p	1:SL2p	SL6p
7:SL0n	3:SL1n	1:SL3n	SL7n
7:SL0p	3:SL1p	1:SL3p	SL7p

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Table 162C–2—PMD to connector signal assignments

PMD signal <PMD number>: <PMD signal>				Connector signal
100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	800GBASE-CR8	
0:DL0n	0:DL0n	0:DL0n	0:DL0n	DL0n
0:DL0p	0:DL0p	0:DL0p	0:DL0p	DL0p
1:DL0n	0:DL1n	0:DL1n	0:DL1n	DL1n
1:DL0p	0:DL1p	0:DL1p	0:DL1p	DL1p
2:DL0n	1:DL0n	0:DL2n	0:DL2n	DL2n
2:DL0p	1:DL0p	0:DL2p	0:DL2p	DL2p
3:DL0n	1:DL1n	0:DL3n	0:DL3n	DL3n
3:DL0p	1:DL1p	0:DL3p	0:DL3p	DL3p
4:DL0n	2:DL0n	1:DL0n	0:DL4n	DL4n
4:DL0p	2:DL0p	1:DL0p	0:DL4p	DL4p
5:DL0n	2:DL1n	1:DL1n	0:DL5n	DL5n
5:DL0p	2:DL1p	1:DL1p	0:DL5p	DL5p
6:DL0n	3:DL0n	1:DL2n	0:DL6n	DL6n
6:DL0p	3:DL0p	1:DL2p	0:DL6p	DL6p
7:DL0n	3:DL1n	1:DL3n	0:DL7n	DL7n
7:DL0p	3:DL1p	1:DL3p	0:DL7p	DL7p
0:SL0n	0:SL0n	0:SL0n	0:SL0n	SL0n
0:SL0p	0:SL0p	0:SL0p	0:SL0p	SL0p
1:SL0n	0:SL1n	0:SL1n	0:SL1n	SL1n
1:SL0p	0:SL1p	0:SL1p	0:SL1p	SL1p
2:SL0n	1:SL0n	0:SL2n	0:SL2n	SL2n
2:SL0p	1:SL0p	0:SL2p	0:SL2p	SL2p
3:SL0n	1:SL1n	0:SL3n	0:SL3n	SL3n
3:SL0p	1:SL1p	0:SL3p	0:SL3p	SL3p
4:SL0n	2:SL0n	1:SL0n	0:SL4n	SL4n
4:SL0p	2:SL0p	1:SL0p	0:SL4p	SL4p
5:SL0n	2:SL1n	1:SL1n	0:SL5n	SL5n

Table 162C–2—PMD to connector signal assignments (continued)

PMD signal <PMD number>: <PMD signal>				Connector signal
100GBASE-CR1	200GBASE-CR2	400GBASE-CR4	800GBASE-CR8	
5:SL0p	2:SL1p	1:SL1p	0:SL5p	SL5p
6:SL0n	3:SL0n	1:SL2n	0:SL6n	SL6n
6:SL0p	3:SL0p	1:SL2p	0:SL6p	SL6p
7:SL0n	3:SL1n	1:SL3n	0:SL7n	SL7n
7:SL0p	3:SL1p	1:SL3p	0:SL7p	SL7p

Annex 162D - IEEE Draft P802.3ck/D3.1

- Revision Annex 162D - add table-5

Table 162D-5—800GBASE-CR8 cable assembly types and supportable PMDs

one end		other end(s)		supportable number of PMDs
Receptacle/Plug	Number	Receptacle/Plug	Number	Number
QSFP-DD800	1	QSFP-DD800	1	1
QSFP-DD800	1	OSFP	1	1
OSFP	1	OSFP	1	1