

## **Meeting Minutes: IEEE P802.3dg Long-Reach Single Pair Ethernet Task Force**

January 20, 2026, Hybrid Task Force Interim Meeting

In-Person in Austin, TX and remotely via Webex

Prepared by Bob Voss

All times in CST

IEEE P802.3dg Task Force meeting was called to order at 1/20/2026, 8:03 AM by the task force chair, George Zimmerman.

The meeting was held electronically using Webex.

Attendance is listed in Appendix A.

All presentations referenced in these minutes are located on the Task Force Public web page:

[https://www.ieee802.org/3/dg/public/May\\_2025/index.htm](https://www.ieee802.org/3/dg/public/May_2025/index.htm)

The Chair displayed and proceeded to review the agenda in

[https://www.ieee802.org/3/dg/public/May\\_2025/agenda\\_3dg\\_01b\\_20260120.pdf](https://www.ieee802.org/3/dg/public/May_2025/agenda_3dg_01b_20260120.pdf)

### **Motion #1**

**Move to approve the agenda as shown on agenda\_3dg\_01a\_20260120.pdf slide 3**

**M: Jon Lewis**

**S: Brian Murray**

**Procedural (> 50%)**

**Motion passes by voice without opposition**

The agenda was approved at 8:08 AM.

Approval of meeting minutes

The chair asked if anyone wanted to review or change the meeting minutes from the November plenary meeting or the December electronic interim. None heard.

### **Motion #2**

**Move to approve the minutes from November plenary and December interim**

**M: Peter Jones**

**S: Jon Lewis**

**Procedural (> 50%)**

**Motion passes by voice without opposition**

The chair continued the agenda deck.

The chair reviewed General Decorum and Task Force Decorum slides with the task force.

**Members of the Press**, at 8:11AM the chair asked for any press members to identify themselves. None heard.

### **Goals for the Meeting**

- Build consensus for resolution of potential initial SA ballot comments on IEEE P802.3dg D3.0

The chair reviewed slides with the task force pertaining to Meeting Fees, Reflector and Web, Task Force Private Area, Ground Rules, and Attendance.

**Attendance**, the Chair advised the group that the attendance would be taken from both IMAT and Webex.

At 8:25 AM, the Chair resumed review of the agenda deck, including the following items – a review of the participation policy, a review of the IEEE copyright policy, a review of the IEEE policy on dominance, and a review of the IEEE Standards process. There were no questions.

**IEEE Patent Policy**, the Chair displayed the slides relating to the patent policy.

The **Call for Patents** was made at 8:33 AM and **none** responded.

The Chair resumed review of the agenda deck material after the call for patents.

The chair reviewed the slides concerning IEEE SA Copyright policy, participant behavior, individual process, fair and equitable consideration, and position of this project in the IEEE 802.3 process.

The chair said there are no liaisons or communications for the task force to consider at this meeting. The chair reviewed the revised timeline with the task force.

### **Presentations and Discussion**

The task force heard presentations beginning at 8:46AM. Presentations intended to build consensus for resolution of potential initial SA ballot comments.

#### **Issues for Discussion in Comments**

- **MDI Mode Conversion (see Graber presentation)**
- **Downshift State Diagram (see Zimmerman presentation)**
- **Timing in the PCS TX state diagram for EEE (discuss)**
- **Transmit path delay in non-FED operation**
- **Do we need to specify TCTL as well for the link segment?**

8:49 AM

**Presentation:** MDI Mode Conversion

**Presenter:** Steffen Graber, Pepperl + Fuchs

**Presentation:** [https://www.ieee802.org/3/dg/public/May\\_2025/graber\\_3dg\\_01\\_01202026.pdf](https://www.ieee802.org/3/dg/public/May_2025/graber_3dg_01_01202026.pdf)

Questions and discussion began at 8:57AM

Mr. Zimmerman asked Mr. Voss to control the queue for the following presentation.

9:03 AM

**Presentation:** Downshift State Diagram Issues

**Presenter:** George Zimmerman, CME Consulting/various

**Presentation:** [https://www.ieee802.org/3/dg/public/May\\_2025/zimmerman\\_3dg\\_01\\_012026.pdf](https://www.ieee802.org/3/dg/public/May_2025/zimmerman_3dg_01_012026.pdf)

Questions and discussion began at 9:10 AM

9:12 AM

**Presentation:** Downshift: Response to “Downshift State Diagram Issues” contribution from George Zimmerman

**Presenter:** Peter Jones, Cisco Systems

**Presentation:** [https://www.ieee802.org/3/dg/public/May\\_2025/jones\\_3dg\\_01a\\_011925.pdf](https://www.ieee802.org/3/dg/public/May_2025/jones_3dg_01a_011925.pdf)

Questions and discussion began at 9:17 AM

The chair introduced a special guest, Sean Vincent of Fieldcomm Group.

At 9:45AM, having completed the planned presentations, the chair returned to the Issues for Resolution slide to initiate discussion on those points.

Discussion Topic – Delay Constraints, per Clause 190.10

### **Morning Break from 9:55 AM to 10:20AM**

At 10:21AM the task force resumed its discussion.

Discussion Topic – TCTL specification

At 10:44AM, the task force began discussing a new topic.

Discussion Topic – Timing in the PCS state diagram for EEE

### **Next Meetings**

- Electronic interim meeting for comment resolution on D 3.0
  - TBA (expect between Feb. 11 and Feb 18)
  - See <https://www.ieee802.org/3/interims/index.html> for information
- March 2026 802 Plenary, Vancouver, BC, Canada
  - March 9-12, 2026, 802.3dg TF days TBD
  - Hyatt Regency Vancouver
  - In-person with remote access – REGISTRATION REQUIRED
    - See <https://www.ieee802.org/3/interims/index.html> for information

Having exhausted the agenda for this meeting, the chair adjourned the meeting at 11:11 AM.

## Appendix A: IEEE P802.3dg Long-Reach Single Pair Ethernet Task Force Attendance

FirstName	LastName	Affiliation	IMAT	WebEx
Shiju	Abraham	Acsia Technologies	X	X
Ramanjit	Ahuja	Onsemi	X	X
Tim	Baggett	Microchip Technology, Inc.	X	
Rich	Boyer	Aptiv Signal and Power Solutions	X	X
Michal	Brychta	Analog Devices Inc.	X	X
Valliaappan	Chidambaram	Acsia	X	X
Ahmad	Chini	Broadcom		X
Shaoan	Dai	Infineon Technologies	X	X
Naomichi	Enoki	Murata Electronics	X	X
Daniel	Estrakh	Valens Semiconductor	X	X
Paul	Fuller	Infineon Technologies	X	X
Sebastien	Gareau	Ciena Corporation	X	
Sachin	Goel	NXP	X	
Steffen	Graber	Pepperl+Fuchs SE	X	X
Anil	Hameed	Acsia Technologies	X	X
Anil	Hameed	Acsia Technologies	X	X
Brad	Jeffreis	Analog Devices Inc.	X	
Brad	Jeffries	ADI		X
Markus	Jochim	General Motors Company	X	
Chad	Jones	Cisco Systems, Inc.	X	X
Peter	Jones	Cisco Systems, Inc.	X	X
Ragnar	Jonsson	Marvell	X	X
Gloria	Joseph	Acsia Technologies	X	X
Venkata	Kandarpa	NXP Semiconductors	X	
Gyudong	Kim	Analog Devices Inc.	X	X
Kihong/Joshua	Kim	Celestica	X	X
Yongbum	Kim	General Motors Company	X	X
Jörg	Kock	NXP	X	X
Dongjae	Lee	Newracom Inc.	X	X
Jon	Lewis	Dell Technologies	X	
Hoei	Lim	NXP Semiconductors	X	X
Richard	Long	TE Connectivity	X	X
Wei	Lou	Broadcom Corporation	X	X
Valerie	Maguire	Copperopolis, aff'l with CME Consulting, Cisco, Dell, ADI, MCHP, and NXP	X	X
Simon	Mark	Würth Elektronik Group	X	X
Brett	McClellan	Marvell Semiconductor, Inc.	X	X
Brian	Mowad	Cisco		X
Brian	Murray	Analog Devices Inc.	X	X
SHOJI	OGAWA	FURUKAWA ELECTRIC	X	

Eiichiro	Oishi	Yazaki Corporation	X	X
Sujan	Pandey	Velinktech	X	X
Arkadiy	Peker	Microchip		X
Neven	Pischl	Broadcom Corporation	X	X
Jason	Potterf	Cisco Systems, Inc.	X	
Alireza	Razavi	Infineon Technologies	X	X
Anton	Schedl	BMW Group	X	X
Rohit	Sharma	Molex Incorporated	X	X
jingcong	Sun	Motorcomm Electronic Technology Co	X	X
MEHMET	TAZEBAY	Broadcom Corporation	X	X
Rajan	Thomas	Acsia Technologies	X	X
Geoffrey	Thompson	INDEPENDENT	X	X
Luisma	Torres	KD	X	X
Sean	Vincent	FCG	X	X
Bob	Voss	Panduit	X	X
Terry	Wei	Analog Devices Inc.	X	X
Natalie	Wienckowski	IVN Solutions LLC; Ethernovia	X	X
James	Withey	Fluke Corporation	X	X
Dance	Wu	Infineon Technologies	X	X
Junjie	Xie	PROVIDED BY THE LEADERSHIP EACH DAY	X	
Tingting	Zhang	Huawei Technologies Co., Ltd	X	X
Yan	Zhuang	Huawei Technologies Co., Ltd	X	X
George	Zimmerman	CME Consulting/Analog Devices, APL Group, Cisco, Infineon, OnSemi, Sony	X	X