Unconfirmed Meeting Minutes: IEEE P802.3dg Long-Reach Single Pair Ethernet Task Force

July 28, 2025, Hybrid Plenary Meeting via Webex Prepared by Bob Voss All times in CEST (Madrid)

IEEE P802.3dg Task Force meeting was called to order at 07/28/2025, 4:31 PM by the task force chair, George Zimmerman.

The meeting was held in person in Madrid, Spain, and electronically using Webex.

Attendance is listed in Appendix A.

All presentations referenced in these minutes are located on the Task Force Public web page: https://www.ieee802.org/3/dg/public/May_2025/index.htm

The Chair displayed and proceeded to review the agenda in https://www.ieee802.org/3/dg/public/May 2025/agenda 3dg 01b 20250728.pdf

The agenda was approved at 4:34 PM by unanimous consent.

Approval of prior meeting minutes

Approve multiple 802.3dg meeting minutes at:

https://www.ieee802.org/3/dg/public/May 2025/Unconfirmed minutes 3dg 03122025.pdf https://www.ieee802.org/3/dg/public/may2025/Unconfirmed minutes 3dg 20250625.pdf https://www.ieee802.org/3/dg/public/may2025/Unconfirmed minutes 3dg 20250709.pdf

Motion #1

Move to approve the May 13, June 25, and July 9, 2025, Interim IEEE P802.3dg minutes https://www.ieee802.org/3/dg/public/May_2025/Unconfirmed_minutes_3dg_03122025.pdf and

https://www.ieee802.org/3/dg/public/may2025/Unconfirmed minutes 3dg 20250709.pdf

M: Jon Lewis

S: Valerie Maguire

Procedural (> 50%)

Motion passes, approved without objection

Members of the Press, at 4:37 PM the chair asked for any press members to identify themselves. None heard.

General Decorum

The chair informed the group of General Decorum expectations and pointed out the prohibition of recordings, including AI transcription assistants.

Task Force Decorum

The Task Force Decorum slide was shown by the chair.

Goals for the Meeting

P802.3dg

- Fill in remaining TBDs
- Review draft for technical completeness
- Request initial Working Group ballot

Meeting fee requirement

The chair announced that this meeting is conducted as part of the IEEE 802 Plenary Meeting. Registration and payment of fees is required, and attendance without properly registering is subject to penalties under IEEE 802 rules.

• See https://www.ieee802.org/3/interims/index.html for further information and a registration link

The chair displayed and reviewed slides for Reflector and Web and 802.3dg Task Force Private Area.

Ground Rules

The chair reviewed the Ground Rules slide with the task force.

Attendance, the Chair advised the group that the attendance would be taken from Webex and IMAT. In-room participants were asked to please log into Webex to request to join the queue by entering the letter "Q" in the chat.

At 4:43 PM, the Chair resumed review of the agenda deck, including the following items – a review of IEEE structure, links to important bylaws and rules, and the patent slides.

IEEE Patent Policy, The Chair asked if anyone needed the patent slides to be read aloud. None responded, so the patent slides 1 through 4 were displayed.

The Call for Patents was made at 4:44 PM and none responded.

The Chair resumed review of the agenda deck material after the call for patents.

At 4:45 PM, the chair reviewed the slides concerning IEEE SA Copyright policy, Participant Behavior, Individual Process, Fair and Equitable Consideration, and position of this project in the IEEE 802.3 process.

Liaisons and Communications

The chair reported that there are no liaisons or communications for the task force to consider at this time

Presentations and Discussion:

At 4:51PM, the chair moved to presentations and discussion.

At 4:52 PM, the chair moved to the first presentation.

Presentation: Transmit PSD, MDI Return Loss and Droop Presenter: Michal Brychta, affiliated with Analog Devices

https://www.ieee802.org/3/dg/public/May 2025/Brychta 3dg 01 07282025 update.pdf

Beginning at 5:14 PM, questions were asked and answered.

At 5:23 PM, the chair moved to the next presentation.

Presentation: MDI Mode Conversion Loss and MDI Fault Tolerance

Presenter: Michal Brychta, affiliated with Analog Devices

https://www.ieee802.org/3/dg/public/May 2025/Curran 3dg 01 07282025 update.pdf

At 5:34 PM, questions were asked and answered.

At 5:51 PM, the chair moved to the next presentation.

Presentation: Delay constraints and LPI timing parameters Presenter: Michal Brychta, affiliated with Analog Devices

https://www.ieee802.org/3/dg/public/May 2025/Riesco 3dg 01 07282025.pdf

The chair called for questions about this presentation. None heard.

The next presentation began at 5:57 PM.

Presentation: Editor's Report

Presenter: Yan Zhuang, affiliated with Huawei Technologies

https://www.ieee802.org/3/dg/public/May 2025/zhuang 3dg 01 0725.pdf

No questions were heard regarding the Editor's Report.

The chair then asked the task force to review the editor's notes in the draft. Yan led the review of these notes by displaying the draft.

During the editor's notes review, the chair asked if there was any objection to editing Figure 190-4, offering to provide a motion to add the corrected figure to the draft. No objections heard.

Yan reviewed the editor's notes review by moving to the next note.

Motion #2

Move to adopt slides 11 & 12 of

https://www.ieee802.org/3/dg/public/May 2025/Riesco 3dg 01 07282025.pdf and remove Editor's Notes associated with the related text in d1p3

M: Jon Lewis

S: Michal Brychta

Technical >=75%

Motion passes without objection.

Motion #3

Move to replace Figure 190-4 with Frame-drawn figure and correct PMA Service Interface Symbols, S_7 and S_12 to S_6 and S_11.

M: Valerie Maguire S: Peter Jones

Technical >= 75%

Motion passes without objection

Motion #4

Move to adopt slide 10 of

https://www.ieee802.org/3/dg/public/May 2025/Curran 3dg 01 07282025 update.pdf

with editorial license to follow equation style.

M: Michal BrychtaS: Peter Jones

Technical >= 75%

Motion passes without objection

The chair asked the task force to consider overnight if the fault tolerance issue should be addressed. Tomorrow's session will begin with motions and proceed to the remaining presentation from Mr. Peter Jones.

Having exhausted the allotted time but still having agenda items to address, the chair recessed this task force meeting until 9:00AM CEST, 29Jul2025.

The chair called the meeting back to order at **9:01 AM CEST on 29Jul2025**. As the meeting was recessed, the chair asked if anyone hadn't seen the front matter slides or wish to review any portion in detail. The chair proceeded to review the agenda deck.

Members of the Press, at **9:04AM** the chair asked if there were any members of the press present and if so, that they announce themselves. None heard.

The chair continued displaying and reviewing the agenda deck

At 9:07AM, after reviewing patent slides 1 through 4, the chair made the Call for Patents. None heard.

The Copyright policy was shown, and the task force was reminded by the chair that we all agree to abide by the IEEE Code of Ethics. Slides regarding Participant Behavior, Individual Process, Fair & Equitable Consideration and the project's position in the IEEE 802.3 Standards Process were displayed and read by the chair.

The chair resumed TF business resuming the previous day's discussion of MDI Fault Tolerance.

Motions

Motion #5

Move to adopt slide 11 of Curran 3dg 01 07282025 update.pdf with editorial license.

M: Michal Brychta
S: Chad Jones

Technical >= 75%

Motion passes without objection.

Motion #6

Move to

Generate draft 2.0 from Draft 1.3 and approved motions this meeting Request that IEEE 802.3 Working Group progress the draft to Working Group Ballot

M: Jon Lewis

S: Valerie Maguire

Technical >= 75%

Motion passes without objection

Having concluded motions, at **9:23 AM**, the chair asked Peter Jones to present his contribution to the task force.

Presentation: IEEE 802.3dg 100BASE-T1L Downshift/Upshift Presenter: Peter Jones, affiliated with Cisco Systems

https://www.ieee802.org/3/dg/public/May_2025/jones_3dg_july_2025_01.pdf (presentation) https://www.ieee802.org/3/dg/public/May_2025/jones_3dg_july_2025_02.pdf (proposed draft text)

Mr. Jones reviewed the presentation and then moved to a preview of the proposed draft text. Both files are posted at the URLs shown above.

Beginning at 9:44 AM, questions were asked and answered.

Having concluded presentations, the chair resumed the agenda deck at 10:10 AM.

Motion #7

Move to request IEEE Editorial Review 802.3dg d2.0 and subject to that review, request sharing the draft with TIA TR42.7.

M: Chad Jones S: Jon Lewis

Technical >= 75%

Motion passes without objection

Next Meetings

- September 2025 Interim, Minneapolis, MN
 - September 15-19, 2025, 802.3dg TF days TBD
 - o In-person with remote access REGISTRATION REQUIRED
 - See https://www.ieee802.org/3/interims/index.html for information.
- Expect much of comment resolution to be on electronic interims, to be announced.
 - Determine tentative dates for these meetings in early August
 - o Chair to keep the TF apprised via the reflector.

Having exhausted the agenda, the chair adjourned the meeting at 10:17 AM CEST.

Appendix A: IEEE P802.3dg Long-Reach Single Pair Ethernet Task Force Attendance

Name	Affiliation	WEBEX	IMAT
Aekins, Rob	Legrand	YES	YES
Ahuja, Ramanjit	Onsemi	YES	YES
Aronson, Joseph	TI	YES	YES
Arroyo, Hector	ADI	YES	YES
Baggett, Tim	Microchip	YES	YES
Bar-Niv, Amir	Marvell	YES	YES
Beauregard, Francois	Belden	YES	YES
Benyamin, Saied	Ethernovia	YES	YES
boyer, rich	Aptiv	YES	YES
Brandt, David	Rockwell Automation	YES	YES
Brychta, Michal	Analog Devices	YES	YES
Chang, Jae-yong	Keysight	YES	YES
Cordaro, Jay	Analog Devices,Inc.	YES	YES
Donahue, Curtis	Rohde & Schwarz	YES	YES
Eguchi, Keisuke	Analog Devices	YES	YES
Ganesan, Aravind	TI	YES	YES
Goto, Hideki	Toyota Motor Corporation	YES	YES
Graber, Steffen	Pepperl+Fuchs	YES	YES
Gubow, Martin	Keysight Technologies	NO	YES
Haydt, Mary Sue	Microchip	YES	YES
Houck, TJ	Marvell	YES	YES
Hutchison, Guy	Aviva Links	YES	YES
HYAKUTAKE, YASUHIRO	Orbray Co., Ltd.	NO	YES
Jeffreis, Brad	ADI	YES	YES
Jones, Chad	Cisco	YES	YES
Jones, Peter	Cisco	YES	YES
Jonsson, Ragnar	Marvell	YES	YES
Kawatsu, Yasuaki	APRESIA Systems	YES	YES
Kikuta, Tomohiro	Orbray Co.,Ltd.	YES	YES
Kim, Do Kyun	LGE	YES	YES
Kim, Gyudong	Analog	YES	YES
Kim, Kihong/Joshua	Hirose Electric (USA), Inc.	NO	YES
Kim, Yongbum	General Motors Company	NO	YES
Kleinwaechter, Mathias	in-tech	YES	YES
Kotani, Yasuhiro	DENSO	YES	YES
Law, David	Hewlett Packard Enterprise	NO	YES
Lewis, Jon	Dell Technologies	NO	YES
Lo, William	Axonne Inc.	NO	YES
Long, Richard	TE Connectivity	YES	YES
Lou, Wei	Broadcom	YES	YES
Maguire, Valerie	Copperopolis, aff'l w/ CME Consulting and Cisco	YES	YES
Mark, Simon	Wuerth Elektronik	YES	YES
McClellan, Brett	Marvell	YES	YES
Miskho, Michael	ADI	YES	YES
Murray, Brian	Analog Devices	YES	YES
Nakamura, Yuto	Furukawaelectric	YES	YES
Niihara, Yoshihiro	Fujikura Ltd.	YES	YES
Oishi, Eiichiro	Yazaki	YES	YES
Pandey, Sujan	Velink	NO	YES
Parthasarathy, Vasu	Broadcom Corporation	NO	YES
Paul, Michael	ADI	YES	YES
Razavi, Alireza	Marvell	NO	YES

Name	Affiliation	WEBEX	IMAT
Regev, Alon	Keysight	YES	YES
Schedl, Anton	BMW Group	YES	YES
Schreiner, Stephan	Rosenberger	YES	YES
SETH, SUMANTRA	TI	YES	YES
Sharma, Rohit	Molex	YES	YES
Shiino, Masato	Furukawa Electric	YES	YES
Sriram, Chandrasekhar	Texas Instruments	YES	YES
Strohmeier, Heiko	Bosch	YES	YES
Sun, jingcong	motorcomm	YES	YES
Takeuchi, Junichi	JAE	YES	YES
Tanc, Ahmet	NXP	YES	YES
TAZEBAY, MEHMET	Broadcom Corporation	NO	YES
Thompson, Geoffrey	GraCaSi-Ind	YES	YES
Voss, Robert	Panduit	YES	YES
Wienckowski, Natalie	IVN Solutions; Ethernovia	YES	YES
Withey, James	Fluke	YES	YES
Zhuang, Yan	Huawei	YES	YES
Zimmerman, George	CME Consulting/ADI,APL Gp,Cisco,Marvell,OnSemi,Sony	YES	YES