Editor's Report: P802.3dg draft 1.3

Yan Zhuang (Chief Editor), Huawei Technologies

Valerie Maguire (Editor), Copperopolis (affiliated w/Cisco & CME Consulting)

George Zimmerman (Chair), CME Consulting, Inc (ADI, APL Group, Cisco, Marvell, OnSemi, Sony)

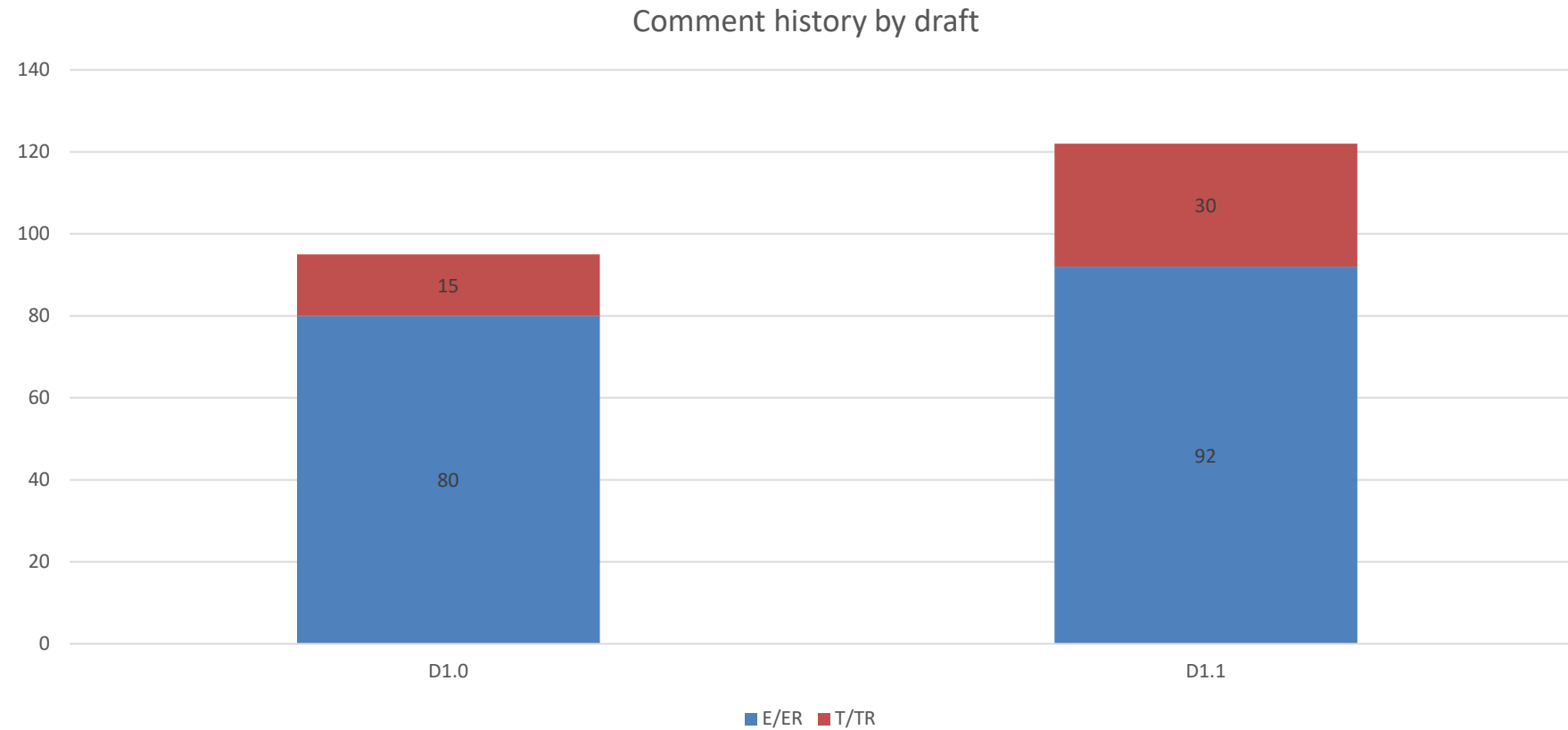IEEE P802.3dg Task Force Plenary, July 2025

Introduction

- Activities since Draft 1.0 (May plenary)
- Comments status
- Remaining technical contents to be filled
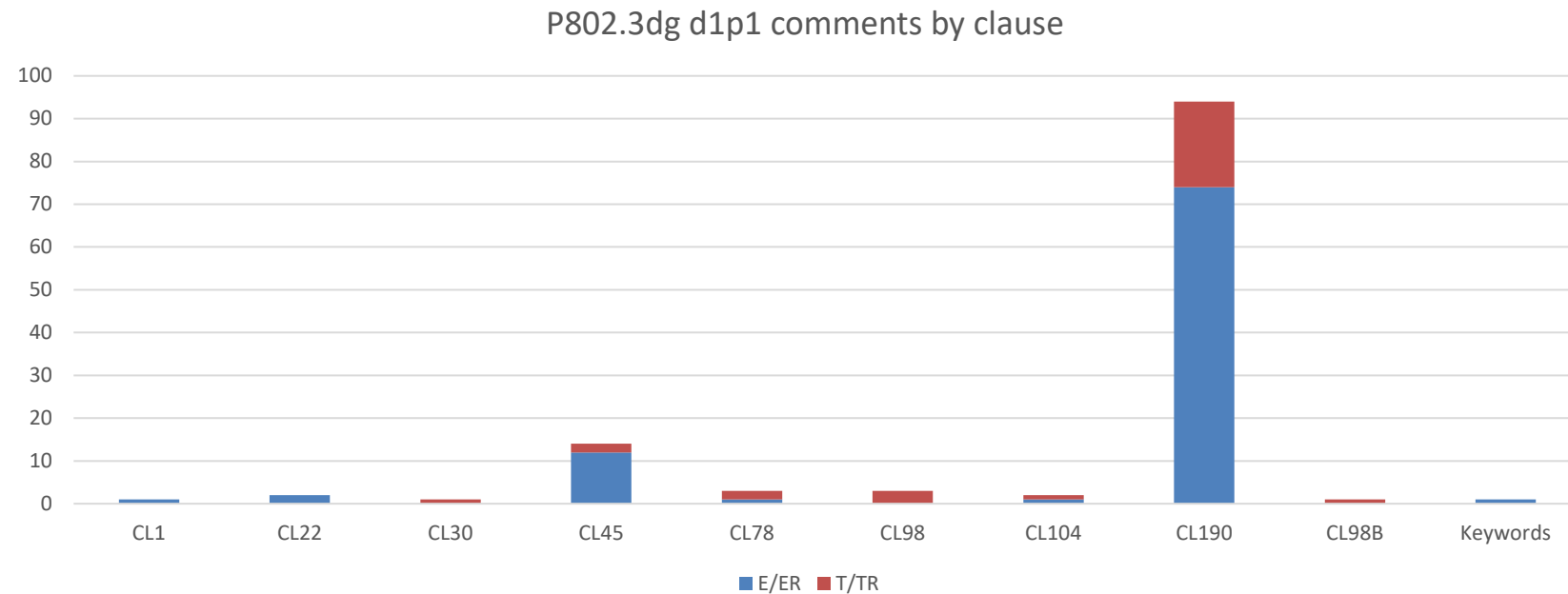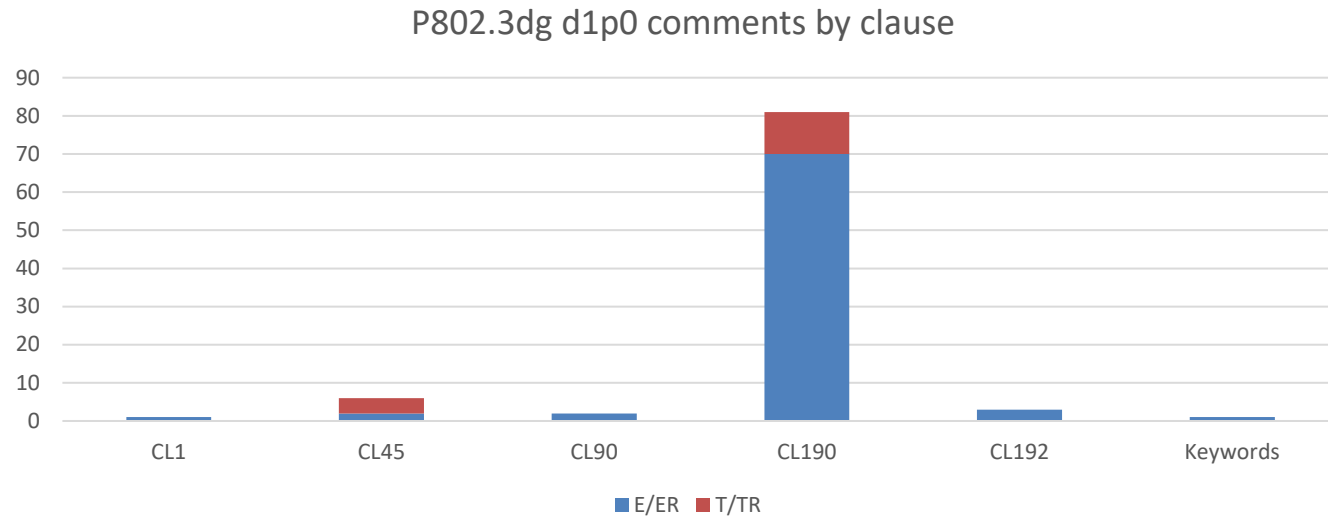- Expectations for this meeting

Activities since Draft 1.0

- Created Draft 1.1 and TF review on d1.1
  - Opened on 5th June and closed on 19th June
  - 121 comments received
- Two electronic interim meetings for comments resolutions.
- Created Draft 1.2 and Draft 1.3

Comment history by draft number

Comment history by draft

Comments by clause

P802.3dg d1p0 comments by clause

P802.3dg d1p1 comments by clause

Ask for Technical contents (1)

Registers - PMA/PMD types	
45.2.1	Table 45-3
45.2.1.7	Tables 45-9 & 45-10 Transmit & Receive fault locations
45.2.1.16	Table 45-19 (BASE-T1 PMA/PMD extended ability)
45.2.1.214	Table 45-178 (BASE-T1 PMA/PMD control register Type selection)
45.2.1.236a	Table 45-198a 100BASE-T1L PMA control register bit definitions
45.2.1.236b	Table 45-198b 100BASE-T1L PMA status register bit definitions
45.2.1.236c	Table 45-198c 100BASE-T1L test mode control register bit definitions
45.2.3	Table 45-233 PCS registers (consider relocating PCS registers at either 3.2280 or 3.2295)
45.2.3.75a	Table 45-297a 100BASE-T1L status register
45.2.3.75b	Table 45-297b 100BASE-T1L PCS status register (location fits within what is in dg draft 0p3)
45.2.3.75c	Table 45-297c 100BASE-T1L training register
45.2.7	Table 45-378 Auto-Negotiation MMD registers
45.2.7.27a	Table 45-402a 100BASE-T1L AN status register (7.528)
45.5.3.3	PMA register PICS – should wait until mostly complete
45.5.3.4	PCS register PICS – should wait until mostly complete

Ask for Technical contents (2)

Clause 78 EEE	
78.1.4	Table 78-1 clause
78.2	Table 78-2 summary of T_s, T_q, T_r
78.5	Table 78-4 Summary of Tw_sys_tx, phy, shrink, and sys timing parameters
Clause 98 Auto-Negotiation	
98.2.1	add 100BASE-T1L
98.5.1	add 100BASE-T1L to power on
98.5.2	add 100BASE-T1L to link_fail_inhibit_timer
98.6.2a	add 100BASE-T1L to option type (or add it to 10BASE-T1L option type existing.
98.6.8	change condition on SD11, SD12 to include 100BASE-T1L
98B.3	Technology Ability Field bit assignments
Clause 104 Power	
104.1.3	add 100BASE-T1L to PoDL system types
104.4, 104.5, 104.6, 104.7, 104.9	add 100BASE-T1L to PoDL system types
Clause 146 Physical Coding Sublayer (PCS), Physical Medium Attachment (PMA) sublayer and baseband medium, type 10BASE-T1L	
146	Removed

Ask for Technical contents (3)

Clause 190 100BASE-T1L	
190.1.3	Proposal for Figure 190–2—Functional block diagram is needed. We should discuss if it is needed. Since 10BASE-T1L didn't have this diagram.
199.5	PMA electrical specifications
199.5.1	EMC Tests
199.5.2	Test modes
199.5.3	Test fixture
199.5.4	Transmitter electrical specification
199.5.5	Receiver electrical specifications
199.5.6	PMA local loopback
199.6.2	LEADER-FOLLOWER configuration
199.8	MDI specification
199.9	Environmental specifications
199.1	Delay constraints

Expectations for this meeting

- Review of all the editor's notes.
- Get consensus on the technical contents to complete the Draft 2.0.
- Generate D2.0 and initiate Working Group ballot.

Thank you!