

Agenda and General Information

**Joint Session -
IEEE P802.3cw Task Force
IEEE P802.3df Task Force
IEEE P802.3dj Task Force**

**IEEE 802.3 May 2023 Interim
San Antonio, TX, USA**

**John D'Ambrosia,
Chair, IEEE P802.3cw Task Force
Chair, IEEE P802.3df Task Force
Chair, IEEE P802.3dj Task Force
Futurewei, U.S. Subsidiary of Huawei**



Non-payment of registration fees

Every attendee (Face-to-Face or Remote) at any IEEE 802.3 meeting during the May 2023 IEEE 802.3 Interim meeting series must register and pay a fee to participate

If the individual is in arrears:

[1] The individual is not permitted to register for any future IEEE 802 Plenary or IEEE 802 Subgroup Interim (if registration is required).

[2] As a result of [1], the individual is not permitted to attend any portion of a technical meeting that is part of an IEEE 802 plenary session. They are also not permitted to attend any portion of a technical meeting that is part of an IEEE 802 Subgroup Interim if that IEEE 802 Subgroup Interim required registration.

Joint Task Force Meetings

- **Per Motion #8 approved @ IEEE 802.3 Closing, Mar 2023** (see minutes @ https://www.ieee802.org/3/minutes/mar23/0323_minutes_draft.pdf)
 - **Move that the IEEE 802.3 Working Group grant approval for the P802.3cw Task Force to meet with P802.3df / P802.3dj Task Forces to take motions during any joint meeting between these task forces only**

- **Joint Task Force meetings will be announced**

Joint IEEE P802.3df / .3dj Task Force Session

■ Agenda

- Approve Agenda
- Approve Minutes (see next page)
- Decorum
- Attendance
- Task Forces Project Information
- Goals For Session
- Ground Rules
- Voting / Voting Rights
- Review IEEE SA Policies (<http://ieee802.org/3/policies.html> / included) *
 - IEEE SA Patent Policy
 - IEEE SA Participation Policy
 - IEEE SA Copyright Policy
- Liaisons Overview
- Baseline Status
- Ad hocs – Summary
 - 802.3df
 - 802.3dj
- Presentations (including discussion, strawpolls, and motions) – See Pages 23-26
- Future Meetings
 - Task Force
 - Ad hocs
- Closing Business

* Note – additional slides covering procedures and IEEE-SA Policies are in Backup

Minutes to be Approved

- IEEE P802.3cw Oct 2022 Session

- https://www.ieee802.org/3/cw/public/22_10/minutes_3cw_2210_unapproved.pdf

- Joint P802.3df / .3dj TF Mtg – Mar 2023 Session

- https://www.ieee802.org/3/dj/public/23_03/minutes_3dfdj_a_2303_unapproved.pdf

- Ad hoc Meetings

- P802.3df Logic Ad hoc –

- 26 Apr 2023- https://www.ieee802.org/3/df/public/adhoc/logic/23_0426/minutes.pdf

- P802.3dj Joint Logic / Optics Ad hoc Meetings

- 27 Apr 2023 - .

- https://www.ieee802.org/3/dj/public/adhoc/optics/0423_OPTX/minutes_3dj_optx_230427_unapproved.pdf

- P802.3dj Electrical Ad hoc

- 04 May 2023 -

- https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0504/minutes_3dj_elec_230504_unapproved.pdf

Meeting Decorum



- **An officer is permitted to make an audio or slideshow recording of this meeting exclusively for the purpose of generating minutes which shall not be copied or distributed. IEEE 802.3 meetings do not use this option. Recording of the proceedings by any other participant or observer, in part or in whole, via any means, is prohibited. (Feb 2023 IEEE-SA Standards Board Ops Manual 5.3.3.2)**
- **Press (i.e., anyone reporting publicly on this meeting) are to announce their presence (Feb 2023 IEEE-SA Standards Board Ops Manual 5.3.3.3)**
- **Cell phone ringers off**
- **Please observe proper decorum in meetings**
- **Please mute your self when not speaking**
- **Please do not turn on video**

Attendance

- **All attendees must register their attendance via IMAT**
 - **IMAT –**
 - **Tutorial - http://iee802.org/3/minutes/attendance_procedures.pdf**
 - **Access details**
 - **URL: <http://imat.ieee.org/>**
 - **802.3 Password – to be given out by chair at meeting**

- **Please be sure to sign-in with your name / affiliation information**
- **Zoom attendance will be used to identify remote attendance as well.**

IEEE P802.3cw Task Force Project information

■ Task Force Organization

- **John D'Ambrosia, Chair, IEEE P802.3cw Task Force**
- **Editorial Team**
 - Tom Issenhuth – Chief Editor, 400 GbE Optical PHY Clause
 - Leon Bruckman - 400 GbE PCS/PMA Clauses
- **Task force web and reflector information**
 - Reflector: <http://www.ieee802.org/3/ct/reflector.html>
 - Home page: <http://www.ieee802.org/3/cw/index.html>
- **Project Documentation –**
 - PAR : http://www.ieee802.org/3/cw/proj_doc/P802d3cw_PAR.pdf
 - CSD: <https://mentor.ieee.org/802-ec/dcn/19/ec-19-0219-00-ACSD-p802-3cw.pdf>
 - Objectives: http://www.ieee802.org/3/cw/proj_doc/3cw_Objectives_190911.pdf
 - Timeline: https://www.ieee802.org/3/cw/proj_doc/timeline_3cw_220321.pdf
- **Ad Hoc page: <http://www.ieee802.org/3/ct/public/adhoc/index.html>**

IEEE P802.3df Task Force Project information

■ Organization

- John D'Ambrosia, Chair, IEEE P802.3dj Task Force
- Mark Nowell, Vice-Chair, IEEE P802.3dj Task Force; Chair, Optics Track
- Kent Lusted, Secretary, IEEE P802.3dj Task Force; Chair, Electrical Track
- Matt Brown, IEEE P802.3dj Chief Editor
- Mark Gustlin, Chair, Architecture and Logic Track

▪ Task force web and reflector information:

- Home page: <https://www.ieee802.org/3/df/index.html>
- Reflector Info - <https://www.ieee802.org/3/df/reflector.html>
 - TF Reflector: stds-802-3-b400g@listserv.ieee.org
 - Logic Reflector: stds-802-3-b400g-logic@listserv.ieee.org
 - Optical Reflector: stds-802-3-b400g-optx@listserv.ieee.org
 - Electrical Reflector: stds-802-3-b400g-elec@listserv.ieee.org

▪ Project Documentation –

- PAR : https://www.ieee802.org/3/df/proj_doc/P802.3df_031222.pdf
- CSD: <https://mentor.ieee.org/802-ec/dcn/21/ec-21-0306-01-ACSD-p802-3df.pdf>
- Objectives: https://www.ieee802.org/3/df/proj_doc/objectives_P802d3df_221117.pdf
- Adopted Timeline: https://www.ieee802.org/3/df/proj_doc/timeline_3df_221004.pdf

▪ P802.3df TF meeting information may be found on:

- Public page: <https://www.ieee802.org/3/df/public/index.html>
- 802.3 Calendar: <https://www.ieee802.org/3/calendar.html>

IEEE P802.3dj Task Force Project information

■ Organization

- John D'Ambrosia, Chair, IEEE P802.3dj Task Force
- Mark Nowell, Vice-Chair, IEEE P802.3dj Task Force; Chair, Optics Track
- Kent Lusted, Secretary, Chair, Electrical Track
- Matt Brown, IEEE P802.3dj Chief Editor
- Mark Gustlin, Chair, Architecture and Logic Track

▪ Task force web and reflector information:

- Home page: <https://www.ieee802.org/3/df/index.html>
- Reflector Info - <https://www.ieee802.org/3/df/reflector.html>
 - TF Reflector: stds-802-3-b400g@listserv.ieee.org
 - Logic Reflector: stds-802-3-b400g-logic@listserv.ieee.org
 - Optical Reflector: stds-802-3-b400g-optx@listserv.ieee.org
 - Electrical Reflector: stds-802-3-b400g-elec@listserv.ieee.org

▪ Project Documentation –

- PAR : https://www.ieee802.org/3/dj/projdoc/P802d3dj_PAR.pdf
- CSD: <https://mentor.ieee.org/802-ec/dcn/22/ec-22-0256-00-ACSD-p802-3dj.pdf>
- Objectives: https://www.ieee802.org/3/dj/projdoc/objectives_P802d3dj_221117.pdf
- Adopted Timeline: https://www.ieee802.org/3/dj/projdoc/timeline_3dj_230116.pdf

▪ P802.3df TF meeting information may be found on:

- Public page: <https://www.ieee802.org/3/df/public/index.html>
- 802.3 Calendar: <https://www.ieee802.org/3/calendar.html>

Goals for the Session

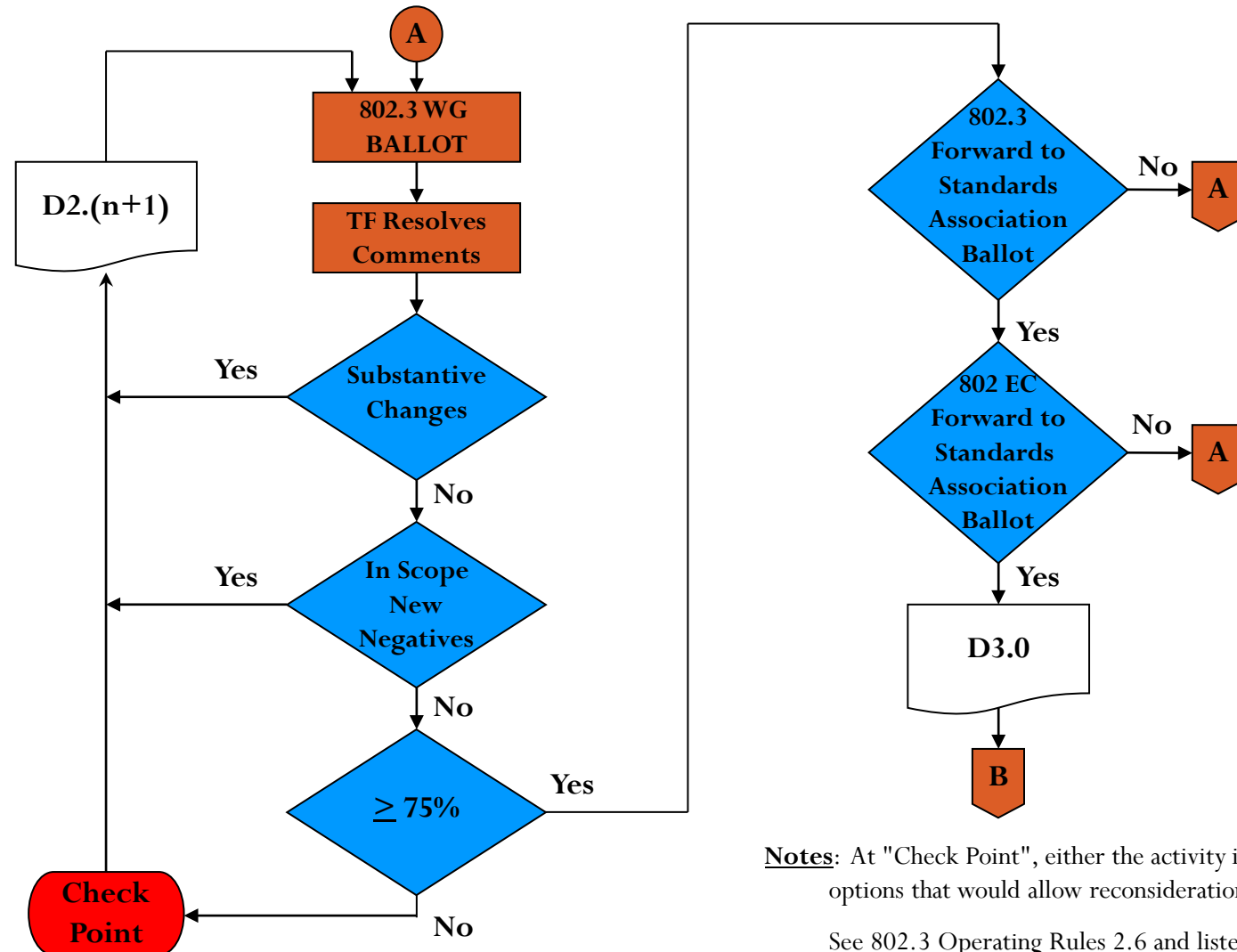
- **IEEE P802.3cw**
 - **Chief Editor's Report, D2.1 Bucket Comment Approval**
 - **Liaison to OIF**
- **IEEE P802.3df**
 - **Chief Editor's Report, D2.0 Bucket Comment Approval**
- **IEEE P802.3dj**
 - **Baseline Adoption**
 - **Consider liaison from ITU-T**

Ground Rules

- **Based upon IEEE 802.3 Rules**
 - **Foundation based upon Robert's Rules of Order**
 - **Anyone in the room may speak**
 - **Anyone in the room may vote on straw polls, but we have to follow 802.3 Voting rules**
- **RESPECT... give it, get it**
- **NO product pitches**
- **NO corporate pitches**
- **NO prices!!!**
 - **This includes costs, ASPs, etc. no matter what the currency**
- **NO restrictive notices**

Overview of IEEE 802.3 Standards Process (3/5) – Working Group Ballot Phase (802.3cw / 802.3df)

802.3cw
802.3 df
- We are here

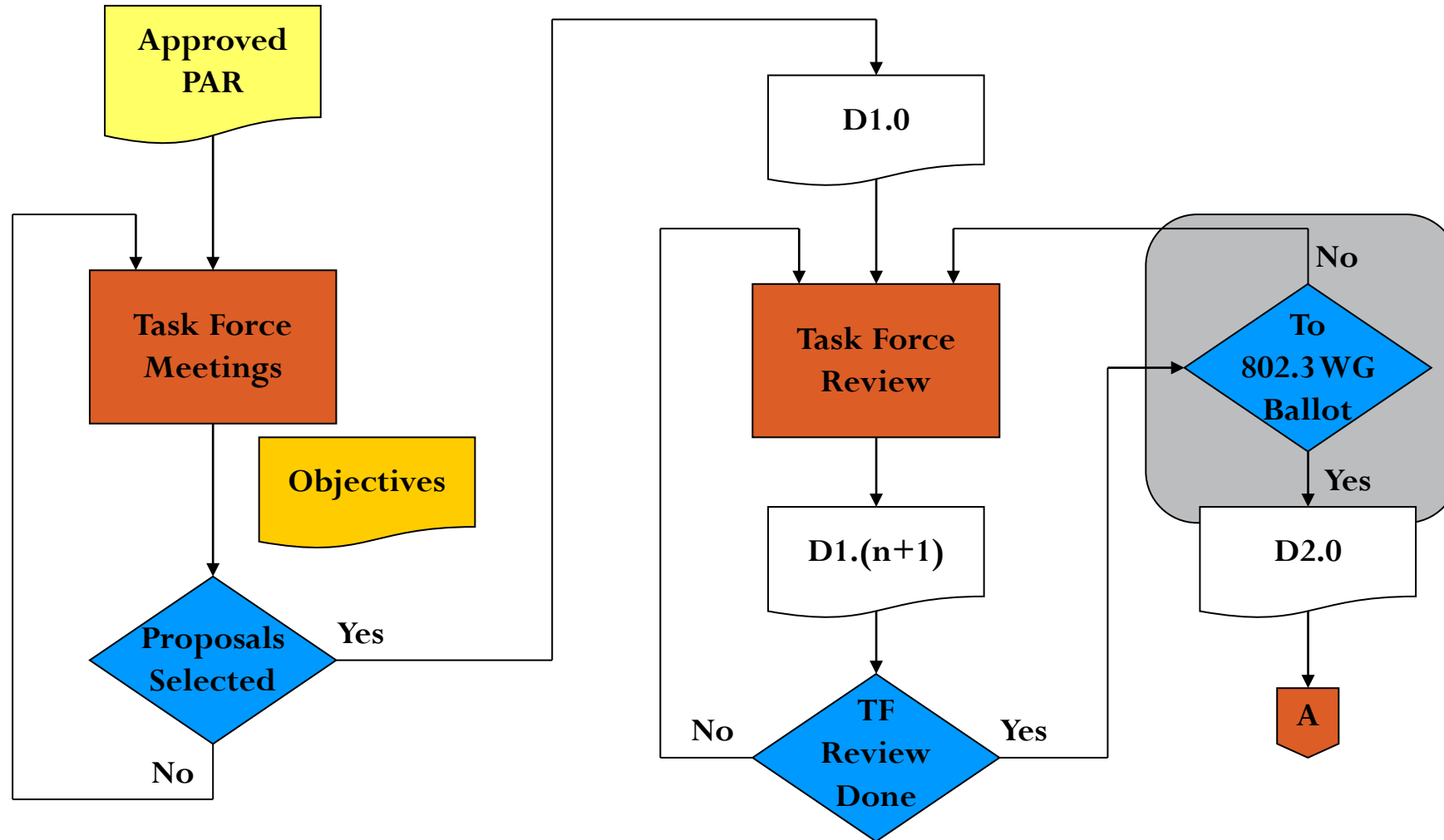


Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See 802.3 Operating Rules 2.6 and listed references for complete description

Overview of IEEE 802.3 Standards Process (2/5) – Task Force Comment Phase (802.3dj)

802.3 dj
- We
are here



Voting

- Mar 2023 IEEE 802.3 Task Force Meeting has been classified by 802.3 WG Chair as "mixed mode"
 - See https://www.ieee802.org/3/email_dialog/msg01401.html

- Reference: Excerpt from Section 3.4.2, IEEE 802.3 Operations Manual
 - http://www.ieee802.org/3/rules/P802_3_rules.pdf

There are two type of votes in the TF; votes on motions and votes in straw polls.

In face-to-face meetings where a TF is operating with membership rules for voting (see 3.3) only the TF members may make and vote on motions. In face-to-face meetings where membership rules are not in force all TF participants may make and vote on motions, subject only to the provision that they believe that they are qualified to vote on the matter before the TF. **Electronic meetings that will consider motions shall be approved by the WG Chair prior to the meeting announcement. In such meetings only IEEE 802.3 Working Group members may make and vote on motions. If a motion is not approved by unanimous consent it shall be taken as a roll call vote**

In all cases, regardless of any membership rules that may or may not be in force, all participants who feel qualified may participate in a straw poll

Note – Chair reserves the right to take informative straw polls by 802.3 WG voting membership

- IEEE 802.3 Requirements for Working Group voting membership – <https://www.ieee802.org/3/rules/member.html>

Review of IEEE SA Policies

■ Email reminders

- IEEE P802.3cw -
<https://www.ieee802.org/3/ct/email/msg01003.html>
- IEEE P802.3df / .3dj -
<https://www.ieee802.org/3/B400G/email/msg00757.html>

sent to Task Force Reflector reminding all to review and be familiar with following policies:

- **Patent Policy**
- **Copyright Policy**
- **Participation**

■ See Backup for slides

Liaisons Overview (As of 11 May 2023)

- **Incoming:**

- **[IEEE P802.3dj] From ITU-T SG15 to IEEE 802.3: G.652 fibre link property**

- https://www.ieee802.org/3/minutes/may23/incoming/SG15-LS051_Redacted.pdf

- **Attachment (Password Protected)d -**

- **Outgoing:**

- **[IEEE P802.3cw] To ITU-T – Request for 400ZR Test Data**

- **Proposed Liaison -**

- https://www.ieee802.org/3/cw/public/23_05/dambrosia_3cw_01_2305.pdf

IEEE P802.3dj Baseline Status - Logic

Ethernet Rate	AUI		CU Cable	SMF 500m/2km	SMF 10km 4 wavelength	SMF 10km 1 wavelength	SMF 40km
	PCS/FEC?	PMA?	PCS/FEC/PMA ?	PCS/FEC/PMA ?	PCS/FEC/PMA ?	PCS/FEC/PMA ?	PCS/FEC/PMA ?
200 Gb/s	FEC Only			With many open issues			
400 Gb/s	FEC Only			With many open issues			
800 Gb/s	FEC Only			With many open issues			
1.6 Tb/s							

Adopted baselines

Proposed Baselines

IEEE P802.3dj Baseline Status

Ethernet Rate	Assumed Signaling Rate	AUI	Cu Cable	SMF 500m	SMF 2km	SMF 10km	SMF 40km
200 Gb/s	200 Gb/s	200GAUI-1 C2C C2M	200GBASE-CR1	200GBASE-DR1	1 pair 200GBASE-FR1		
400 Gb/s	200 Gb/s	400GAUI-2 C2C C2M	400GBASE-CR2	400GBASE-DR2			
800 Gb/s	200 Gb/s	800GAUI-4 C2C C2M	800GBASE-CR4	800GBASE-DR4	1. 800GBASE-DR4-2 2. 800GBASE-FR4	Over four λ 's over single SMF in each direction	
	800 Gb/s					Over single SMF in each direction	
	TBD						Over single SMF in each direction IMDD Coherent
1.6 Tb/s	100 Gb/s	1.6TAUI-16 C2C C2M					
	200 Gb/s	1.6TAUI-8 C2C C2M	1.6TBASE-CR8	1.6TBASE-DR8	1.6TBASE-DR8-2		

Adopted baselines

Proposed Baselines (3dj)

IEEE P802.3df Ad Hocs

Announcement - <https://www.ieee802.org/3/B400G/email/msg00257.html>

1. Architecture and Logic Ad hoc – Mark Gustlin, Ad Hoc Chair

Charter: The Architecture and Logic Ad Hoc is chartered to address the following:

- 1) Develop terminology and definitions for terms related to architecture, including but not limited to FEC Architecture, FEC Scheme, End-to-end FEC, Segmented FEC, Concatenated FEC. Draft definitions to be presented to Task Force at May 2022 session.
- 2) Act as forum to discuss architectural requirements and consider proposals related to PCS, FEC, and PMA logic sublayers.

2. Electrical Ad Hoc – Kent Lusted, Ad Hoc Chair

Charter: The Electrical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to electrical interfaces and electrical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact electrical interfaces and electrical PMD sublayers
 - c. Electrical interfaces and electrical PMD nomenclature
- 2) Provide inputs based on electrical interfaces and electrical PMD sublayers into any relevant ad hocs

3. Optics Ad Hoc – Mark Nowell, Ad Hoc Chair

Charter: The Optical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to optical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact optical PMD sublayers
 - c. Optical PMD nomenclature
- 2) Provide inputs based on optical PMDs into any relevant ad hocs

IEEE P802.3dj Ad Hocs

The following ad hocs are appointed. They meet with respective IEEE P802.3df ad hocs when announced.

1. Architecture and Logic Ad hoc – Mark Gustlin, Ad Hoc Chair

Charter: The Architecture and Logic Ad Hoc is chartered to address the following:

- 1) Develop terminology and definitions for terms related to architecture, including but not limited to FEC Architecture, FEC Scheme, End-to-end FEC, Segmented FEC, Concatenated FEC. .
- 2) Act as forum to discuss architectural requirements and consider proposals related to PCS, FEC, and PMA logic sublayers.

2. Electrical Ad Hoc – Kent Lusted, Ad Hoc Chair

Charter: The Electrical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to electrical interfaces and electrical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact electrical interfaces and electrical PMD sublayers
 - c. Electrical interfaces and electrical PMD nomenclature
- 2) Provide inputs based on electrical interfaces and electrical PMD sublayers into any relevant ad hocs

3. Optics Ad Hoc – Mark Nowell, Ad Hoc Chair

Charter: The Optical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to optical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact optical PMD sublayers
 - c. Optical PMD nomenclature
- 2) Provide inputs based on optical PMDs into any relevant ad hocs

Ad hoc Summary

- **802.3df Ad Hoc Meetings**
 - **Architecture Ad hoc (26 Feb)**
 - 2 technical presentations considered – Skew
 - https://www.ieee802.org/3/df/public/23_05/gustlin_3df_01_2305.pdf
- **802.3dj Ad Hoc Meetings**
 - **Joint Logic / Optics Ad hoc (13, 27 April)**
 - 15 technical presentations considered
 - https://www.ieee802.org/3/dj/public/23_05/nowell_3dj_01_2305.pdf
 - **Electrical Ad hoc (06 Apr, 20 Apr, 04 May)**
 - 18 technical presentations considered
 - https://www.ieee802.org/3/dj/public/23_05/lusted_3dj_01_2305.pdf

May 2023 Session (1 of 4)

All times and order listed are subject to change.
See meeting webpage for latest presentation version

Mon	15-May-23			
Time	Presenter	Affiliation	Title	File Name
8:00 AM	John D'Ambrosia	Futurewei, U.S.	Agenda and General Information	agenda_3dfdj_2303
8:40 AM	Kent Lusted	Intel	IEEE P802.3dj Electrical Ad Hoc Update	lusted_3dj_01_2305
8:55 AM	John D'Ambrosia	Futurewei	Motions and State of the Task Force	dambrosia_3dj_01_2305
9:15 AM	Mike Li	Intel	Reference Package Model Update for 200G/Lane Electrical I/Os	li_3dj_02_2305
9:35 AM	Nathan Tracy	TE Connectivity	Updated 200G Chip to Module Channels	shanbhag_3dj_03_2305
10:00 AM	Break			
10:20 AM	Mike Li	Intel	224 Gbps-PAM4 Chip-to-Module Link Simulation and Analysis with a "Universal Port" Channel: Design	li_3dj_04_2305
10:45 AM	Mike Li	Intel	224 Gbps-PAM4 Chip-to-Module Link Simulation and Analysis with a "Universal Port" Channel: Design	li_3dj_06_2305
11:05 AM	Kent Lusted	IntelMediaTek	200 Gbps/lane AUI C2M Channel Selection Criteria	lusted_3dj_02_2305
11:35 AM	Tobey P.-R. Li	MediaTek	200Gbps/Lane AUI C2C Considerations	lit_3dj_01_2305
12:00 PM	Lunch			
1:15 PM	Ali Ghiasi	Ghiasi Quantum	Updated CRU Bandwidth Recommendation for 200G Interfaces	ghiasi_3dj_01_2305
1:35 PM	Adam Healey	Broadcom	Considerations for use of the MLSE model in COM	healey_3dj_01_2305
1:55 PM	Nathan Tracy	TE Connectivity	Updated 200G Passive Copper Cable Assembly CR Channels	shanbhag_3dj_01_2305
2:20 PM	Sam Kocsis	Amphenol	Copper Cable Channel Characteristics	kocsis_3dj_01_2305
2:50 PM	Break			
3:10 PM	Mike Li	IntelTE	Demonstration of a 224Gbps-PAM4 CR SERDES in Supporting a 1 Meter Passive DAC Long Reach Chan	li_3dj_01_2305
3:35 PM	Mike Li	Intel	224 Gbps-PAM4 Long Reach Link Simulation and Analysis with a 1 Meter DAC Channel: Design A	li_3dj_08_2305
4:05 PM	Mike Li	Intel	224 Gbps-PAM4 Long Reach Link Simulation and Analysis with a 1 Meter DAC Channel: Design B	li_3dj_10_2305
4:30 PM	Discussion and Straw Polls			
5:30 PM	Recess for Day			

May 2023 Session (2 of 4)

All times and order listed are subject to change.
See meeting webpage for latest presentation version

Time	Presenter	Affiliation	Title	File Name
Tues 16-May-23				
8:00 AM	John D'Ambrosia	Futurewei, U.S.	Chair's Opening Comments	
8:05 AM	Nathan Tracy	TE Connectivity	Updated KR Channels In Support of a 200G Backplane Objective	shanbhag_3dj_02_2305
8:30 AM	Jim Weaver	Arista Networks	212Gb/s Per Lane PAM4 KR Cabled Backplane Channels with Asymmetric NPC Cable Lengths	weaver_3dj_01_2305
9:00 AM	Rich Mellitz	Samtec	200 Gb/s per lane KR Backplane Objective Proposal	mellitz_3dj_01_2305
9:30 AM	Discussion and Straw Polls			
10:00 AM	Break			
10:20 AM	Dave Ofelt	Juniper	Economics of Latency	ofelt_3dj_01_2305
10:40 AM	Ali Ghiasi	Ghiasi Quantum	Partitioning AUI C2M and C2C BERs	ghiasi_3dj_02_2305
11:00 AM	Adee Ran	Cisco	Proposal for BER budget allocation for AUIs in Type 1 and Type 2 PHYs	ran_3dj_01_2305
11:30 AM	Kent Lusted	Intel	BER Targets for Type 1 and Type 2 PHYs	lusted_3dj_03_2305
12:00 PM	Lunch			
1:15 PM	Xiang He	Huawei	FEC_I Sublayer Architecture Proposal for Type 2 PHYs	he_3dj_01_2305
1:45 PM	Xiang He	Huawei	4x RS Codeword Interleaving Proposal for 200 GbE and 400 GbE	he_3dj_02_2305
2:15 PM	Zvi Rechtman	nVidia	Simplified Pad Insertion for Inner FEC	rechtman_3dj_01_2305
2:40 PM	Lenin Patra	Marvell	Various Convolutional Interleaver options for Inner FEC code (128, 120)	patra_3dj_01_2305
3:00 PM	Break			
3:20 PM	Lenin Patra	Marvell	25G PCSL & 100G PCSL based FEC sublayer Architecture for Inner Code	patra_3dj_02_2305
3:45 PM	Brian Welch	Cisco	FEC Inner Code Bypass Options for 200G/L IMDD Optics	welch_3dj_03_2305
4:10 PM	Brian Welch	Cisco	Baseline proposals for 200G/L PMD specifications for single wavelength 500m and 2km standards	welch_3dj_01_2305
4:40 PM	Discussion and Straw Polls			
6:00 PM	Recess for Day			

May 2023 Session (3 of 4)

All times and order listed are subject to change.
See meeting webpage for latest presentation version

Wed	17-May-23			
	Presenter	Affiliation	Title	File Name
8:00 AM	John D'Ambrosia	Futurewei, U.S.	Chair's Opening Comments	
8:05 AM	Tom Issenhuth	Huawei	IEEE P802.3cw Chief Editor's Report	issenhuth_3cw_01_2305
8:20 AM	Matt Brown	Huawei	IEEE P802.3df Chief Editor's Report	brown_3df_01_2305
8:35 AM	Motions (802.3cw / 802.3df)			
8:55 AM	Roberto Rodes	Coherent	Considerations on an 800G-FR4 baseline	rodes_3di_02_2305
9:20 AM	Guangcan Mi	Huawei Technologies	Updates on Baseline Proposal for 800G-FR4 Objective	mi_3dj_01_2305
10:00 AM	Break			
10:20 AM	Brian Welch	Cisco	Baseline proposal for 200G/L PMD specification for 4 wavelengths over a single SMF in each direction	welch_3dj_02_2305
10:45 AM	Discussion and Straw Polls			
11:15 AM	Chris Cole	Coherent	802.3 SMF Channel Proposal Using Existing ITU-T Codes	cole_3dj_01_2305
11:45 AM	Roberto Rodes	Coherent	Towards an 800G-LR4 IMDD Specification Consensus - May 2023 update	rodes_3dj_01_2305
12:00 PM	Lunch			
1:15 PM	Tao Gui	Huawei	Feasibility Study on Baud-Rate Sampling and Equalization (BRSE) for 800G-LR1	gui_3dj_01_2305
1:45 PM	Tao Gui	Huawei	Feasibility Study on O-Band Coherent 800G-LR1 & ER1	gui_3dj_02_2305
2:15 PM	Or Vidal	Alphawave Semi	Benefits of a Coherent Solution for 800G-LR1	carusone_3dj_01_2305
2:35 PM	Haojie Wang,	China Mobile	Considerations on GMP bypass for 800G-LR1/ER1	wang_3dj_01_2305
3:00 PM	Break			
3:20 PM	Tom Williams	Cisco	Update to oFEC-based single lambda baseline for 10km and 40km objectives	williams_3dj_01_2305
3:40 PM	Gary Nicholl	Cisco	Logic baseline proposal (PCS and PMA) for a single Lambda coherent solution to address the 800GbE	nichol_3dj_01_2305
4:10 PM	Eric Maniloff	Ciena	Logic baseline proposal for 800GBASE-LR1	maniloff_3dj_01_2305
4:40 PM	Discussion and Straw Polls			
5:30 PM	Recess for Day			

May 2023 Session (4 of 4)

All times and order listed are subject to change.
See meeting webpage for latest presentation version

Thurs	18-May-23	IEEE P802.3dj		
	Presenter	Affiliation	Title	File Name
8:00 AM	John D'Ambrosia	Futurewei, U.S.	Chair's Opening Comments	
8:05 AM	Discussion, Strawpolls, & Motions			
10:00 AM	Break			
10:20 AM	Discussion, Strawpolls, & Motions			
12:00 PM	Lunch			
1:15 PM	Discussion, Strawpolls, & Motions			
3:00 PM	Break			
3:20 PM	Discussion, Strawpolls, & Motions			
5:30 PM	Closing Business			
6:00 PM	Adjourn			

Note – Start time of “Discussions, Strawpolls & Motions” may vary depending upon completion of presentations.

Future Meetings

IEEE P802.3cw: <https://www.ieee802.org/3/cw/public/index.html>

IEEE P802.3df: <https://www.ieee802.org/3/df/public/index.html>

IEEE P802.3dj: <https://www.ieee802.org/3/dj/public/index.html>

- IEEE P802.3df May 23 -25 Electronic Interim
 - May 23 – May 25
- IEEE P802.3cw June 2023 Electronic Series
 - 31 May, 01 June, 05-08 June
- Joint IEEE P802.3cw / .3df / .3dj TF July 2023 Plenary
 - Week of July 10 – 13, 2023
 - Estrel Berlin
 - Berlin, Germany
- Joint IEEE P802.3cw / .3df / .3dj TF Sept 2023 Interim
 - Week of Sept 11 – 14, 2023
 - Campinas, Brazil
- Joint IEEE P802.3cw / .3df / .3dj TF Nov 2023 Plenary
 - Week of Nov 13 – 16, 2023
 - Hawaiian Village
 - Oahu, Hawaii, USA
- Any additional meetings will be announced, as appropriate.

Future Ad hoc Meetings

See:

- **Joint Logic / Optics Ad hocs**
 - 15 June, 10:00 to 1:00pm EDT
 - 29 June, 10:00 to 1:00pm EDT
- **Electrical Ad hoc**
 - 01 June, 9:30 to 12:30pm EDT
 - 08 June, 9:30 to 12:30pm EDT
 - 22 June, 9:30 to 12:30pm EDT

THANK YOU!



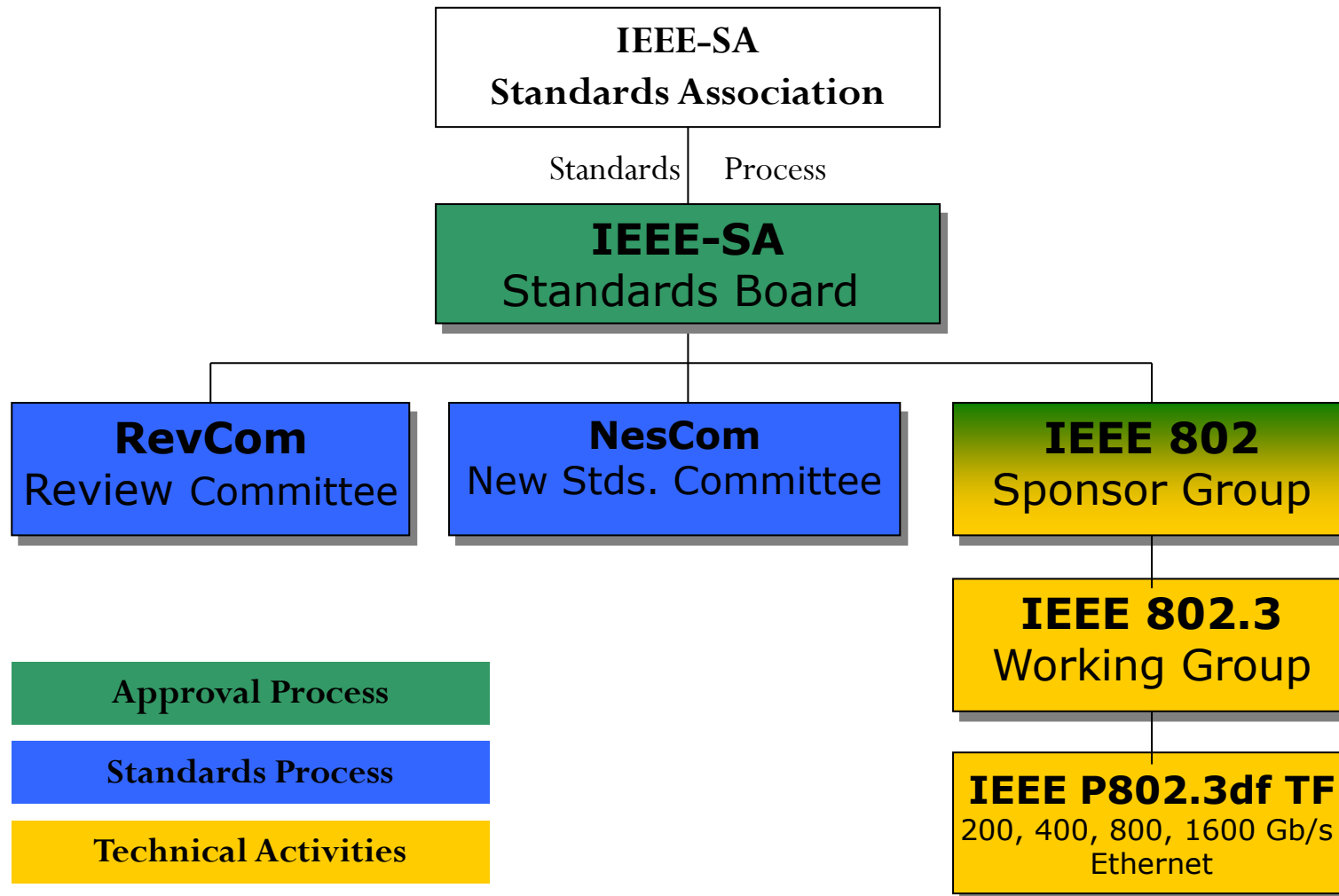
BACKUP



The Role of the Chair / Vice Chair

- **Per the IEEE 802.3 Ethernet WG Operations Manual (http://www.ieee802.org/3/rules/P802_3_rules.pdf)**
 - **Per 3.4 - Operation of the Task Force**
 - **The operation of the TF has to be balanced between democratic procedures that reflect the desires of the TF members and the TF Chair's responsibility to produce a draft standard, recommended practice, or guideline in a reasonable amount of time for review and approval by the WG. Robert's Rules of Order shall be used in combination with these operating rules to achieve this balance.**
 - **The full responsibilities of the chair are specified in 3.4.3 Task Force Chair's Responsibilities.**
 - **The Role of the Vice Chair is specified in 3.2 –**
 - **There may be a TF Vice-Chair. A TF Vice-Chair carries out the TF Chair's duties if the TF Chair is temporarily unable to do so or chooses to delegate specific duties.**
- **Reminders –**
 - **We are a contribution driven organization. It should not be assumed by anyone that work will just get done without contributions to drive it. Areas not moving forward will be brought to the attention of the Task Force for consideration on how to address.**
 - **Individual standards activities within the WG are, at the discretion of the WG, carried out by Task Forces (TF) operating under, and reporting to, the WG.**

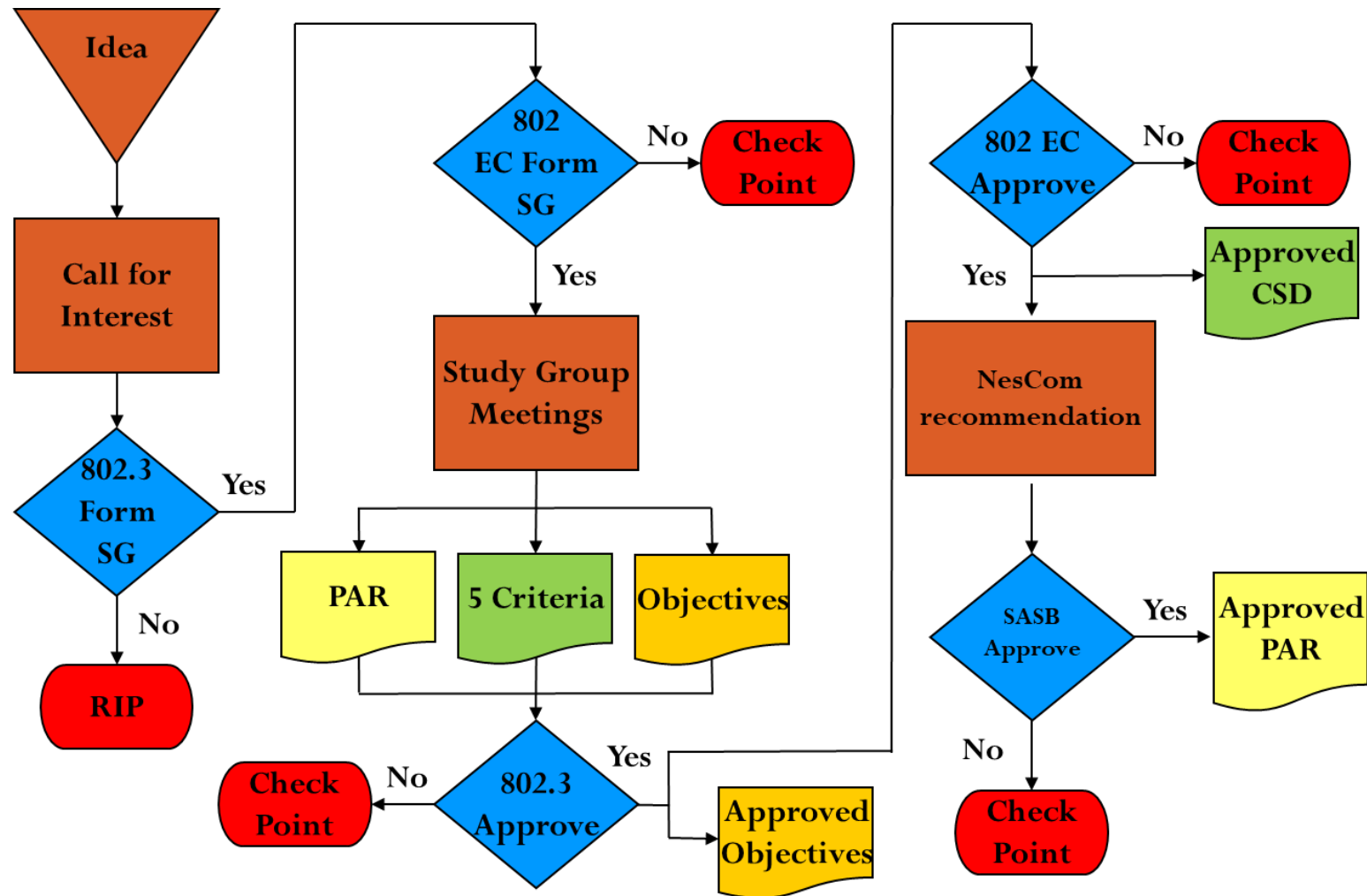
IEEE Structure



Important Bylaws, Rules, and References

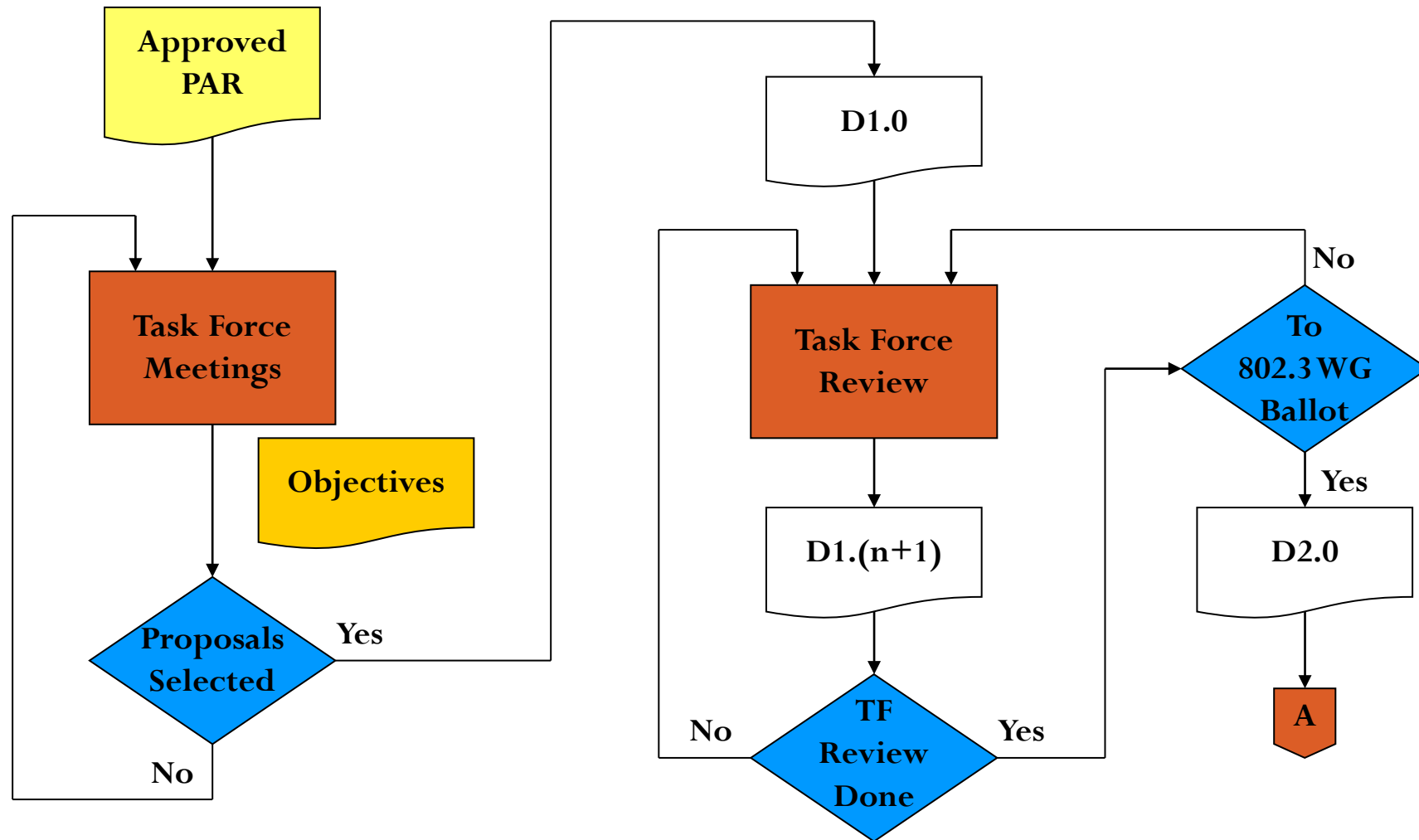
- **IEEE-SA Operations Manual**
http://standards.ieee.org/develop/policies/sa_opman/
- **IEEE-SA Standards Board Bylaws**
<http://standards.ieee.org/develop/policies/bylaws/>
- **IEEE-SA Standards Board Operations Manual**
<http://standards.ieee.org/develop/policies/opman/>
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Policies and Procedures**
<https://ieee.app.box.com/v/PandP-LMSC>
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Operations Manual**
<http://www.ieee802.org/devdocs.shtml>
- **IEEE 802 LAN/MAN Standards Committee (LMSC) Working Group (WG) Policies and Procedures**
<http://www.ieee802.org/devdocs.shtml>
- **IEEE 802.3 Working Group Operating Rules**
http://ieee802.org/3/rules/P802_3_rules.pdf
- **“Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association's Antitrust and Competition Policy”**
<http://standards.ieee.org/develop/policies/antitrust.pdf>

Overview of IEEE 802.3 Standards Process (1/5)- Study Group Phase

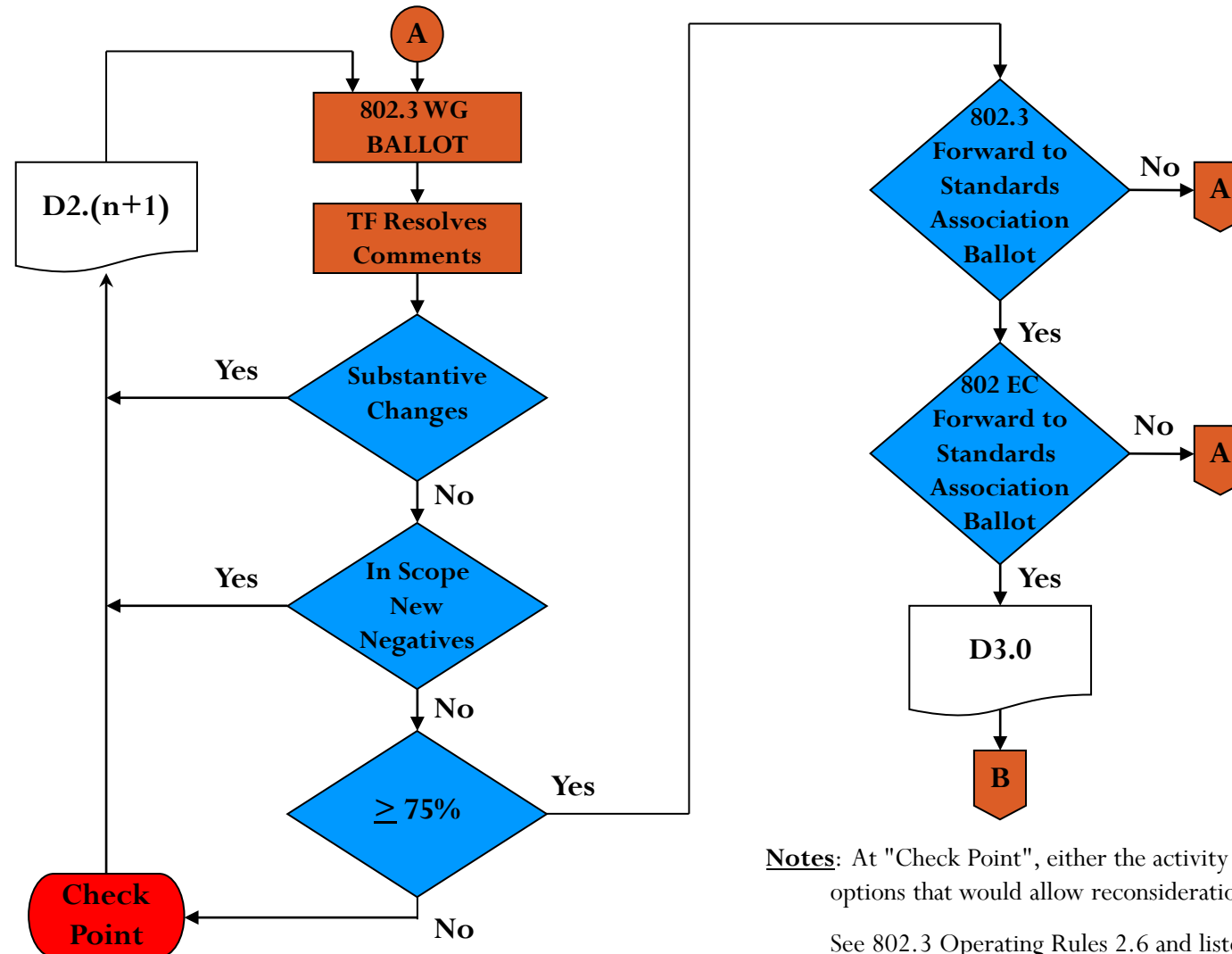


Note: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

Overview of IEEE 802.3 Standards Process (2/5) – Task Force Comment Phase



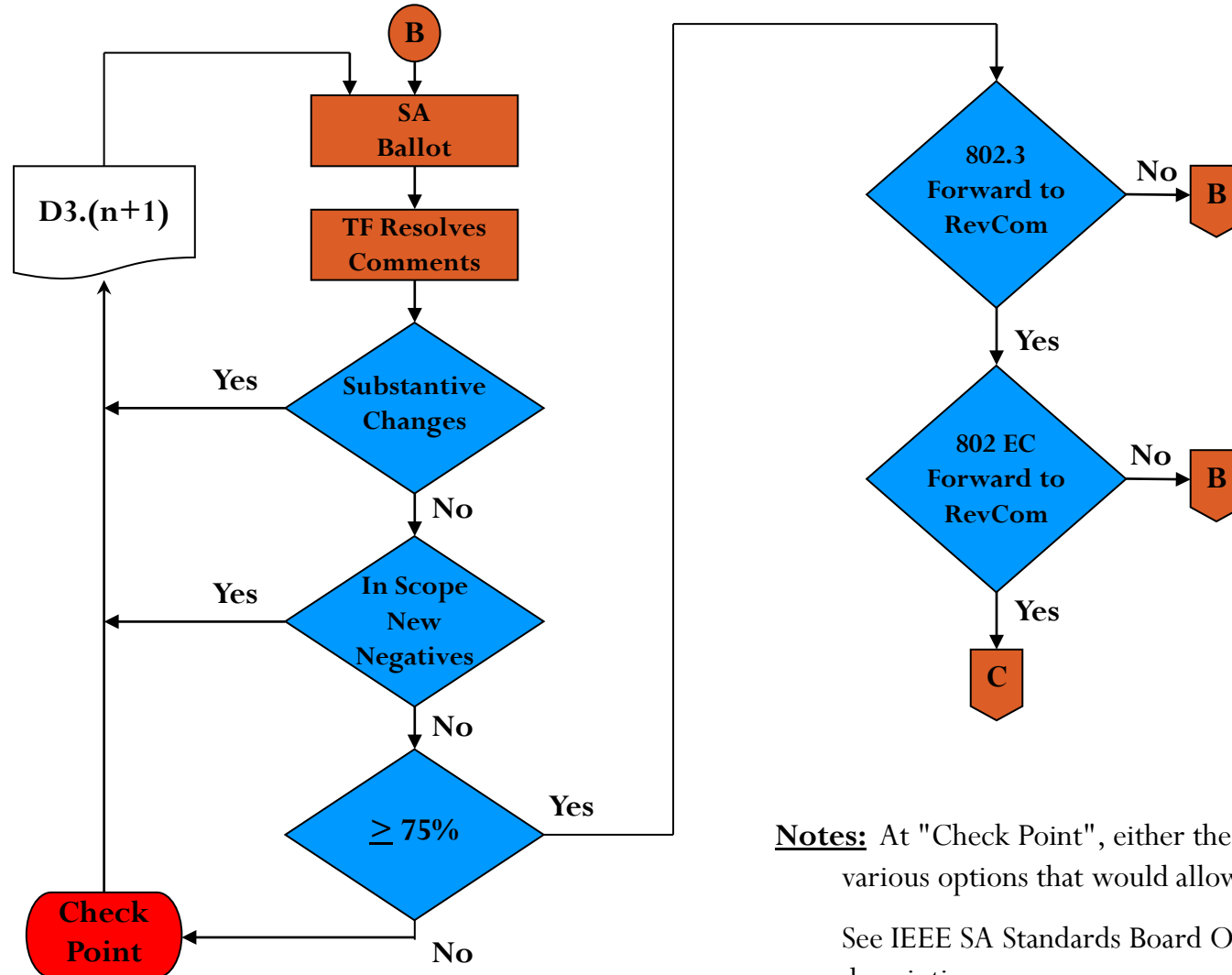
Overview of IEEE 802.3 Standards Process (3/5) – Working Group Ballot Phase



Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See 802.3 Operating Rules 2.6 and listed references for complete description

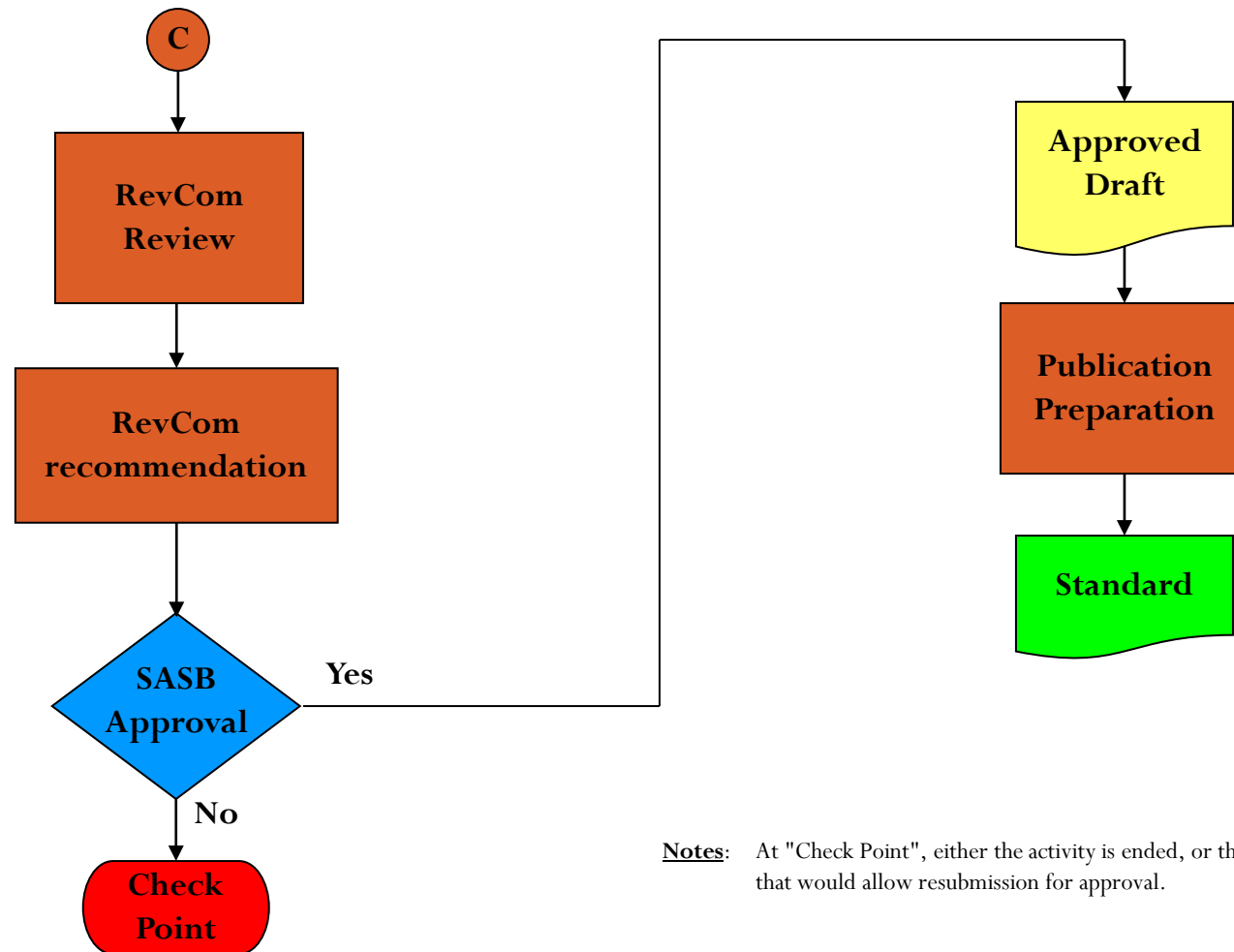
Overview of IEEE 802.3 Standards Process (4/5)- Standards Association (SA) Ballot Phase



Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See IEEE SA Standards Board Operation Manual 5.4 for complete description.

Overview of IEEE 802.3 Standards Process (5/5) – Final Approvals / Standard Release



Notes: At "Check Point", either the activity is ended, or there may be various options that would allow resubmission for approval.

IEEE SA POLICIES

IEEE SA PATENT POLICY

Instructions for the WG Chair

- The IEEE SA strongly recommends that at each WG meeting the chair or a designee:
 - Show slides 1 through 4 of this presentation
 - Advise the WG attendees that:
 - IEEE's patent policy is described in Clause 6 of the *IEEE SA Standards Board Bylaws*;
 - Early identification of patent claims which may be essential for the use of standards under development is strongly encouraged;
 - There may be Essential Patent Claims of which IEEE is not aware. Additionally, neither IEEE, the WG, nor the WG Chair can ensure the accuracy or completeness of any assurance or whether any such assurance is, in fact, of a Patent Claim that is essential for the use of the standard under development.
 - Instruct the WG Secretary to record in the minutes of the relevant WG meeting:
 - That the foregoing information was provided and that slides 1 through 4 (and this slide 0, if applicable) were shown;
 - That the chair or designee provided an opportunity for participants to identify patent claim(s)/patent application claim(s) and/or the holder of patent claim(s)/patent application claim(s) of which the participant is personally aware and that may be essential for the use of that standard
 - Any responses that were given, specifically the patent claim(s)/patent application claim(s) and/or the holder of the patent claim(s)/patent application claim(s) that were identified (if any) and by whom.
 - The WG Chair shall ensure that a request is made to any identified holders of potential essential patent claim(s) to complete and submit a Letter of Assurance.
 - It is recommended that the WG Chair review the guidance in *IEEE SA Standards Board Operations Manual* 6.3.5 and in FAQs 14 and 15 on inclusion of potential Essential Patent Claims by incorporation or by reference.
- Note: **WG** includes Working Groups, Task Groups, and other standards-developing committees with a PAR approved by the IEEE SA Standards Board.

Participants have a duty to inform the ieee

- Participants shall inform the IEEE (or cause the IEEE to be informed) of the identity of each holder of any potential Essential Patent Claims of which they are personally aware if the claims are owned or controlled by the participant or the entity the participant is from, employed by, or otherwise represents
- Participants should inform the IEEE (or cause the IEEE to be informed) of the identity of any other holders of potential Essential Patent Claims

–Early identification of holders of potential Essential Patent Claims is encouraged

Ways to inform ieee

- Cause an LOA to be submitted to the IEEE SA (patcom@ieee.org); or
- Provide the chair of this group with the identity of the holder(s) of any and all such claims as soon as possible; or
- **Speak up now and respond to this Call for Potentially Essential Patents**
 - ▣ If anyone in this meeting is personally aware of the holder of any patent claims that are potentially essential to implementation of the proposed standard(s) under consideration by this group and that are not already the subject of an Accepted Letter of Assurance, please respond at this time by providing relevant information to the WG Chair

Other Guidelines for IEEE Working Group Meetings

- All IEEE SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.
 - Don't discuss the interpretation, validity, or essentiality of patents/patent claims.
 - Don't discuss specific license rates, terms, or conditions.
 - Relative costs of different technical approaches that include relative costs of patent licensing terms may be discussed in standards development meetings.
 - Technical considerations remain the primary focus.
 - Don't discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.
 - Don't discuss the status or substance of ongoing or threatened litigation.
 - Don't be silent if inappropriate topics are discussed. Formally object to the discussion immediately.
- For more details, see *IEEE SA Standards Board Operations Manual*, clause 5.3.10 and *Antitrust and Competition Policy: What You Need to Know* at <http://standards.ieee.org/develop/policies/antitrust.pdf>

Patent-related information

- **The patent policy and the procedures used to execute that policy are documented in the:**
 - *IEEE SA Standards Board Bylaws*
(<http://standards.ieee.org/develop/policies/bylaws/sect6-7.html#6>)
 - *IEEE SA Standards Board Operations Manual*
(<http://standards.ieee.org/develop/policies/opman/sect6.html#6.3>)

–Material about the patent policy is available at

<http://standards.ieee.org/about/sasb/patcom/materials.html>

- **If you have questions, contact the IEEE SA Standards Board Patent Committee Administrator at patcom@ieee.org**

IEEE SA COPYRIGHT POLICY

Instructions for Chairs of standards development activities

- **At the beginning of each standards development meeting the chair or a designee is to:**
 - Show the following slides (or provide them beforehand)
 - Advise the standards development group participants that:
 - IEEE SA's copyright policy is described in Clause 7 of the IEEE SA Standards Board Bylaws and Clause 6.1 of the IEEE SA Standards Board Operations Manual;
 - Any material submitted during standards development, whether verbal, recorded, or in written form, is a Contribution and shall comply with the IEEE SA Copyright Policy;
 - Instruct the Secretary to record in the minutes of the relevant meeting:
 - That the foregoing information was provided and that the copyright slides were shown (or provided beforehand).

IEEE SA Copyright Policy

- **By participating in this activity, you agree to comply with the IEEE Code of Ethics, all applicable laws, and all IEEE policies and procedures including, but not limited to, the IEEE SA Copyright Policy.**
 - Previously Published material (copyright assertion indicated) shall not be presented/submitted to the Working Group nor incorporated into a Working Group draft unless permission is granted.
 - Prior to presentation or submission, you shall notify the Working Group Chair of previously Published material and should assist the Chair in obtaining copyright permission acceptable to IEEE SA.
 - For material that is not previously Published, IEEE is automatically granted a license to use any material that is presented or submitted.

IEEE SA Copyright Policy

- The IEEE SA Copyright Policy is described in the IEEE SA Standards Board Bylaws and IEEE SA Standards Board Operations Manual
- IEEE SA Copyright Policy, see
 - Clause 7 of the IEEE SA Standards Board Bylaws
<https://standards.ieee.org/about/policies/bylaws/sect6-7.html#7>
 - Clause 6.1 of the IEEE SA Standards Board Operations Manual
<https://standards.ieee.org/about/policies/opman/sect6.html>
- IEEE SA Copyright Permission
- <https://standards.ieee.org/content/dam/ieee-standards/standards/web/documents/other/permissionltrs.zip>
- IEEE SA Copyright FAQs
- <http://standards.ieee.org/faqs/copyrights>
- IEEE SA Best Practices for IEEE Standards Development
- http://standards.ieee.org/content/dam/ieee-standards/standards/web/documents/other/best_practices_for_ieee_standards_development_051215.pdf
- Distribution of Draft Standards (see 6.1.3 of the SASB Operations Manual)
- <https://standards.ieee.org/about/policies/opman/sect6.html>

IEEE SA PARTICIPATION SLIDES

Participant behavior in IEEE-SA activities is guided by the IEEE Codes of Ethics & Conduct

- All participants in IEEE-SA activities are expected to adhere to the core principles underlying the:
 - [IEEE Code of Ethics](#)
 - [IEEE Code of Conduct](#)
- The core principles of the IEEE Codes of Ethics & Conduct are to:
 - *Uphold the highest standards of integrity, responsible behavior, and ethical and professional conduct*
 - *Treat people fairly and with respect, to not engage in harassment, discrimination, or retaliation, and to protect people's privacy.*
 - *Avoid injuring others, their property, reputation, or employment by false or malicious action*
- The most recent versions of these Codes are available at <http://www.ieee.org/about/corporate/governance>

Participants in the IEEE-SA “*individual process*” shall act independently of others, including employers

- The [IEEE-SA Standards Board Bylaws](#) require that “*participants in the IEEE standards development individual process shall act based on their qualifications and experience*”
- This means participants:
 - **Shall act & vote** based on their personal & independent opinions derived from their expertise, knowledge, and qualifications
 - **Shall not act or vote** based on any obligation to or any direction from any other person or organization, including an employer or client, regardless of any external commitments, agreements, contracts, or orders
 - **Shall not direct** the actions or votes of other participants or retaliate against other participants for fulfilling their responsibility to act & vote based on their personal & independently developed opinions
- By participating in standards activities using the “*individual process*”, you are deemed to accept these requirements; if you are unable to satisfy these requirements then you shall immediately cease any participation

IEEE-SA standards activities shall allow the fair & equitable consideration of all viewpoints

- The [IEEE-SA Standards Board Bylaws](#) (clause 5.2.1.3) specifies that *“the standards development process shall not be dominated by any single interest category, individual, or organization”*
 - This means no participant may exercise *“authority, leadership, or influence by reason of superior leverage, strength, or representation to the exclusion of fair and equitable consideration of other viewpoints”* or *“to hinder the progress of the standards development activity”*
- This rule applies equally to those participating in a standards development project and to that project’s leadership group
- Any person who reasonably suspects that dominance is occurring in a standards development project is encouraged to bring the issue to the attention of the Standards Committee or the project’s IEEE-SA Program Manager