Motions and Straw Polls

IEEE P802.3cw, IEEE P802.3df and P802.3dj Task Force Joint Meeting

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Foreword

- Straw polls related to resolving comments may be found in the associated comment response files.
- This contribution summarizes motions and straw polls not related to comments.
- This contribution is not the official minutes of the meeting.

If there is any discrepancy between this contribution and the meeting minutes, then the minutes take precedence.

15 May 2023

Straw Poll 1

I would support adopting differential PAM4 signaling as the basis for all of the 200 Gbps/lane passive copper cable and backplane PMDs

Y: 80, N: 0, NMI: 5

Note: Backplane objective is subject to Task Force adoption and WG approval

Straw Poll 2

I would support adopting RS(544,514,10) as the only FEC encoding for all of the 200 Gbps/lane passive copper cable and backplane PMDs

Y: 56 , N: 3 , NMI: 24

Note: Backplane objective is subject to Task Force adoption and WG approval

Straw Poll #3

I support a CRU bandwidth and jitter tolerance corner frequency of 4 MHz for all 802.3dj PMD/AUIs operating at KP4 FEC and 4.27 MHz for all 802.3dj with SFEC per SFEC definition in https://www.ieee802.org/3/dj/public/23_03/patra_3dj_01b_2303.pdf (The calculation for CRU BW is based on the following fBaud/26562.5 equation)

Y: 38 , N: 5 , NMI: 13 , A: 30

Straw poll #4

I would support using lit_3dj_01a_2305 slide 7 as the direction toward a baseline for C2C.

Y: 52 , N: 1 , NMI: 9 , A: 20

Straw Poll #5

I support a CRU bandwidth and jitter tolerance corner frequency of 4 MHz for all 802.3dj PMD/AUIs operating at RS544 FEC (The calculation for CRU BW is based on the following fBaud/26562.5 equation)

Y: 57 , N: 0 , NMI: 6 , A: 17

Straw Poll #6

I support a CRU bandwidth and jitter tolerance corner frequency of X MHz for all 802.3dj with SFEC (inner code FEC) per SFEC definition in https://www.ieee802.org/3/dj/public/23_03/patra_3dj_01b_2303.pdf

A. X=4 MHz (The calculation for CRU BW is based on the following fBaud/28359.38 equation)

B. X=4.27 MHz (The calculation for CRU BW is based on the following fBaud/26562.5 equation)

C. Need more information

D. Abstain.

Results: A: 14, B: 19, C: 13, D: 30

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Straw Poll #7

I would support adopting the backplane objectives for 200GBASE-KR1, 400BASE-KR2, 800GBASE-KR4, and 1.6TBASE-KR8 in mellitz_3dj_01a_2305, slide 13

Y: 87, N: 0, A: 19

choose one

All in the room

Straw Poll #8

I would support adding a 4-codeword interleaving function in 200 Gb/s per lane PMAs used with 200GBASE-R and 400GBASE-R PCS, as proposed in he_3dj_02_2305.

Y: 51 , N: 5 , NMI: 23 , A: 23

Straw Poll #9

If adopting a 4-codeword interleaving function for 200 Gb/s per lane PMA used with 200GBASE-R and 400GBASE-R PCS, I prefer the following method:

A: option 1 (delay half of the PCS lanes) on slides 6 and 7 in he_3dj_02_2305

B: option 2 (convolutional) on slide 8 in he_3dj_02_2305

C: either option 1 or option 2

D: Need more information

E: Abstain

(pick one)

Results: A: 9 , B: 2 , C: 18 , D: 39 , E: 31

Straw Poll #10

I am supportive of the direction of patra 3dj_ 02a_2305 as the baseline Convolutional Interleaver proposal for Inner Code FEC (128,120) for 200GbE/400GbE/800GbE/1.6TbE PCS.

Y: 36 , N: 12 , NMI: 26 , A: 29