

IEEE P802.3dj Joint Optics/Logic Track Ad Hoc Report

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Report

- 2 ad hoc calls since May 2023 meeting
 - 15 June, 29 June
 - Well attended
 - 7 contributions
- Next meetings will occur between July and September TF meetings :
 - Will be announced over the track email reflector
- Minutes:
 - [15 June](#), [29 June](#)

Presentations

June 29

- "By-Passing the DR/FR inner FEC code for 200G/Lane" presented by Mike Dudek, Marvell
- "Module output BER requirements with inner FEC" presented by Adeo Ran, Cisco
- "TDECQ Analysis with Increased SER Targets" presented by Greg LeCheminant, Keysight

June 15

- "802.3dj SMF Channel Definition - CD_Q approach utilizing PMD_Q methodology " presented by Vince Ferretti, Corning
- "Dependence of $CD_{min,Q}$ and $CD_{max,Q}$ on Q and the Number of Fiber Segments (M) in 800G-LR4" presented by Xiang Liu, Huawei
- "1.6TbE alignment marker characteristics" presented by Ryan Wang, Broadcom
- "FEC_I Sublayer Proposal for Type 2 PHYs " presented by Xiang He, Huawei

Key Themes

- Baseline proposal updates:
 - Logic updates (1.6TbE alignment marker clock/BLW analysis, inner FEC lanes for Type 2 PHYs, Interleaving analysis on AUI BER requirements)
- Useful technical contributions to help with analysis of baselines
 - New approach to address Chromatic Dispersion:
 - CD_Q methodology – based on known ITU specified PMD_Q approach
 - Proposal to enable inner FEC Bypass
 - Continued support of enabling inner FEC Bypass, but how to enable will need to be figured out
 - Initial TDECQ analysis in presence of inner code FEC
 - Potential to become a big-ticket item

THANKS!