Agenda and General Information

Joint Session - IEEE P802.3cw Task Force IEEE P802.3df Task Force IEEE P802.3dj Task Force

IEEE 802.3 Sept 2023 Interim Campinas, Brazil

John D'Ambrosia, Chair, IEEE P802.3cw Task Force Chair, IEEE P802.3df Task Force Chair, IEEE P802.3dj Task Force Futurewei, U.S. Subsidiary of Huawei



Joint Task Force Meetings

- Per Motion #8 approved @ IEEE 802.3 Closing, Mar 2023 (see minutes @ https://www.ieee802.org/3/minutes/mar23/0323_minutes_draft.pdf)
 - Move that the IEEE 802.3 Working Group grant approval for the P802.3cw Task Force to meet with P802.3df / P802.3dj Task Forces to take motions during any joint meeting between these task forces only
- Joint Task Force meetings will be announced

Joint IEEE P802.3cw / .3df / .3dj Task Force Session

Agenda

- Approve Agenda
- Approve Minutes (see next page)
- Decorum
- Attendance
- Task Forces Project Information
- Goals For Session
- Ground Rules
- Voting / Voting Rights
- Review IEEE SA Policies (http://ieee802.org/3/policies.html / included) *
 - IEEE SA Patent Policy
 - IEEE SA Participation Policy
 - IEEE SA Copyright Policy
- Liaisons Overview
- 802.3dj Baseline Status
- Ad hocs Summary
- Presentations 802.3dj
- Chief Editor's Report / Comment Agenda 802.3df
- Chief Editor's Report 802.3dj
- Future Meetings
 - Task Force
 - Ad hocs
- Closing Business

^{*} Note - additional slides covering procedures and IEEE-SA Policies are in Backup

Minutes to be Approved

- IEEE P802.3cw / .3df / .3dj Joint Task Force Plenary Session July 2023
 - https://www.ieee802.org/3/dj/public/23_07/minutes_3dj_2307_unapproved.pdf
 - Modification request received (name spelling correction not implemented yet)
- **IEEE P802.3df /3dj 20 July 2023 Electron Interim**
 - https://www.ieee802.org/3/dj/public/23_0720/minutes_3dfdj_230720_unapproved.pdf
- **IEEE P802.3cw 22 Aug 2023 Electronic Interim**
 - https://www.ieee802.org/3/cw/public/23_0822/minutes_3cw_230822_unapproved.pdf
- IEEE P802.3dj Electrical Ad hoc 31 Aug 2023
 - https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0831/minutes_3dj_elec_230 831.pdf
- IEEE P802.3dj Joint Logic / Optis Ad Hoc 29 Aug 2023
 - https://www.ieee802.org/3/dj/public/adhoc/optics/0823_OPTX/minutes_3dj_optx_230829_unapproved.pdf

Meeting Decorum







- An officer is permitted to make an audio or slideshow recording of this
 meeting exclusively for the purpose of generating minutes which shall not
 be copied or distributed. IEEE 802.3 meetings do not use this option.
 Recording of the proceedings by any other participant or observer, in part or
 in whole, via any means, is prohibited. (Feb 2023 IEEE-SA Standards Board
 Ops Manual 5.3.3.2)
- Press (i.e., anyone reporting publicly on this meeting) are to announce their presence (Feb 2023 IEEE-SA Standards Board Ops Manual 5.3.3.3)
- Cell phone ringers off
- Please observe proper decorum in meetings
- Please mute your self when not speaking
- Please do not turn on video

Attendance

- All attendees must register their attendance via IMAT
 - IMAT -
 - Tutorial http://ieee802.org/3/minutes/attendance_procedures.pdf
 - Access details
 - URL: http://imat.ieee.org/
 - 802.3 Password to be given out by chair at meeting

- Please be sure to sign-in with your name / affiliation information
- Zoom attendance will be used to identify remote attendance as well.

IEEE P802.3cw Task Force Project information

Task Force Organization

- John D'Ambrosia, Chair, IEEE P802.3cw Task Force
- Editorial Team
 - **▶** Tom Issenhuth Chief Editor, 400 GbE Optical PHY Clause
 - **Leon Bruckman 400 GbE PCS/PMA Clauses**
- Task force web and reflector information
 - Reflector: http://www.ieee802.org/3/ct/reflector.html
 - Home page: http://www.ieee802.org/3/cw/index.html
- Project Documentation
 - PAR: http://www.ieee802.org/3/cw/proj_doc/P802d3cw_PAR.pdf
 - > CSD: https://mentor.ieee.org/802-ec/dcn/19/ec-19-0219-00-ACSD-p802-3cw.pdf
 - > Objectives: http://www.ieee802.org/3/cw/proj_doc/3cw_Objectives_190911.pdf
 - Timeline: https://www.ieee802.org/3/cw/proj_doc/timeline_3cw_220321.pdf
- Ad Hoc page: http://www.ieee802.org/3/ct/public/adhoc/index.html

IEEE P802.3df Task Force Project information

Organization

- John D'Ambrosia, Chair, IEEE P802.3dj Task Force
- Mark Nowell, Vice-Chair, IEEE P802.3dj Task Force; Chair, Optics Track
- Kent Lusted, Secretary, IEEE P802.3dj Task Force; Chair, Electrical Track
- Matt Brown, IEEE P802.3dj Chief Editor
- Mark Gustlin, Chair, Architecture and Logic Track

Task force web and reflector information:

- Home page: https://www.ieee802.org/3/df/index.html
- Reflector Info https://www.ieee802.org/3/df/reflector.html
 - TF Reflector: <u>stds-802-3-b400g@listserv@ieee.org</u>
 - Logic Reflector: <u>stds-802-3-b400g-logic@listserv@ieee.org</u>
 - Optical Reflector: <u>stds-802-3-b400g-optx@listserv@ieee.org</u>
 - Electrical Reflector: stds-802-3-b400g-elec@listserv@ieee.org

Project Documentation –

- PAR: https://www.ieee802.org/3/df/proj_doc/P802.3df_031222.pdf
- CSD: https://mentor.ieee.org/802-ec/dcn/21/ec-21-0306-01-ACSD-p802-3df.pdf
- Objectives: https://www.ieee802.org/3/df/proj_doc/objectives_P802d3df_221117.pdf
- Adopted Timeline: https://www.ieee802.org/3/df/proj_doc/timeline_3df_221004.pdf

P802.3df TF meeting information may be found on:

- Public page: https://www.ieee802.org/3/df/public/index.html
- 802.3 Calendar: https://www.ieee802.org/3/calendar.html

IEEE P802.3dj Task Force Project information

Organization

- John D'Ambrosia, Chair, IEEE P802.3dj Task Force
- Mark Nowell, Vice-Chair, IEEE P802.3dj Task Force; Chair, Optics Track
- Kent Lusted, Secretary, Chair, Electrical Track
- Matt Brown, IEEE P802.3dj Chief Editor
- Mark Gustlin, Chair, Architecture and Logic Track

Task force web and reflector information:

- Home page: https://www.ieee802.org/3/df/index.html
- Reflector Info https://www.ieee802.org/3/df/reflector.html
 - TF Reflector: stds-802-3-b400g@listserv@ieee.org
 - Logic Reflector: <u>stds-802-3-b400g-logic@listserv@ieee.org</u>
 - Optical Reflector: <u>stds-802-3-b400g-optx@listserv@ieee.org</u>
 - Electrical Reflector: stds-802-3-b400g-elec@listserv@ieee.org

Project Documentation –

- PAR: https://www.ieee802.org/3/dj/projdoc/P802d3dj_PAR.pdf
- CSD: https://mentor.ieee.org/802-ec/dcn/22/ec-22-0256-00-ACSD-p802-3dj.pdf
- Objectives: https://www.ieee802.org/3/dj/projdoc/objectives_P802d3dj_230518.pdf
- Adopted Timeline: https://www.ieee802.org/3/dj/projdoc/timeline_3dj_230116.pdf

P802.3dj TF meeting information may be found on:

- Public page: https://www.ieee802.org/3/df/public/index.html
- 802.3 Calendar: https://www.ieee802.org/3/calendar.html

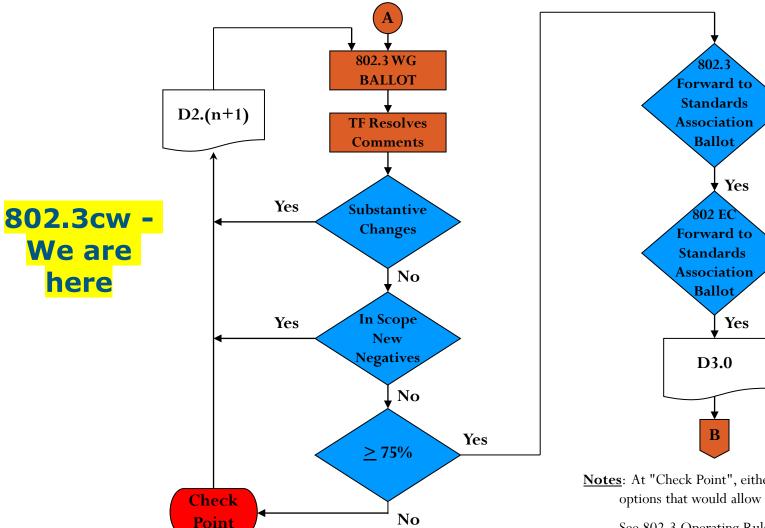
Goals for the Session

- IEEE P802.3cw
 - Consider proposed liaison from OIF
- IEEE P802.3df
 - Chief Editor's Report
 - Start consideration of comments submitted during initial Standards
 Association Ballot
 - Bucket 1
- IEEE P802.3dj
 - Hear technical presentations and proposed baselines
 - Consider liaison from Ultra Ethernet Consortium

Ground Rules

- Based upon IEEE 802.3 Rules
 - Foundation based upon Robert's Rules of Order
 - Anyone in the room may speak
 - Anyone in the room may vote on straw polls, but we have to follow 802.3 Voting rules
- RESPECT... give it, get it
- NO product pitches
- NO corporate pitches
- NO prices!!!
 - This includes costs, ASPs, etc. no matter what the currency
- NO restrictive notices

Overview of IEEE 802.3 Standards Process (3/5) – Working Group Ballot Phase (802.3cw)



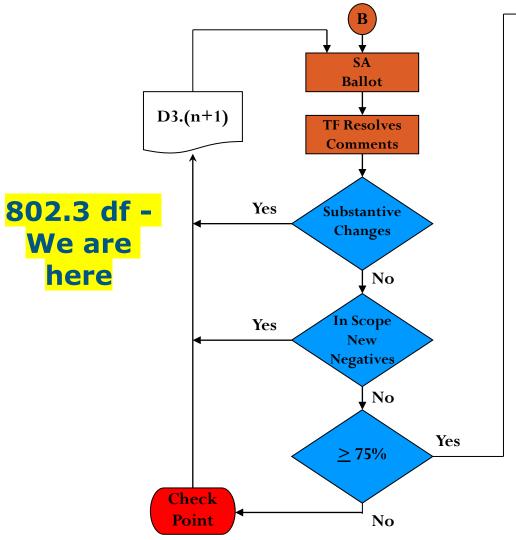
<u>Notes</u>: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

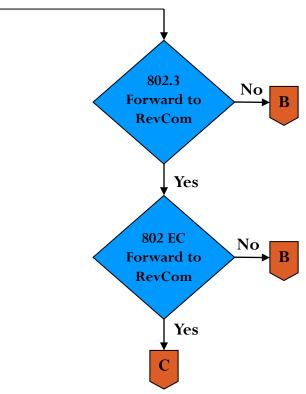
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No

See 802.3 Operating Rules 2.6 and listed references for complete description

Overview of IEEE 802.3 Standards Process (4/5)-Standards Association (SA) Ballot Phase (802.3df)

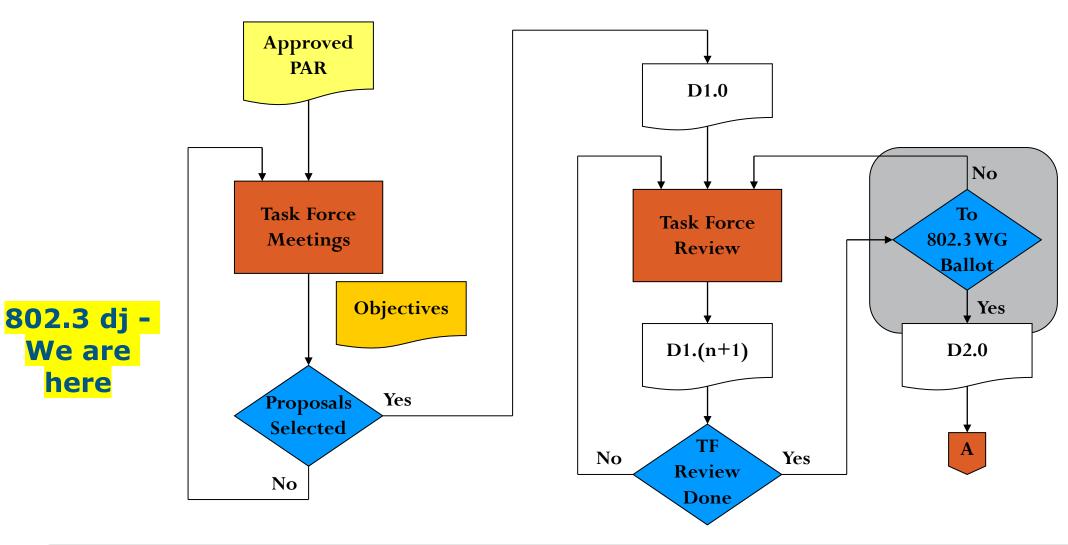




Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See IEEE SA Standards Board Operation Manual 5.4 for complete description.

Overview of IEEE 802.3 Standards Process (2/5) -Task Force Comment Phase (802.3dj)



Sept 2023

We are

here

IEEE P802.3dj Task Force

Voting

- Per 802.3 WG Chair "the September 2023 interim meeting series, the IEEE 802.3 Working Group interim meeting will be held in person, with provision for remote attendance provided on a best-effort basis.", i.e. "electronic meeting"
 - See https://www.ieee802.org/3/email-dialog/msg01464.html
- Reference: Excerpt from Section 3.4.2, IEEE 802.3 Operations Manual
 - http://www.ieee802.org/3/rules/P802 3 rules.pdf

There are two type of votes in the TF; votes on motions and votes in straw polls.

In face-to-face meetings where a TF is operating with membership rules for voting (see 3.3) only the TF members may make and vote on motions. In face-to-face meetings where membership rules are not in force all TF participants may make and vote on motions, subject only to the provision that they believe that they are qualified to vote on the matter before the TF. Electronic meetings that will consider motions shall be approved by the WG Chair prior to the meeting announcement. In such meetings only IEEE 802.3 Working Group members may make and vote on motions. If a motion is not approved by unanimous consent it shall be taken as a roll call vote

In all cases, regardless of any membership rules that may or may not be in force, all participants who feel qualified may participate in a straw poll

Note - Chair reserves the right to take informative straw polls by 802.3 WG voting membership

■ IEEE 802.3 Requirements for Working Group voting membership – https://www.ieee802.org/3/rules/member.html

Review of IEEE SA Policies

- Email reminders
 - IEEE P802.3cw https://www.ieee802.org/3/ct/email/msg01098.html
 - IEEE P802.3df / .3dj https://www.ieee802.org/3/B400G/email/msg00908.html

sent to Task Force Reflector reminding all to review and be familiar with following policies:

- Patent Policy
- Copyright Policy
- Participation
- See Backup for slides

Liaisons Overview (As of 11 Sept 2023)

- Incoming:
 - [IEEE P802.3cw] From OIF to IEEE 802.3: Response to request for data related to EVM and OIF 400ZR IA maintenance update
 - https://www.ieee802.org/3/minutes/sep23/incoming/OIF_liaison_IEEE802.3_EVM_14Aug23_ _Redacted.pdf
 - [IEEE P802.3dj] Liaison from Ultra Ethernet Consortium
 - https://ieee802.org/3/minutes/sep23/incoming/UEC_liaison_to_802d3_redacted.pdf

Summary IEEE P802.3dj Progress @ End of 20 July 2023 Meeting – Logic

	AUI	Backplane	CU Cable	SMF 500m/2km	SMF 10km 4 Wavelength	SMF 10km 1 Wavelength	SMF 40km
Ethernet Rate	PCS/FEC/PMA?	PCS/FEC/PMA?	PCS/FEC/PMA?	PCS/FEC/PMA?	PCS/FEC/PMA?	PCS/FEC/PMA?	PCS/FEC/PMA?
200 Gb/s				Open issues *			
400 Gb/s				Open issues *			
800 Gb/s				Open issues *	Open issues *		Two proposals
1.6 Tb/s				Open issues *			

Adopted baselines

Proposed Baselines

- * Open Issues include:
- Inner FEC pad usage
- Inner FEC synchronization method

Summary IEEE P802.3dj Progress @ End of 20 July 2023 Meeting - PMDs (& AUIs)

Ethernet Rate	Assumed Signaling Rate	AUI	Backplane	Cu Cable	SMF 500m	SMF 2km	SMF 10km	SMF 40km
200 Gb/s	200 Gb/s	200GAUI-1 C2C <mark>C2M</mark>	200GBASE-KR1	200GBASE-CR1	200GBASE-DR1	200GBASE-FR1		
400 Gb/s	200 Gb/s	400GAUI-2 C2C <mark>C2M</mark>	400GBASE-KR2	400GBASE-CR2	400GBASE-DR2	400GBASE-DR2-2		
800 Gb/s	200 Gb/s	800GAUI-4 C2C <mark>C2M</mark>	800GBASE-KR4	800GBASE-CR4	800GBASE-DR4	1. 800GBASE-DR4-2 2. 800GBASE-FR4	800GBASE-LR4	
	800 Gb/s						Over single SMF in each direction Coherent	Over single SMF in each direction Coherent
1.6 Tb/s	100 Gb/s	1.6TAUI-16 C2C C2M						
	200 Gb/s	1.6TAUI-8 C2C <mark>C2M</mark>	1.6TBASE-KR8	1.6TBASE-CR8	1.6TBASE-DR8	1.6TBASE-DR8-2		

Adopted baselines

Proposed Baselines

IEEE P802.3df Ad Hocs

Announcement - https://www.ieee802.org/3/B400G/email/msg00257.html

1. Architecture and Logic Ad hoc - Mark Gustlin, Ad Hoc Chair

Charter: The Architecture and Logic Ad Hoc is chartered to address the following:

- 1) Develop terminology and definitions for terms related to architecture, including but not limited to FEC Architecture, FEC Scheme, End-to-end FEC, Segmented FEC, Concatenated FEC. Draft definitions to be presented to Task Force at May 2022 session.
- 2) Act as forum to discuss architectural requirements and consider proposals related to PCS, FEC, and PMA logic sublayers.

2. Electrical Ad Hoc – Kent Lusted, Ad Hoc Chair

Charter: The Electrical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to electrical interfaces and electrical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact electrical interfaces and electrical PMD sublayers
 - c. Electrical interfaces and electrical PMD nomenclature
- 2) Provide inputs based on electrical interfaces and electrical PMD sublayers into any relevant ad hocs

3. Optics Ad Hoc - Mark Nowell, Ad Hoc Chair

Charter: The Optical Ad Hoc is chartered to address the following:

- 1) Act as forum to discuss and consider technical proposals and contributions:
 - a. Related to optical PMD sublayers
 - b. Related to logic PCS, FEC, and PMA sublayers that may impact optical PMD sublayers
 - c. Optical PMD nomenclature
- 2) Provide inputs based on optical PMDs into any relevant ad hocs

Ad hoc Summary

- 802.3dj Ad Hoc Meetings
 - Joint Logic / Optics Ad hoc (15th, 29th of Aug 2023)
 - Meeting materials https://www.ieee802.org/3/dj/public/adhoc/optics/0823_OPTX/index.html
 - Ad hoc Report https://www.ieee802.org/3/dj/public/23_09/nowell_3dj_01_2309.pdf
 - Electrical Ad hoc (17th, 31st of Aug 2023)
 - Meeting materials https://www.ieee802.org/3/dj/public/adhoc/electrical/index.html
 - Ad hoc Report https://www.ieee802.org/3/dj/public/23_09/lusted_3dj_01_2309.pdf

Sept 2023

Sept 2023 Session (1 of 4)

All times and order listed are subject to change. See meeting webpage for latest presentation version

Mon	11-Sep-23		4			
Time	Presenter	Affiliation	Title	File Name		
9:00 AM	John D'Ambrosia	Futurewei, U.S. Subsidiary of Huawei	Agenda and General Information	agenda_3cwdfdj_2309		
9:30 AM	Kent Lusted	Intel	IEEE P802.3dj Electrical Ad Hoc Update	lusted_3dj_01_2309		
9:40 AM	Kent Lusted	Intel	Open Electrical Items in P802.3dj as of Sept 2023	lusted_3dj_03_2309		
10:00 AM	Kent Lusted	Intel	IEEE P802.3dj Package Status Update & Next Steps	lusted_3dj_04_2309		
10:30 AM	Break					
11:00 AM	Mike Li	Intel	A 212.5 Gbps-PAM4 1 Meter DAC Long Reach Channel and Its Characteristics: Design C	lim_3dj_03_2309		
11:15 AM	Mike Li	Intel	212.5 Gbps-PAM4-LR Link Simulation and Analysis with a 1 Meter DAC Channel: Design C	lim_3dj_04_2309		
11:30 AM	Chris DiMinico	PHY-SI/SenTekse/MC Communications	CR Considerations for Insertion Loss Budget Baseline	diminico_3dj_01_2309		
12:00 PM	Strawpolls					
1:00 PM	Lunch					
2:15 PM	Rich Mellitz	Samtec	Brief on aspects of COM configuration for 200 Gb/s PAM 4 - How to get what you want!	mellitz_3dj_01_2309		
2:45 PM	Mike Li	Intel	Reference Receiver FFE with Floating Taps in COM Methodology	lim_3dj_05_2309		
3:05 PM	Tobey P.R. Li	MediaTek	Considerations on COM reference model	lit_3dj_01_2309		
3:30 PM	Break					
4:00 PM	Adam Healey	Broadcom	Considerations for the choice of eta0	healey_3dj_01_2309		
4:30 PM	Upen Reddy Kareti	Cisco	200G AUI C2M System Application and Chanel Model Update	kareti_3dj_01_2309		
5:00 PM	Recess for Day					

Sept 2023 Session (2 of 4)

All times and order listed are subject to change. See meeting webpage for latest presentation version

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<u>Tues</u>	12-Sep-23			
Time	Presenter	Presenter Affiliation Title		File Name
9:00 AM	John D'Ambrosia	Futurewei, U.S. Subsidiary of Huawei	Chair's Opening Comments	
9:10 AM	Mike Li	Intel	A 212.5 Gbps-PAM4 Chip-to-Module Channel for "Universal Port" and Its Characteristics: Design C	lim_3dj_01_2309
9:25 AM	Mike Li	Intel	212.5 Gbps-PAM4 Chip-to-Module Link Simulation and Analysis with a "Universal Port" Channel: Des	iglim_3dj_02_2309
9:40 AM	Kent Lusted	Intel	200 Gbps/lane AUI C2M Channel Selection Criteria Update – Sept 2023	lusted_3dj_02_2309
10:10 AM	Strawpolls			•
10:30 AM	Break			
11:00 AM	Adee Ran	Cisco	Baseline proposal for chip-to-module attachment unit interface (AUI-C2M) at 200 Gb/s per lane	ran_3dj_01_2309
11:30 AM	Ali Ghiasi	Ghiasi Quantum / Marvell	Toward AUI C2M Baseline Assuming Two Package Classes	ghiasi_3dj_02_2309
12:00 PM	Kent Lusted	Intel	200G/lane AUI C2M IL target	lusted_3dj_06_2309
12:20 PM	Kent Lusted Intel		200G/lane AUI BER Allocation for Type 1 and Type 2 PHYs	lusted_3dj_05_2309
1:00 PM	Lunch			
2:30 PM	Strawpolls			
3:00 PM	Xiang He	Huawei	Inner Codeword Self-sync Proposal	he_3dj_01_2309
3:30 PM	Break			
4:00 PM	Sridhar Ramesh	Maxlinear	Reliability Analysis of In-Band signaling Message Fields for Type 2 PHYs	ramesh_3dj_01_2309
4:30 PM	Zvi Rechtman	Nvidia	Pad insertion with 8:1 Hamming Interleaver for Inner F	rechtman_3dj_01_2309
5:00 PM	Recess for Day			

Sept 2023 Session (3 of 4)

All times and order listed are subject to change. See meeting webpage for latest presentation version

	Presenter	Affiliation	Title	File Name		
9:00 AM	John D'Ambrosia	Futurewei, U.S. Subsidiary of Huawei	Chair's Opening Comments			
9:05 AM	Strawpolls					
9:35 AM	Mark Nowell	Cisco	IEEE P802.3dj Joint Logic / Optics Ad hoc Update	nowell_3dj_01_2309		
9:45 AM	Brian Welch	Cisco	Optical Specifications for Different FEC Modes at 200G/L - Methodology	welch_3dj_01_2309		
10:15 AM	Brian Welch	Cisco	Baseline Proposals for 200G/L PMD Specifications for Single Wavelength 500m and 2km Standards	welch_3dj_03_2309		
10:40 AM	Break					
11:10 AM	Eric Bernier	Huawei	Enablement of multiple FEC modes for 200 Gb/s per lane, 500m and 2km optical PMDs.	bernier_3dj_01_2309		
11:40 AM	Brian Welch	/elch Cisco 800G-FR4 without Inner FEC - Market Needs		welch_3dj_02_2309		
12:05 PM	Brian Welch	Cisco	Baseline Proposals for 800GBASE-FR4	welch_3dj_04_2309		
12:30 PM	Strawpolls					
1:00 PM	Lunch					
2:30 PM	Anil Mehta	Broadcom	Reference: Transmitter Training Proposal for 256GFC	mehta_3dj_01_2309		
3:00 PM	Ali Ghiasi	Ghiasi Quantum / Marvell	Extending Link Configuration and Training to Optical PMDs Requested time	ghiasi_3dj_01_2309		
3:30 PM	Break					
4:00 PM	Mike Dudek	ke Dudek Marvell The effect of AUI error propagation on required optical error rate.		dudek_3dj_01_2309		
5:00 PM	Recess for Day					

Sept 2023 Session (4 of 4)

All times and order listed are subject to change. See meeting webpage for latest presentation version

<u>Thurs</u>	14-Sep-23	IEEE P802.3dj				
	Presenter	Affiliation	Title	File Name		
9:00 AM	John D'Ambrosia	Futurewei, U.S. Subsidiary of Huawei	Chair's Opening Comments			
9:05 AM	Greg Le Cheminant	Keysight Technologies	Physical measurements for TDECQ versus SER	lecheminant_3dj_01_2309		
9:35 AM	Roberto Rodes	Coherent	Towards an 800G-LR4 IMDD Specification Consensus - September 2023 update	rodes_3dj_01_2309		
9:55 AM	Eric Maniloff	Ciena	Update on key technical decisions for the 802.3dj coherent optical objectives	maniloff_3dj_01_2309		
10:20 AM	Break					
10:50 AM	Strawpolls					
11:20 AM	Matt Brown	Alphawave Semi	IEEE P802.3df Chief Editor's Report	brown_3df_02_2309		
11:30 AM	Matt Brown	Alphawave Semi	IEEE P802.3df - D3.0 Comment agenda preview	brown_3df_01_2309		
11:40 AM	Matt Brown	Alphawave Semi	IEEE P802.3dj Chief Editor's Report	brown_3dj_01_2309		
12:00 PM	Discussion, Strawpolls, Motions					
1:00 PM	Lunch					
2:30 PM	Discussion, Strawpolls, Motions					
3:30 PM	Break Street Str					
4:00 PM	Discussion, Strawpolls, Motions					
4:30 PM	Closing Business					
5:00 PM	Adjourn					

Future Meetings

IEEE P802.3cw: https://www.ieee802.org/3/cw/public/index.html
IEEE P802.3df: https://www.ieee802.org/3/dj/public/index.html

- Contingent 21 Sept 2023 Joint IEEE P802.3dj Interim
- IEEE P802.3cw Comment Consideration (4TH WG Recirculation)
 - 25 Sept 2023
- IEEE P802.3df Comment Consideration (1 st SA Ballot)
 - -26-28 Sept 2023
- Joint IEEE P802.3cw / .3df / .3dj TF Nov 2023 Plenary
 - Week of Nov 13 16,2023
 - Hawaiian Village
 - Oahu, Hawaii, USA
- Any additional meetings will be announced, as appropriate.

Future Ad hoc Meetings

To Be Announced

THANK YOU!



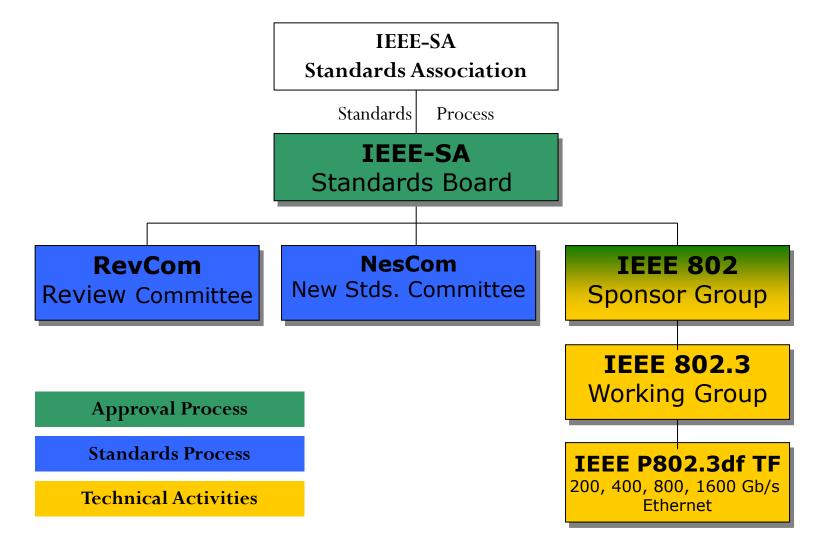
BACKUP



The Role of the Chair / Vice Chair

- Per the IEEE 802.3 Ethernet WG Operations Manual (http://www.ieee802.org/3/rules/P802_3_rules.pdf)
 - Per 3.4 Operation of the Task Force
 - The operation of the TF has to be balanced between democratic procedures that reflect the desires of the TF members and the TF Chair's responsibility to produce a draft standard, recommended practice, or guideline in a reasonable amount of time for review and approval by the WG. Robert's Rules of Order shall be used in combination with these operating rules to achieve this balance.
 - The full responsibilities of the chair are specified in 3.4.3 Task Force Chair's Responsibilities.
 - The Role of the Vice Chair is specified in 3.2 -
 - There may be a TF Vice-Chair. A TF Vice-Chair carries out the TF Chair's duties if the TF Chair is temporarily unable to do so or chooses to delegate specific duties.
- Reminders
 - We are a contribution driven organization. It should not be assumed by anyone that work will
 just get done without contributions to drive it. Areas not moving forward will be brought to the
 attention of the Task Force for consideration on how to address.
 - Individual standards activities within the WG are, at the discretion of the WG, carried out by Task Forces (TF) operating under, and reporting to, the WG.

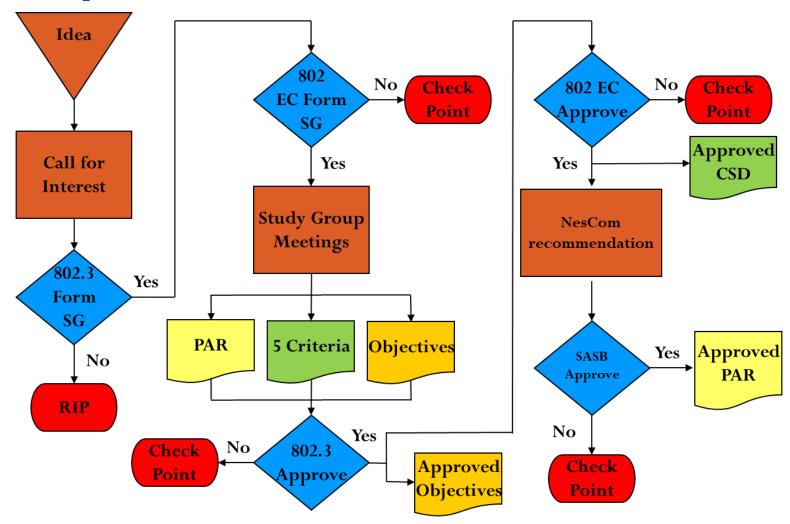
IEEE Structure



Important Bylaws, Rules, and References

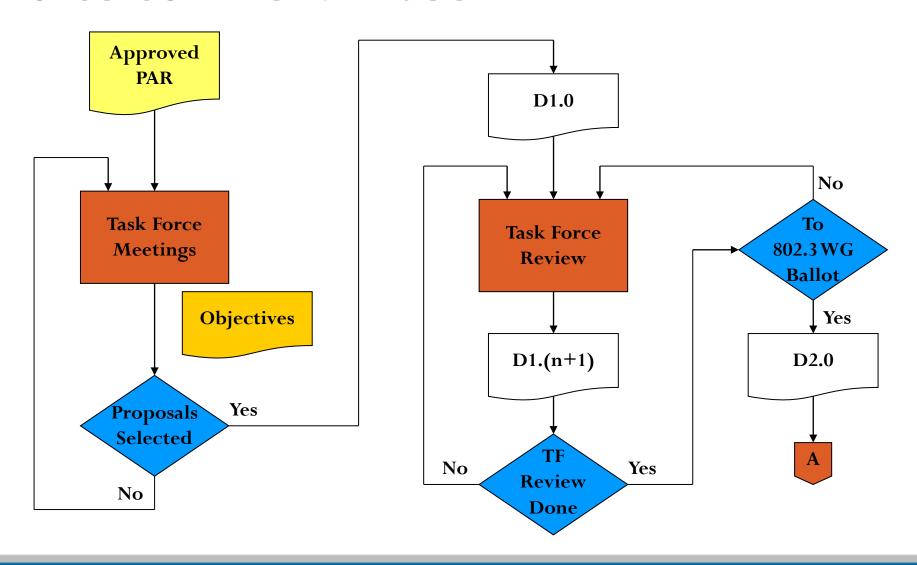
- IEEE-SA Operations Manual http://standards.ieee.org/develop/policies/sa_opman/
- IEEE-SA Standards Board Bylaws http://standards.ieee.org/develop/policies/bylaws/
- IEEE-SA Standards Board Operations Manual http://standards.ieee.org/develop/policies/opman/
- IEEE 802 LAN/MAN Standards Committee (LMSC) Policies and Procedures https://ieee.app.box.com/v/PandP-LMSC
- IEEE 802 LAN/MAN Standards Committee (LMSC) Operations Manual http://www.ieee802.org/devdocs.shtml
- IEEE 802 LAN/MAN Standards Committee (LMSC) Working Group (WG) Policies and Procedures
 - http://www.ieee802.org/devdocs.shtml
- IEEE 802.3 Working Group Operating Rules http://ieee802.org/3/rules/P802_3_rules.pdf
- "Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association's Antitrust and Competition Policy" http://standards.ieee.org/develop/policies/antitrust.pdf

Overview of IEEE 802.3 Standards Process (1/5)-**Study Group Phase**

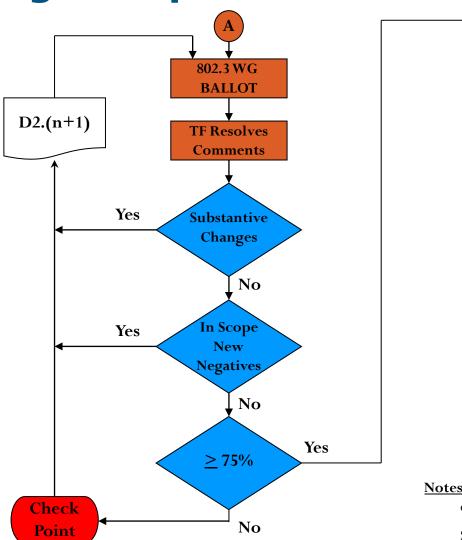


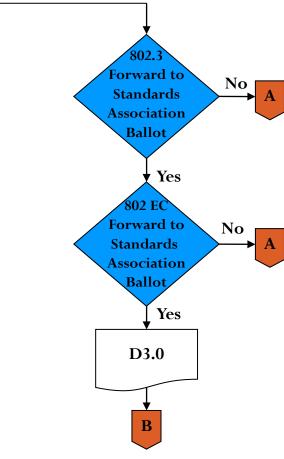
Note: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

Overview of IEEE 802.3 Standards Process (2/5) – Task Force Comment Phase



Overview of IEEE 802.3 Standards Process (3/5) – Working Group Ballot Phase

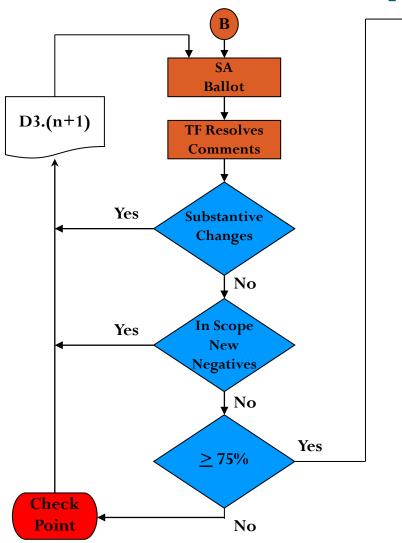


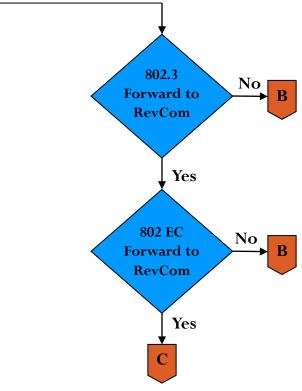


<u>Notes</u>: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See 802.3 Operating Rules 2.6 and listed references for complete description

Overview of IEEE 802.3 Standards Process (4/5)-Standards Association (SA) Ballot Phase

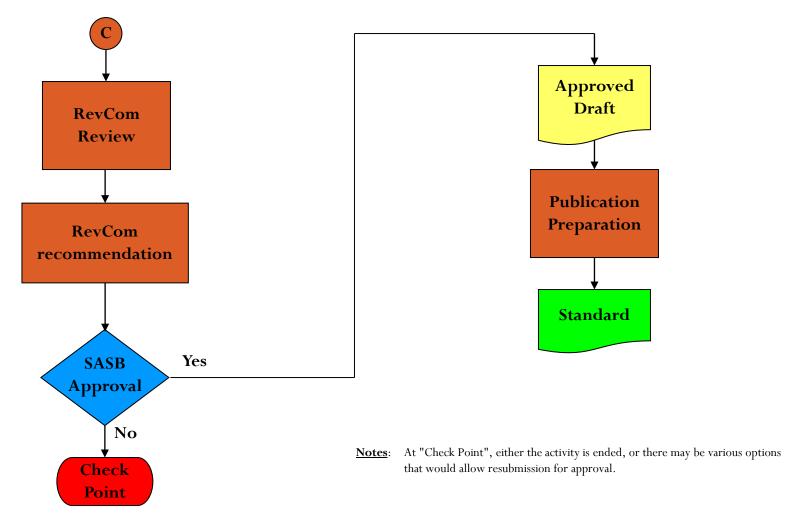




Notes: At "Check Point", either the activity is ended, or there may be various options that would allow reconsideration of the approval.

See IEEE SA Standards Board Operation Manual 5.4 for complete description.

Overview of IEEE 802.3 Standards Process (5/5) – Final Approvals / Standard Release



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IEEE SA POLICIES

IEEE SA PATENT POLICY

Instructions for the WG Chair

- The IEEE SA strongly recommends that at each WG meeting the chair or a designee:
 - Show slides 1 through 4 of this presentation
 - Advise the WG attendees that:
 - IEEE's patent policy is described in Clause 6 of the IEEE SA Standards Board Bylaws;
 - Early identification of patent claims which may be essential for the use of standards under development is strongly encouraged;
 - There may be Essential Patent Claims of which IEEE is not aware. Additionally, neither IEEE, the WG, nor the WG Chair can ensure the accuracy or completeness of any assurance or whether any such assurance is, in fact, of a Patent Claim that is essential for the use of the standard under development.
 - Instruct the WG Secretary to record in the minutes of the relevant WG meeting:
 - That the foregoing information was provided and that slides 1 through 4 (and this slide 0, if applicable) were shown;
 - That the chair or designee provided an opportunity for participants to identify patent claim(s)/patent application claim(s) and/or the holder of patent claim(s)/patent application claim(s) of which the participant is personally aware and that may be essential for the use of that standard
 - Any responses that were given, specifically the patent claim(s)/patent application claim(s) and/or the holder of the patent claim(s)/patent application claim(s) that were identified (if any) and by whom.
 - The WG Chair shall ensure that a request is made to any identified holders of potential essential patent claim(s) to complete and submit a Letter of Assurance.
 - It is recommended that the WG Chair review the guidance in *IEEE SA Standards Board Operations Manual* 6.3.5 and in FAQs 14 and 15 on inclusion of potential Essential Patent Claims by incorporation or by reference.
- Note: WG includes Working Groups, Task Groups, and other standards-developing committees with a PAR approved by the IEEE SA Standards Board.





Participants have a duty to inform the ieee

- Participants <u>shall</u> inform the IEEE (or cause the IEEE to be informed) of the identity of each holder of any potential Essential Patent Claims of which they are personally aware if the claims are owned or controlled by the participant or the entity the participant is from, employed by, or otherwise represents
- Participants <u>should</u> inform the IEEE (or cause the IEEE to be informed)
 of the identity of any other holders of potential Essential Patent
 Claims

-Early identification of holders of potential Essential Patent Claims is encouraged





Ways to inform ieee

- Cause an LOA to be submitted to the IEEE SA (patcom@ieee.org); or
- Provide the chair of this group with the identity of the holder(s) of any and all such claims as soon as possible; or
- Speak up now and respond to this Call for Potentially Essential Patents
- If anyone in this meeting is personally aware of the holder of any patent claims that are potentially essential to implementation of the proposed standard(s) under consideration by this group and that are not already the subject of an Accepted Letter of Assurance, please respond at this time by providing relevant information to the WG Chair





Other Guidelines for IEEE Working Group Meetings

- All IEEE SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.
 - Don't discuss the interpretation, validity, or essentiality of patents/patent claims.
 - Don't discuss specific license rates, terms, or conditions.
 - Relative costs of different technical approaches that include relative costs of patent licensing terms may be discussed in standards development meetings.
 - Technical considerations remain the primary focus.
 - Don't discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.
 - Don't discuss the status or substance of ongoing or threatened litigation.
 - Don't be silent if inappropriate topics are discussed. Formally object to the discussion immediately.
 - For more details, see IEEE SA Standards Board Operations
 Manual, clause 5.3.10 and
 Antitrust and Competition Policy: What You Need to Know at http://standards.ieee.org/develop/policies/antitrust.pdf







Patent-related information

- The patent policy and the procedures used to execute that policy are documented in the:
 - IEEE SA Standards Board Bylaws
 (http://standards.ieee.org/develop/policies/bylaws/sect6-7.html#6)
 - IEEE SA Standards Board Operations Manual (http://standards.ieee.org/develop/policies/opman/sect6.html#6.3)
 - -Material about the patent policy is available at http://standards.ieee.org/about/sasb/patcom/materials.html

If you have questions, contact the IEEE SA Standards Board Patent Committee Administrator at patcom@ieee.org





IEEE SA COPYRIGHT POLICY

Instructions for Chairs of standards development activities

- At the beginning of each standards development meeting the chair or a designee is to:
 - Show the following slides (or provide them beforehand)
 - Advise the standards development group participants that:
 - IEEE SA's copyright policy is described in Clause 7 of the IEEE SA Standards Board Bylaws and Clause 6.1 of the IEEE SA Standards Board Operations Manual;
 - Any material submitted during standards development, whether verbal, recorded, or in written form, is a Contribution and shall comply with the IEEE SA Copyright Policy;
 - Instruct the Secretary to record in the minutes of the relevant meeting:
 - That the foregoing information was provided and that the copyright slides were shown (or provided beforehand).





IEEE SA Copyright Policy

- By participating in this activity, you agree to comply with the IEEE Code of Ethics, all applicable laws, and all IEEE policies and procedures including, but not limited to, the IEEE SA Copyright Policy.
 - Previously Published material (copyright assertion indicated) shall not be presented/submitted to the Working Group nor incorporated into a Working Group draft unless permission is granted.
 - Prior to presentation or submission, you shall notify the Working Group Chair of previously Published material and should assist the Chair in obtaining copyright permission acceptable to IEEE SA.
 - For material that is not previously Published, IEEE is automatically granted a license to use any material that is presented or submitted.





IEEE SA Copyright Policy

- The IEEE SA Copyright Policy is described in the IEEE SA Standards Board Bylaws and IEEE SA Standards Board Operations Manual
- IEEE SA Copyright Policy, see
 Clause 7 of the IEEE SA Standards Board Bylaws
 https://standards.ieee.org/about/policies/bylaws/sect6-7.html#7
 Clause 6.1 of the IEEE SA Standards Board Operations Manual https://standards.ieee.org/about/policies/opman/sect6.html
 - IEEE SA Copyright Permission
- https://standards.ieee.org/content/dam/ieee-standards/standards/web/documents/other/permissionltrs.zip
 - IEEE SA Copyright FAQs
- http://standards.ieee.org/faqs/copyrights
 - IEEE SA Best Practices for IEEE Standards Development
- http://standards.ieee.org/content/dam/ieee-standards/standards/web/documents/other/best_practices_for_ieee_standards_development_051215.pdf
 - Distribution of Draft Standards (see 6.1.3 of the SASB Operations Manual)
- https://standards.ieee.org/about/policies/opman/sect6.html





IEEE SA PARTICIPATION SLIDES

Participant behavior in IEEE-SA activities is guided by the IEEE Codes of Ethics & Conduct

- All participants in IEEE-SA activities are expected to adhere to the core principles underlying the:
 - IEEE Code of Ethics
 - IEEE Code of Conduct
- The core principles of the IEEE Codes of Ethics & Conduct are to:
 - Uphold the highest standards of integrity, responsible behavior, and ethical and professional conduct
 - Treat people fairly and with respect, to not engage in harassment, discrimination, or retaliation, and to protect people's privacy.
 - Avoid injuring others, their property, reputation, or employment by false or malicious action
- The most recent versions of these Codes are available at http://www.ieee.org/about/corporate/governance

Participants in the IEEE-SA "individual process" shall act independently of others, including employers

- The <u>IEEE-SA Standards Board Bylaws</u> require that "participants in the IEEE standards development individual process shall act based on their qualifications and experience"
- This means participants:
 - Shall act & vote based on their personal & independent opinions derived from their expertise, knowledge, and qualifications
 - Shall not act or vote based on any obligation to or any direction from any other person or organization, including an employer or client, regardless of any external commitments, agreements, contracts, or orders
 - Shall not direct the actions or votes of other participants or retaliate against other participants for fulfilling their responsibility to act & vote based on their personal & independently developed opinions
- By participating in standards activities using the "individual process", you are deemed to accept these requirements; if you are unable to satisfy these requirements then you shall immediately cease any participation

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IEEE-SA standards activities shall allow the fair & equitable consideration of all viewpoints

- The <u>IEEE-SA Standards Board Bylaws</u> (clause 5.2.1.3) specifies that "the standards development process shall not be dominated by any single interest category, individual, or organization"
 - This means no participant may exercise "authority, leadership, or influence by reason of superior leverage, strength, or representation to the exclusion of fair and equitable consideration of other viewpoints" or "to hinder the progress of the standards development activity"
- This rule applies equally to those participating in a standards development project and to that project's leadership group
- Any person who reasonably suspects that dominance is occurring in a standards development project is encouraged to bring the issue to the attention of the Standards Committee or the project's IEEE-SA Program Manager