200G/lane AUI BER Allocation for Type 1 and Type 2 PHYs

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Supporters and Contributors

Supporters:

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Introduction

- In July 2023, the 3dj Task Force adopted a DER_0 value of 2.67E-5 for the AUIs within a PHY
- This contribution will focus on the allocation of BER for AUIs inside a Type 1 or Type 2 PHY (not part of an extender sublayer)
 - AUIs are optional instantiations
- Note: "BER" is loosely used in this contribution to represent "random BER" and recognize there is much discussion on the topic

Motion #6

Move to adopt one DERO value of 2.67e-5 (equivalent to measured BER of 4e-5 with precoding ON) as the total allocation for 200Gbps/lane AUIs within a PHY (BER division between C2C and C2M as well as the measurement method to be determined later)

M: Adee Ran
S: Tobey P.-R. Li
Technical (>=75%)
802.3 voters only
Result: passed by unanimous consent. 9:19 a.m.

https://www.ieee802.org/3/dj/public/23_07/motions_3cwdfdj_2307.pdf

PHY/FEC types defined in https://www.ieee802.org/3/dj/public/23_03/brown_3dj_01a_2303.pdf

Current status

The allocation of the DER_0 between the AUI C2M vs. AUI C2C is currently not adopted

Presentations on the topic include:

- <u>https://www.ieee802.org/3/dj/public/23_07/ran_3dj_01a_2307.pdf</u>
- https://www.ieee802.org/3/dj/public/23_07/ghiasi_3dj_02a_2307.pdf
- https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0817/ran_3dj_01_230817.pdf
- https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0622/lit_3dj_elec_01_230622.pdf
- https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0622/ghiasi_3dj_02_2309.pdf
- https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0622/lusted_3dj_02_2309.pdf

Explicit allocation of DER_0 is needed to progress towards AUI C2M and AUI C2C baseline proposals

Link Diagram Reference - BER



Link Diagram Reference Using <u>DER_0</u>

Will use DER_0 to avoid "BER" term conundrum DER_0 total was set by a motion DER_0 needed for COM analysis



Note: AUIs inside Type 1 and Type 2 PHYs

Common AUI Use Case Examples - FPP

Host IC "Retimer" to passive copper cable

No IC "Retimer" to Module

Host IC "Retimer" to Module

Two Choices for DER_0 Allocation of C2M

Choice A: "always a single value" Choice B: "one of two values"

https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0817/ran_3dj_01_230817.pdf

A1: "always a single value" - 50/50

Divide the allocation equally between C2M and C2C (50%/50%) Allocation never changes Favors simplicity over DER_0 budget

Note: AUIs inside Type 1 and Type 2 PHYs

A2: "always a single value" - X/Y

Divide the allocation with ratio TBD between C2M (Y) and C2C (x) x+y = 2.67 and x != y and x < y Allocation never changes Favors simplicity over DER_0 budget

Note: AUIs inside Type 1 and Type 2 PHYs

B1: "one of two values" - 50/50

Divide the allocation between C2M and C2C:

- If host has only one AUI , DER_0 = 2.67E-5
- If two has two AUIs (e.g. C2M and C2C), each AUI is allocated a DER_0 = 1.33E-5

Favors DER_0 budget over simplicity

B2: "one of two values" - X/Y

Divide the allocation between C2M and C2C:

- If host has only one AUI, DER_0 = 2.67E-5
- If two has two AUIs (e.g. C2M and C2C) with ratio TBD between C2M (Y) and C2C (x)
- x+y = 2.67 and x != y and x < y

Favors DER_0 budget over simplicity

AUI DER_0 Allocation Choices

	DER_0 allocation		
	AUI C2C and AUI C2M	AUI C2C only	AUI C2M only
Choice A1	1.33E-5 and 1.33E-5	1.33E-5	1.33E-5
Choice A2	xE-5 and yE-5 (x+y = 2.67 and x != y and x < y)	xE-5	<mark>yE-5</mark>
Choice B1	1.33E-5 and 1.33E-5	2.67E-5	2.67E-5
Choice B2	xE-5 and yE-5 (x+y = 2.67 and x != y and x < y)	2.67E-5	<mark>2.67E-5</mark>

Summary

- The allocation of the DER_0 between the AUI C2M vs. AUI C2C is currently not adopted
- Two high-level choices for DER_0 allocation for C2M:
 - Choice A: "always a single value"
 - Choice B: "one of two values"
- Each choice has a second level decision of 50/50 or x/y ratios

Straw polls and possible motions on these topics were requested

We must determine the AUI DER_0 allocation (C2M vs. C2C) <u>now</u> to progress towards baselines!

Thanks!