

IEEE P802.3dj Reference Package Model and Parameters

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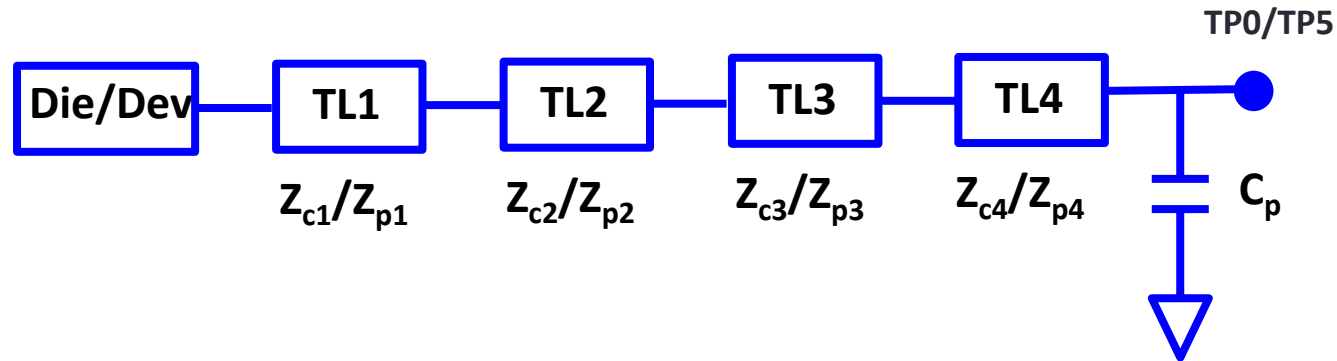
Background and Objectives

- 802.3dj is heading to adopt two classes of reference packages [15]: “Class A”, ([1], [2], [3],[4], [5], [6],[7],[8]) optimized for loss; and “Class B” ([9],[10],[11],[[12],[13],[14]) optimized for high radix applications.
- This presentation intends to present a unified and latest reference package model, and model parameter specifics towards baselines for 802.3dj.

Summary of Key Design/Material Characteristics of “Class A” and “Class B” PKGs

Package Design Specifics	“Class A” PKG Characteristics	“Class B” PKG Characteristics
ABF (Ajinomoto build-up film) material	GL107 Like	GL107 Like
Cross-section	8-2-8, or 10-2-10	6-2-6, to 9-2-9
Core thickness	~1000 μm	800-1200 μm
Trace routing lengths	33 mm max	45 mm max
Surface treatment	CZ8401 Like	CZ8401 Like
BGA ball pitch	0.8 mm	1.0 mm
Skip Layer	Yes (x%)	No
Trace line / space	~30 / 60 / 30 μm	32-45-32 μm
Trace line / space (Skip Layer)	~80 / 80 / 80 μm	NA
Impedance	~87.5 ohms	90-92 ohms
ABF height	35 μm	45 μm

A Unified Reference PKG Model for “Class A” and “Class B”



For “Class A”

- Only TL1 (i.e., horizontal trace) and TL2 (vertical PTH) will to be used.

For “Class B”

- All four TLs will be used.

Summary of Ref PKG Performance for the Proposed “Class A” and “Class B” (1/2)

Param	802.3ck PKG T-Line Model Param	Prop 802.3dj “Class A” PKG T-Line Model Param ([8])	Prop 802.3dj “Class B” PKG T-Line Model Param ([14])
Z_p	30 mm	30 mm	30 mm
γ_0	0 /mm	5e-4 /mm	5e-4 /mm
τ	6.141e-3 ns/mm	6.141e-3 ns/mm	6.141e-3 ns/mm
a_1	9.909e-4 ns ^{1/2} /mm	8.9e-4 ns ^{1/2} /mm	6.5e-4 ns ^{1/2} /mm
a_2	2.772e-4 ns/mm	2.0e-4 ns/mm	3.0e-4 ns/mm
Z_{c1}	87.5 Ω	87.5 Ω	92 Ω
R_o	50 Ω	50 Ω	50 Ω



- “Class B” 212G Pkg 30mm T-Line Nov’23
 - “Class A” 212G Pkg 30mm T-Line May’23
 - 802.3ck 106G Pkg 30mm T-Line
- “Class A” temp at 90C
 - “Class B” temp at 90C

Summary of Ref PKG Performance for the Proposed “Class A” and “Class B” (2/2)

Param	802.3ck PKG T-Line Model Param	Prop 802.3dj “Class A” PKG T-Line Model Param ([8])	Prop 802.3dj “Class B” PKG T-Line Model Param ([14])
Z_p	33 mm	33 mm	33mm
γ_0	0 /mm	5e-4 /mm	5e-4 /mm
τ	6.141e-3 ns/mm	6.141e-3 ns/mm	6.141e-3 ns/mm
a_1	9.909e-4 ns ^{1/2} /mm	8.9e-4 ns ^{1/2} /mm	6.5e-4 ns ^{1/2} /mm
a_2	2.772e-4 ns/mm	2.0e-4 ns/mm	3.0e-4 ns/mm
Z_{c1}	87.5 Ω	87.5 Ω	92 Ω
Z_{p2}	1.8 mm	1.8 mm	1/1/0.5 mm
Z_{c2}	92.5 Ω	92.5 Ω	70/80/100 Ω
R_o	50 Ω	50 Ω	50 Ω
C_p	87 fF	40 fF	40 fF

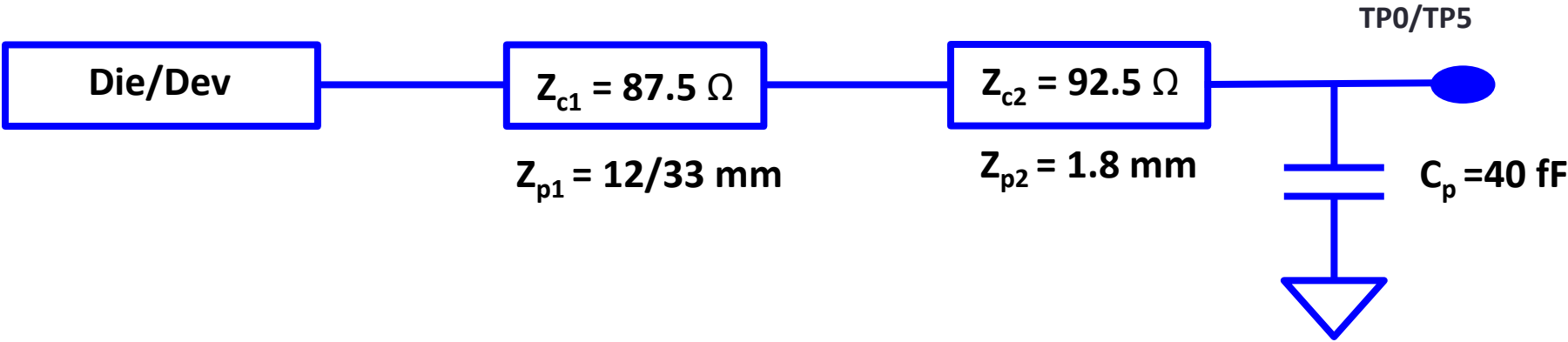


- “Class B” 212G Pkg 33mm T-Line+PTH+Cp Nov’23
- “Class A” 212G Pkg 33mm T-Line+PTH+Cp May’23
- 802.3ck 106G Pkg 33mm T-Line+PTH+Cp

“Class A” temp at 90C
 “Class B” temp at 90C

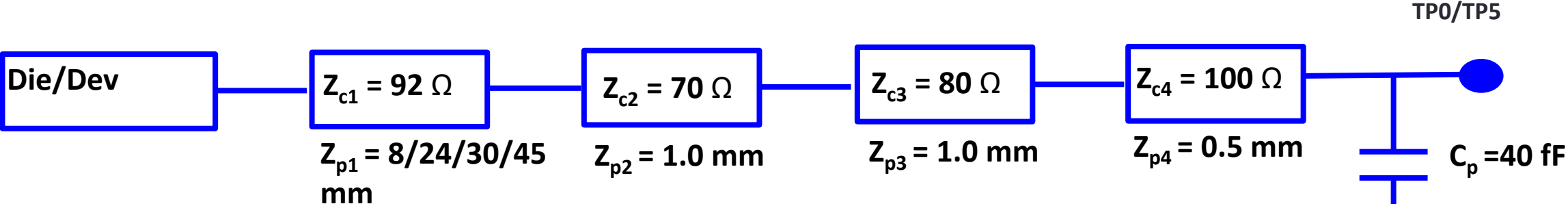
Recap of Proposed “Class A” PKG Model Parameters

Parameter	Setting	Units
package_tl_gamma0_a1_a2	[0.0005 0.00089 0.0002]	
package_tl_tau	0.006141	ns/mm



Recap of Proposed “Class B” PKG Model Parameters

Parameter	Setting	Units
package_tl_gamma0_a1_a2	[0.0005 0.00065 0.0003]	
package_tl_tau	0.006141	ns/mm



Summary

- “Class A” and “Class B” reference package model and parameters are reviewed and corresponding performances are highlighted.
- “Class A” and “Class B” model parameter specifics are proposed towards baselines for 802.3dj

References

- [1] J. Jiang et al, “Designing 224G PAM4 High Performance FPGA Package and Board with Confidence”, *Designcon*, 2021.
- [2] M. Li et al. “224G Package Investigations and COM Reference Model”, OIF (<https://www.oiforum.com>, oif2021.263.00), Nov, 2021
- [3] M. Li et al. “Reference Die and Package Models for CEI-224G-PAM4”, OIF (<https://www.oiforum.com>, oif2022.065.01), Feb, 2022
- [4] M. Li et al. : https://www.ieee802.org/3/df/public/22_03/mli_3df_01a_220316.pdf, Mar, 2022
- [5] M. Li et al. : https://www.ieee802.org/3/df/public/22_03/mli_3df_02a_220316.pdf, Mar, 2022
- [6] Mellitz et al. : https://www.ieee802.org/3/df/public/22_03/mellitz_3df_01b_220316.pdf, Mar, 2022
- [7] M. Li et al. : https://www.ieee802.org/3/dj/public/23_05/li_3dj_02_2305.pdf , May, 2023
- [8] M. Li et al. : https://www.ieee802.org/3/dj/public/23_0720/lim_3dj_02a_2307.pdf, Jul, 2023
- [9] L. Ben-Artsi, and R. Mellitz: https://www.ieee802.org/3/df/public/22_07/benartsi_3df_01b_2207.pdf, Jul, 2022
- [10] R. Mellitz, A. Ran, L. Ben-Artsi: https://www.ieee802.org/3/df/public/22_11/benartsi_3df_01a_2211.pdf , Jul, 2022
- [11] A. Ghiasi, A. Ran, R. Mellitz, L. Ben-Artsi:
https://www.ieee802.org/3/dj/public/adhoc/electrical/23_0622/benartsi_3dj_elec_01a_230622.pdf , June, 2023
- [12] A. Ghiasi, https://www.ieee802.org/3/df/public/22_10/22_0927/ghiasi_3df_01_220927.pdf , Sept, 2022
- [13] L. Ben-Artsi, https://www.ieee802.org/3/dj/public/23_07/benartsi_3dj_02_2307.pdf, Jul, 2023
- [14] L. Ben-Artsi et al., https://www.ieee802.org/3/dj/public/23_11/benartsi_3dj_01_2311.pdf, Nov, 2023
- [15] K. Lusted et al., https://www.ieee802.org/3/dj/public/23_11/lusted_3dj_02_2311.pdf, TBI, Nov, 2023

Thank You!