IEEE P802.3dj Electrical Ad Hoc Report

Kent Lusted, Intel Electrical Track Ad Hoc Chair

Report

- · 1 ad hoc call since September 2023 meeting
 - 26 October
 - 75+ attendees
 - 5 contributions
 - 2 additional channel contributions
- Next meeting TBD:
 - To be announced over the electrical track email reflector

Presentations

• Meeting minutes and presentation materials:

https://www.ieee802.org/3/dj/public/adhoc/electrical/index.html

- 26 October
 - "P802.3dj Reference Die/Device Model Parameter Values Status Update", Kent Lusted
 - "IEEE P802.3dj 200Gbps/lane Cabled Backplane Channels", Femi Akinwale
 - "C2M Cabled Host Channel Contribution with Skew Considerations", Brandon Gore
 - "C2M Channel Analysis Trends Suggesting COM Parameters Path Forward", Rich Mellitz
 - "CR/KR Channel Analysis Trends Suggesting COM Parameters Path Forward", Rich Mellitz

Key Themes

- Many important reference equalizer details need to be worked out.
 - C2M
 - CR
- C2M channels: tradeoffs to consider for reference EQ taps, MLSE, and PHY type (1/2/3)
- Channel intra-pair skew becoming very important

THANKS!