

IEEE P802.3dj/.3df Joint Optics/Logic Track Ad Hoc Report

Mark Nowell, Cisco
Optics Track Ad Hoc Chair

Gary Nicholl, Cisco
Logic Track Ad Hoc Chair

Report

- 2 ad hoc calls since September 2023 meeting
 - 19 Oct, 2 Nov
 - Joint optics and logic track meeting. Nov 2nd meeting was also joint with 802.3df to incorporate some .3df topic)
 - 70+ attendees each time
 - 6 contributions
- Next meetings will occur between November and January TF meetings :
 - Will be announced over the track email reflector
- Minutes:
 - [19 Oct](#), [2 Nov](#)

Presentations

Oct 19th

- "Test Validation of 800G-LR4 Transceivers" presented by Xiang Liu, Huawei
- "Enablement of multiple FEC modes for single-wavelength 200 Gb/s per lane 500 m and 2 km PMDs" presented by Eric Bernier

November 2

- "Missing pieces of the logic baseline" presented by Mark Gustlin, Cisco
- "Codeword examples erratum" presented by Xiang He, Huawei
- "Considerations on FEC for 4x200G objectives in P802.3dj" presented by Peter Stassar, Huawei
- "Impact on Project Timeline" presented by John D'Ambrosia, Futurewei, U.S. Subsidiary of Huawei

Key Themes

- Continued discussion on FEC modes
 - No resolution or straw polls but more discussion
 - TF Chair has presented that resolution of this topic is affecting project timeline (see below)
- Logic topics clean up
 - 802.3df logic issue found in D3.0 review. Proposal to address reviewed
 - Some holes found in 802.3dj logic baselines identified and proposals to address reviewed
- Project Timeline
 - TF chair has shown that adoption of all baselines in next 1-2 meeting is necessary to ensure timely completion of project

THANKS!