Motions and Straw Polls

IEEE P802.3cw, IEEE P802.3df and P802.3dj Task Force Joint Meeting

January 2024 Interim

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Foreword

- Straw polls related to resolving comments may be found in the associated comment response files.
- This contribution summarizes motions and straw polls not related to comments.
- This contribution is not the official minutes of the meeting.

If there is any discrepancy between this contribution and the meeting minutes, then the minutes take precedence.

22 January 2024

I would support adopting the COM Die/Device model parameter values in lim_3dj_01_2401 slide 8 for 200G/Lane KR, CR, AUI chip-to-chip and chip-to-module

Results (all): Y: 49, N: 0, A: 23

I would support adopting the updated parameter values for Class B packages per benartsi_3dj_01_2401 slide 7

Results (all): Y: 44 , N: 1 , A: 39

I would support adopting the 200G/lane electrical baseline proposals summarized on ran_3dj_01a_2401 slide 29, with the addition that test fixtures for the CR PHYs are TBD.

Results (all): Y: 59, N: 0, A: 23

I would support adopting link training based on IEEE Std. 802.3ck-2022, Cl 162.8.11 as the baseline for 200G/lane Backplane and Copper Cable PMDs (with max_wait_timer = TBD) and in-band training based on the clause 136 training frame structure (Figure 136-3) for all PMAs with physically instantiated interfaces (AUIs) at 200 Gb/s per lane

Results (all): Y: N: A:

Note: Straw Poll #4 was tabled pending improved wording

I would support adopting the AN73 baseline proposal in lusted_3dj_04_2401, slides 6-14

Results (all) Y: 53 , N: 2 , A: 28

I would support the proposed reference receiver framework in healey_3dj_01_2401.pdf, slides 5-15

Results (all): Y: 65 , N: 0 , A: 21

For the 200G/lane electrical interfaces or PMDs having MLSE capability, the MLSE solution approach that I prefer is:

- A. Include MLSE COM calculations based on equation U1.a in shakiba_3dj_01b_2401 slide 9
- B. Include MLSE COM calculations based on equation U1.b in shakiba_3dj_01b_2401 slide 10
- C. Include MLSE COM calculations based on equation U1.c in shakiba_3dj_01b_2401 slide 11
- D. Need more information
- E. None of the above

(choose one)

Results (all): A: 0 , B: 1 , C: 47 , D: 16 , E: 7

23 January 2024

I would support adopting a PMD control function based on 162.8.11 (IEEE Std. 802.3ck-2022) for 200G/lane Backplane and Copper Cable PMDs, with max_wait_timer = TBD

Results (all): Y: 64, N: 0, A: 22

Note: Straw Poll #8 was an improved wording of Straw Poll #4 and focused on Backplane and Copper Cable PMDs

I would support adopting in-band training for PMAs with physically instantiated chip-to-module interfaces (AUI-C2M) at 200 Gb/s per lane, based on 162.8.11 (IEEE Std. 802.3ck-2022) with training frame bit assignments and state diagrams TBD

Results (all): Y: 49 , N: 8 , A: 27

Note: Straw Poll #9 was an improved wording of Straw Poll #4 and focused on AUI C2M

I would support adopting in-band training for PMAs with physically instantiated chip-to-chip interfaces (AUI-C2C) at 200 Gb/s per lane, based on 162.8.11 (IEEE Std. 802.3ck-2022) with training frame bit assignments and state diagrams TBD

Results (all): Y: 49, N: 2, A: 29

Note: Straw Poll #10 was an improved wording of Straw Poll #4 and focused on AUI C2C

I would support the adoption of the 800GBASE-FR4-500 baseline as shown in welch_3dj_01a_2401 pages 10-16

Results (all): Y: 53 , N: 22 , A: 13

I would support removing the convolutional interleaver from the inner FEC sublayer for the following PHYs:

 200GBASE-FR1, 400GBASE-DR2-2, 800GBASE-DR4-2, 800GBASE-FR4, 1.6TBASE-DR8-2

Results (all): Y: 38 , N: 11 , NMI: 33 , A: 17

I support the adoption of a target SER limit of 9.6E-3 for TECQ/TDECQ/SECQ for the 2km FECi based PMDs

Results (all): Y: , N: , NMI: , A:

Note: Straw Poll #13 was withdrawn

24 January 2024

I would support the proposal in cheng_3dj_01b_2401 pg 14.

Results (all): Y: 67 , N: 20 , A: 20

I would consider the adoption of more than one SMF Channel model approach for P802.3dj SMF PMDs if appropriate.

Results (all): Y: 59, N: 2, NMI: 5, A: 17

25 January 2024

Move that the P802.3cw/3df/3dj Task Forces approve:

- IEEE_802d3_to_ITU_3cw_2401_draft_Redacted.pdf with editorial license granted to the Chair (or his appointed agent) as liaison communications from the IEEE 802.3 Working Group to ITU.
- IEEE_802d3_to_ITU_OIF_3df_0124_draft_Redacted.pdf with editorial license granted to the Chair (or his appointed agent) as liaison communications from the IEEE 802.3 Working Group to ITU and OIF.

M: Tom Huber

S: Ali Ghiasi

Technical (>=75%)

802.3 voters only

Results: passed by unanimous consent. 8:57 a.m.

Move to adopt lusted_nowell_3dj_01_2401 page 3

M: Kent Lusted

S: Mark Nowell

Technical (>=75%)

802.3 voters only

Result: passed by unanimous consent. 9:05 a.m.

Move to adopt lusted_nowell_3dj_01_2401 page 2

M: Mark Nowell

S: Matt Brown

Technical (>=75%)

802.3 voters only

Result: Y: 76, N: 13, A: 12 motion passed 10:36 a.m.

Move to amend motion #3 to read:

 adopt lusted_nowell_3dj_01_2401 page 2 with the removal of the new 20km objective

M: Eric Maniloff

S: Xiang Liu

Technical (>=75%)

802.3 voters only

Result: Y: 19, N: 56, A: 18 Motion failed 10;33 a.m.

Move to adopt the 800GBASE-FR4-500 baseline as shown in welch_3dj_01a_2401 pages 10-16

M: Mark Nowell

S: Kent Lusted

Technical (>=75%)

802.3 voters only

Result: Y: 68, N: 16, A: 14 motion passed 10:53 a.m.

Move to adopt the COM Die/Device model parameters in lim_3dj_01_2401 slide 8 for 200G/Lane KR, CR, AUI chip-to-chip and chip-to-module

M: Kent Lusted

S: Mark Nowell

Technical (>=75%)

802.3 voters only

Result: passed by unanimous consent. 10:57 a.m.

Move to adopt lusted_nowell_3dj_01_2401 page 4

M: Kent Lusted

S: Mark Nowell

Technical (>=75%)

802.3 voters only

Result: Y: 58, N: 3, A: 20 Motion passed 11:33 a.m.

Move to table motion #7

M: Piers Dawe

S:

Technical (>=75%)

802.3 voters only

Result: Motion failed for a lack of a second. 11:26 a.m.

Move to adopt lusted_nowell_3dj_01_2401 page 6

M: Kent Lusted

S: Mark Nowell

Technical (>=75%)

802.3 voters only

Result: Y: 57, N: 5, A: 15 motion passed 11:48 a.m.

Move to adopt lusted_nowell_3dj_01_2401 page 7

M: Kent Lusted

S: Adee Ran

Technical (>=75%)

802.3 voters only

Result: passed by unanimous consent 1:41 p.m.

I would support adopting the 800GBASE-LR1 state diagrams in bruckman_3dj_01a_2401, slides 4-6 (with values of N and M as TBD)

Results (all): Y: 48, N: 0 A: 40

Move to adopt the 800GBASE-LR1 state diagrams in bruckman_3dj_01a_2401, slides 4-6 (with values of N and M as TBD)

M: Leon Bruckman

S: Eric Maniloff

Technical (>=75%)

802.3 voters only

Result: Passed by unanimous consent. 1:50 p.m.

Move to adopt the IMDD inner FEC example test vectors in levy_3dj_02a_2401.7z, as described in levy_3dj_01b_2401.

M: Matt Brown

S: Xiang He

Technical (>=75%)

802.3 voters only

Result: passed by unanimous consent. 1:53 p.m.

I would support adopting the optical PHY type auto-negotiation (OAN) proposed in brown_3dj_02_2401, slides 5-23 with the exception that the timing characteristics on slide 20 are TBD

Results (all): Y: 38, N: 19, A: 35