

200G/Lane electrical interface jitter parameters

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IEEE P802.3dj Task Force

January 2024

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Abstract

- ❖ Jitter and transition time is a trade off between performance and implementation power and cost. Jitter and transition time linearly scaled down from 100G/lane is challenging for massive 200G/lane productions given power and cost constraints.
- ❖ [ran 3dj elec 01a 231207](#) proposed to move the standard forward with some preliminary parameters. We investigated how to optimize σ_{RJ} and A_{DD} for broad implementation and reasonable performance.

Initial simulation conditions

- ❖ COM parameters (detail COM parameters in back up)
 - fb : 106.25 GBaud
 - Tx FIR : pre 2 taps, post 0 taps
 - Rx FFE : pre 6 taps, post 45 taps
 - DFE : 1 tap
 - No floating taps
 - DER_0 : 1E-4
 - η_0 : 4.0E-9
 - Tr : 4ps
 - σ_{RJ} : 10mUI
 - A_{DD} : 20mUI
 - Package A (case 2: zp=33mm)
 - ✓ Without MLSE
 - Package B (case 4: zp=45mm)
 - ✓ With MLSE

KR/CR Channels used for the study

- ❖ Selected 26 KR/CR channels from contribution to P802.3dj
 - For each data set, selected a few channels with die2die IL \leq 40dB
 - ✓ If possible, selected with PKG B
 - ✓ If not possible with PKG B, selected with PKG A
 - With PKG A, all channels have die2die IL \leq 40dB
 - ✓ All 26 channels are used for study with PKG A
 - With PKG B, only channels 1-9,12,16 have die2die IL \leq 40dB
 - ✓ Only those 11 channels are used for study with PKG B

CH#	data set	tag	CH IL (dB)	Fit CH IL (dB)	D2D IL PKGA(dB)	D2D IL PKGB(dB)	ILD (dB)	ERL (dB)	ICN (mV)	COM (dB) w/ PKG A w/ MLSE w/o MLSE	COM (dB) w/ PKG B w/ MLSE w/o MLSE	used for study w/ PKG A w/ PKG B
1	mellitiz_3dj_02_elec_230504	CA 200mm, FO 100mm, PCB 75mm	22.7638	21.5792	32.7456	39.4515	0.20705	18.6363	0.94363	5.1718 4.1102 4.9269 2.9748		x x
2		CA 200mm, FO 200mm, PCB 50mm	22.3229	21.3837	32.9123	39.2556	0.21796	17.9239	0.93516	4.9469 3.8088 4.2941 2.3837		x x
3		CA 500mm, FO 100mm, PCB 50mm	22.2536	20.9543	32.9592	39.2247	0.22056	17.7278	0.97363	4.9214 3.7906 4.3284 2.4181		x x
4		CA 1000mm, FO 100mm, PCB 25mm	21.8556	21.7501	33.0429	39.2081	0.22531	16.7368	0.8868	6.3565 5.0673 5.5984 3.5566		x x
5	weaver_3dj_02_2305	host1 150mm, host2 150mm	21.7407	22.0272	33.0356	39.1232	0.095847	17.1272	1.2286	4.9157 3.7017 4.2864 2.2928		x x
6		host1 150mm, host2 200mm	22.302	22.5379	33.4566	39.6155	0.088222	17.1397	1.2241	5.1304 3.6487 4.1698 2.1693		x x
7		host1 150mm, host2 250mm	22.9027	23.0468	34.1459	40.249	0.08713	17.1585	1.2199	4.7737 3.4397 3.9583 1.9165		x x
8	akinwale_3dj_01_2310	External 100mm	20.8348	20.5607	30.4362	37.305	0.13515	15.6556	1.127	5.9554 4.5797 5.329 3.2735		x x
9		External 300mm	22.361	22.2294	31.9688	38.8317	0.13663	15.8851	1.0168	5.6418 4.1382 4.7348 2.6861		x x
10		External 500mm	24.3314	23.9436	33.9163	40.7901	0.13558	16.0544	0.92529	5.3391 3.6619 3.98 1.9382		x
11	shanhbag_3dj_02_2305	TP0-TP5 27.4dB	27.414	27.2668	39.7417	45.4115	0.05707	20.5098	0.15032	5.0721 3.5175 1.9905 0.052272		x
12		TP0-TP5 19.3dB	19.2615	19.8094	31.3187	37.09	0.088295	18.5845	0.19937	6.703 5.7976 5.8174 4.0546		x x
13		TP0-TP5 28.0dB	27.7126	28.2966	39.7106	45.5274	0.089873	20.5282	0.12077	5.7527 3.8493 2.1651 0.21991		x
14	shanhbag_3dj_01_2305	TP0-TP5 23.5dB	23.5093	23.2555	35.423	41.351	0.16085	15.9775	0.86255	4.4951 3.6752 3.3999 1.5455		x
15		TP0-TP5 25.9dB	25.9311	25.6616	37.7637	43.5927	0.15881	16.7607	0.77498	4.2497 3.1728 2.4709 0.57448		x
16	weaver_3dj_elec_01_230622	host1 150mm, host2 150mm, room temp	21.7407	22.0272	33.0356	39.1232	0.095847	17.1272	0.94075	5.6358 4.437 4.7272 2.7335		x x
17		host1 150mm, host2 150mm, high temp	24.3128	24.5487	35.5648	41.6778	0.096646	17.9171	0.90613	5.3181 3.7417 3.6152 1.5871		x
18	weaver_3dj_02_2311	host1 HH, CA-A, host2 HN, vendor Y	23.4169	24.3217	34.729	40.8113	0.15852	16.6477	1.472	5.0421 3.6487 3.6152 1.5871		x
19		host1 HH, CA-B, host2 HL, vendor Y	26.6144	27.6132	37.8587	43.9932	0.14939	16.1264	1.1745	4.2487 2.6507 -0.49792 -0.49792		x
20		host1 HN, CA-B, host2 HN, vendor Y	25.1857	26.2401	36.4209	42.5509	0.15243	16.3062	1.2638	4.4788 3.0485 2.2955 0.3365		x
21		host1 HN, CA-C, host2 HL, vendor Y	28.5224	29.5245	39.6505	45.8497	0.14894	16.409	1.0179	3.5853 1.7449 -2.0418 -2.0418		x
22	kocsis_3dj_02_2305	config 3, host1 4dB, cable 1.5m, host2 4dB	26.6484	27.6629	37.9955	44.0577	0.16851	16.0875	1.3974	4.2955 2.4411 -0.48971 -0.48971		x
23		config 4, host1 3dB, cable 1m, host2 9dB	26.7388	27.8445	37.956	44.0924	0.17463	15.2592	1.3913	4.2293 2.3609 -0.57143 -0.57143		x
24		config 5, host1 9dB, cable 1m, host2 3dB	26.7388	27.8445	37.9603	44.0941	0.17463	15.2592	1.3913	4.1839 2.3154 -0.57143 -0.57143		x
25	lim_3dj_03_230629	Design A	28.0458	28.4533	38.2284	44.8606	0.17267	18.5992	1.7018	3.5111 1.8196 -1.334 -1.334		x
26	lim_3dj_04_230629	Design B	28.869	27.7792	38.8244	45.586	0.15683	18.2444	0.65531	3.0161 1.608 -1.3489 -1.3489		x

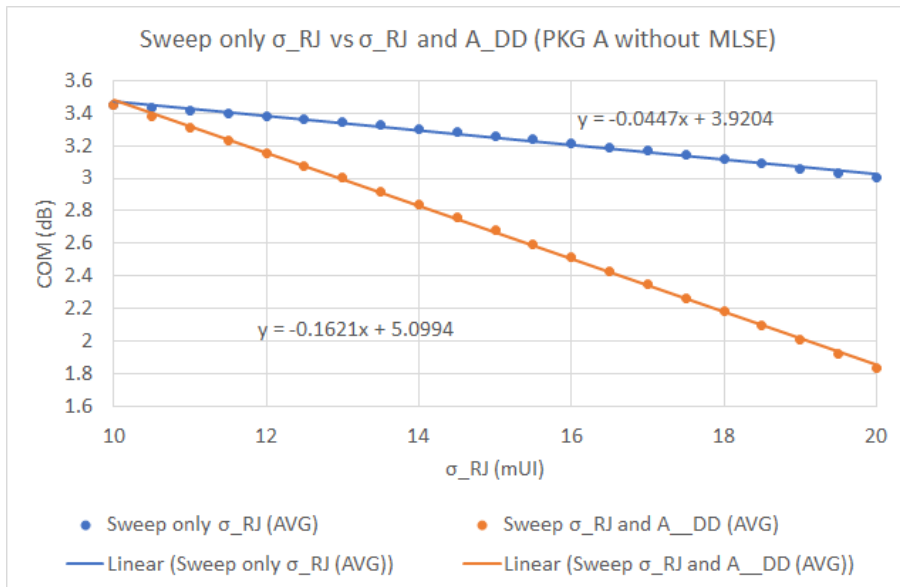
Sweep Conditions

- ❖ Sweep 1 : Sweep only σ_{RJ}
 - Sweep σ_{RJ} from 10 mUI to 20 mUI in 0.5 mUI step
 - Fix A_{DD} at 20 mUI

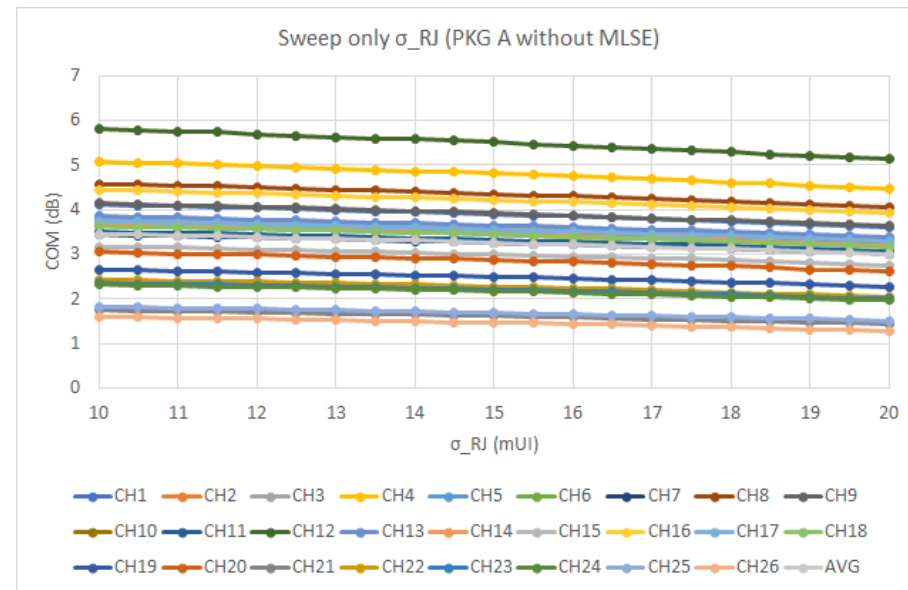
- ❖ Sweep 2 : Sweep σ_{RJ} and A_{DD} simultaneously with the same ratio
 - Sweep σ_{RJ} from 10 mUI to 20 mUI in 0.5 mUI step
 - Sweep A_{DD} from 20 mUI to 40 mUI in 1.0 mUI step

Results of Sweep 1&2 w/ PKG A (without MLSE)

- ❖ Sweeping only σ_{RJ} changes COM only moderately
- ❖ Sweeping σ_{RJ} and A_{DD} results in larger COM difference



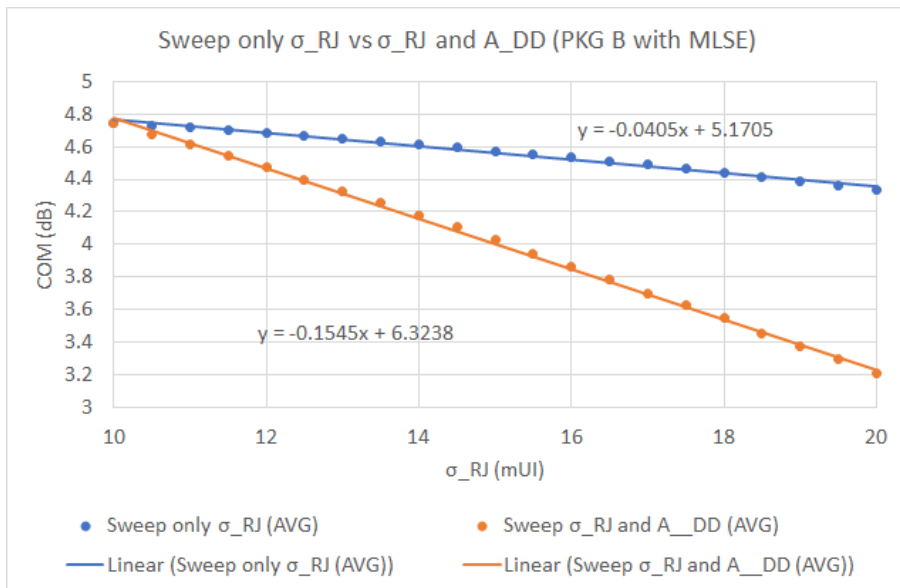
COM in average



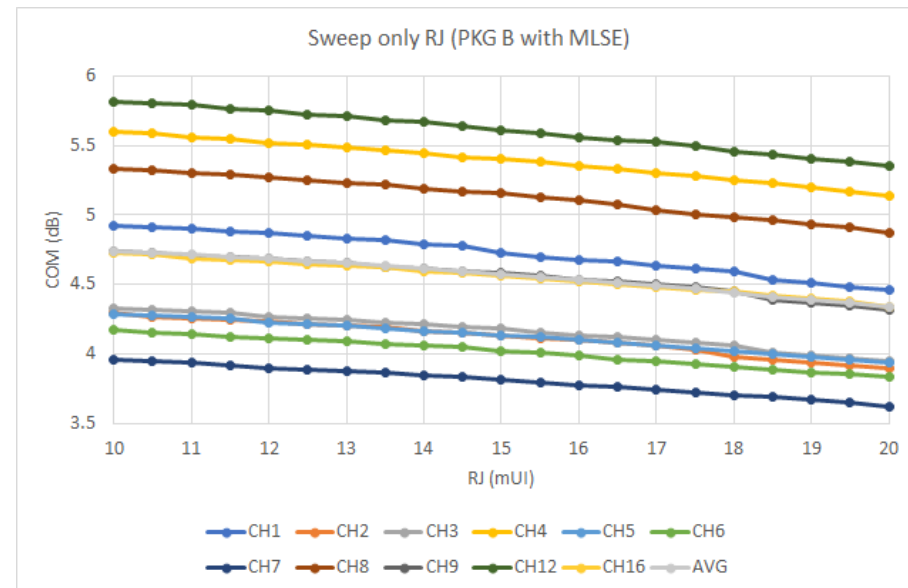
COM of each channel

Results of Sweep 1&2 w/ PKG B (with MLSE)

- ❖ Sweeping only σ_{RJ} changes COM only moderately
- ❖ Sweeping σ_{RJ} and A_{DD} results in larger COM difference



COM in average



COM of each channel

Summary of Results of Sweep 1 & 2

- ❖ COM difference for $\sigma_{RJ} = 15.0\text{mUI}$ from 10mUI with $A_{DD} = 20\text{mUI}$ is about 0.18dB

Sweep only σ_{RJ}								Sweep σ_{RJ} and A_{DD}							
σ_{RJ}	A_{DD}	COM (dB)			dCOM			σ_{RJ}	A_{DD}	COM (dB)			dCOM		
mUI rms	mUI pk	PKG A	PKG B	AVG	PKG A	PKG B	AVG	mUI rms	mUI pk	PKG A	PKG B	AVG	PKG A	PKG B	AVG
		w/o MLSE	w/ MLSE		w/o MLSE	w/ MLSE				w/o MLSE	w/ MLSE		w/o MLSE	w/ MLSE	
		26 CHs	11 CHs		26 CHs	11 CHs				26 CHs	11 CHs		26 CHs	11 CHs	
10.0	20	3.452912	4.742791	4.097851	0	0	0	10.0	20	3.452912	4.742791	4.097851	0	0	0
10.5	20	3.436073	4.729791	4.082932	-0.01684	-0.013	-0.01492	10.5	21	3.380985	4.679218	4.030101	-0.07193	-0.06357	-0.06775
11.0	20	3.418831	4.715573	4.067202	-0.03408	-0.02722	-0.03065	11.0	22	3.309512	4.613418	3.961465	-0.1434	-0.12937	-0.13639
11.5	20	3.402381	4.699636	4.051009	-0.05053	-0.04315	-0.04684	11.5	23	3.234262	4.543964	3.889113	-0.21865	-0.19883	-0.20874
12.0	20	3.384735	4.682382	4.033558	-0.06818	-0.06041	-0.06429	12.0	24	3.156638	4.473691	3.815165	-0.29627	-0.2691	-0.28269
12.5	20	3.366715	4.666073	4.016394	-0.0862	-0.07672	-0.08146	12.5	25	3.077377	4.396055	3.736716	-0.37553	-0.34674	-0.36114
13.0	20	3.345092	4.651409	3.998251	-0.10782	-0.09138	-0.0996	13.0	26	3.001277	4.326127	3.663702	-0.45163	-0.41666	-0.43415
13.5	20	3.325669	4.633582	3.979626	-0.12724	-0.10921	-0.11823	13.5	27	2.919192	4.254045	3.586619	-0.53372	-0.48875	-0.51123
14.0	20	3.3056	4.614691	3.960145	-0.14731	-0.1281	-0.13771	14.0	28	2.841219	4.179773	3.510496	-0.61169	-0.56302	-0.58736
14.5	20	3.283423	4.597264	3.940343	-0.16949	-0.14553	-0.15751	14.5	29	2.762642	4.107482	3.435062	-0.69027	-0.63531	-0.66279
15.0	20	3.261904	4.574964	3.918434	-0.19101	-0.16783	-0.17942	15.0	30	2.680096	4.026855	3.353475	-0.77282	-0.71594	-0.74438
15.5	20	3.239185	4.553818	3.896501	-0.21373	-0.18897	-0.20135	15.5	31	2.597932	3.944682	3.271307	-0.85498	-0.79811	-0.82654
16.0	20	3.215796	4.532491	3.874144	-0.23712	-0.2103	-0.22371	16.0	32	2.51553	3.859682	3.187606	-0.93738	-0.88311	-0.91025
16.5	20	3.191473	4.513882	3.852677	-0.26144	-0.22891	-0.24517	16.5	33	2.432946	3.783682	3.108314	-1.01997	-0.95911	-0.98954
17.0	20	3.167681	4.491773	3.829727	-0.28523	-0.25102	-0.26812	17.0	34	2.349994	3.700736	3.025365	-1.10292	-1.04205	-1.07249
17.5	20	3.141208	4.468509	3.804858	-0.3117	-0.27428	-0.29299	17.5	35	2.267328	3.626527	2.946928	-1.18558	-1.11626	-1.15092
18.0	20	3.117088	4.441736	3.779412	-0.33582	-0.30105	-0.31844	18.0	36	2.180478	3.545691	2.863084	-1.27243	-1.1971	-1.23477
18.5	20	3.088696	4.4111	3.749898	-0.36422	-0.33169	-0.34795	18.5	37	2.093207	3.452073	2.77264	-1.3597	-1.29072	-1.32521
19.0	20	3.060958	4.388036	3.724497	-0.39195	-0.35475	-0.37335	19.0	38	2.013534	3.376218	2.694876	-1.43938	-1.36657	-1.40298
19.5	20	3.034265	4.364873	3.699569	-0.41865	-0.37792	-0.39828	19.5	39	1.926112	3.294264	2.610188	-1.5268	-1.44853	-1.48766
20.0	20	3.006127	4.338536	3.672332	-0.44678	-0.40425	-0.42552	20.0	40	1.836115	3.208573	2.522344	-1.6168	-1.53422	-1.57551

σ_{RJ} and A_{DD} vs Calculated J_{RMS} and J_{3u}/J_{4u}

- ❖ If we keep A_{DD} 20mUI and compare the cases with 15 mUI and 10 mUI σ_{RJ} , the difference of J_{RMS} is 11.8%, J_{3u} is 30.4% and J_{4u} is 32.5%.

σ_{RJ} (mUI)	A_{DD} (mUI)	J_{RMS} (mUI)	J_{RMS} (ratio)	J_{3u} (mUI)	J_{3u} (ratio)	J_{4u} (mUI)	J_{4u} (ratio)
10.0	20	22.3607	100.000%	101.810	100.000%	114.385	100.000%
10.5	20	22.5887	101.020%	104.900	103.035%	118.104	103.251%
11.0	20	22.8254	102.078%	107.990	106.071%	121.823	106.503%
11.5	20	23.0705	103.175%	111.080	109.106%	125.542	109.754%
12.0	20	23.3238	104.307%	114.171	112.141%	129.261	113.005%
12.5	20	23.5850	105.475%	117.261	115.177%	132.980	116.257%
13.0	20	23.8537	106.677%	120.351	118.212%	136.699	119.508%
13.5	20	24.1299	107.912%	123.441	121.247%	140.418	122.759%
14.0	20	24.4131	109.179%	126.532	124.282%	144.137	126.010%
14.5	20	24.7032	110.476%	129.622	127.318%	147.856	129.262%
15.0	20	25.0000	111.803%	132.712	130.353%	151.576	132.513%

J_{RMS} and J_{3u}/J_{4u} in 100G spec

❖ Current jitter specs for 100G/L interfaces:

Interface	σ_{RJ} (mUI)	A_{DD} (mUI)	J_{RMS} (mUI)	J_{3u03} (mUI)	J_{3u} (mUI)	J_{4u03} (mUI)	J_{4u} (mUI)
CR (100G/L)	10	20	23	115	125		
KR (100G/L)	10	20	23	106	115		
C2C (100G/L)	10	20	23			118	128

J_{RMS} and J_{3u}/J_{4u} in 200G spec with 15mUI σ_{RJ}

- ❖ Compared to 100G spec, we can relax J_{RMS} by 11.8% in terms of UI, J_{3u} by 30.4% and J_{4u} by 32.5%.

Interface	σ_{RJ} (mUI)	A_{DD} (mUI)	J_{RMS} (mUI)	J_{3u03} (mUI)	J_{3u} (mUI)	J_{4u03} (mUI)	J_{4u} (mUI)
CR (200G/L)	15	20	25.7	150	163		
KR (200G/L)	15	20	25.7	138	150		
C2C (200G/L)	15	20	25.7			156	170

Note: The test point insertion loss (TP2 (CR) or TP0d (KR,C2C)) is assumed to be the same as 100G spec in this calculation but will certainly go up. For this increase of insertion loss, the J_{RMS} , J_{3u} , and J_{4u} specifications shall be relaxed further after the test point insertion loss specs are finalized.

Measurement Result of 212.5Gbps Instrument

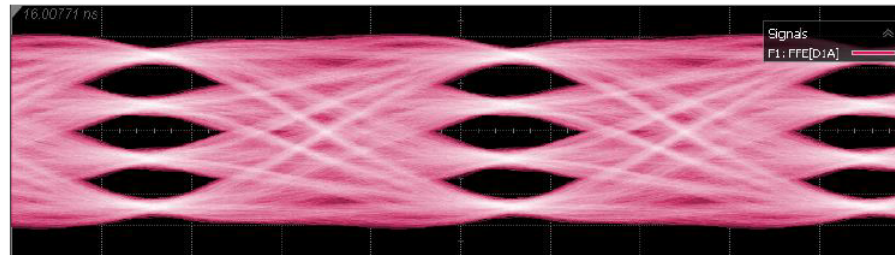
- ❖ Measurement results of instrument-grade TX of 212.5Gbps at TP0d reported by John Calvin (calvin_3dj_elec_01a_240104) passes our proposal for KR (200G/L) spec, without much margin to cover PVT variations. It fails KR (100G/L) spec, if simply scaled from P802.3ck spec.

	Measured result (212.5Gbps)	KR (100G/L) 802.3ck spec	KR (200G/L) our proposal
Jrmsms	19.6 mUI	23 mUI	25.7 mUI
J3u03	112 mUI	106 mUI	138 mUI
J3u	133 mUI	115 mUI	150 mUI

calvin_3dj_elec_01a_240104, slide 5

TP0d Instrument direct setup

Measurement	To LD	To L1	To L2	To L3
J3u (All)	133 mUI			
J3u (R03/F30)	112 mUI			
From L3	109 mUI	115 mUI	132 mUI	---
From L2	115 mUI	131 mUI	---	145 mUI
From L1	142 mUI	---	139 mUI	114 mUI
From L0	---	145 mUI	117 mUI	115 mUI
Jrms (All)	19.6 mUI			
Jrms (R03/F30)	17.0 mUI			
From L3	16.5 mUI	17.5 mUI	20.4 mUI	---
From L2	17.6 mUI	19.8 mUI	---	22.3 mUI
From L1	21.4 mUI	---	20.7 mUI	17.4 mUI
From L0	---	22.2 mUI	17.9 mUI	17.5 mUI
EOJ (All)	19 mUI			
EOJ (R03/F30)	6 mUI			
From L3	1 mUI	9 mUI	14 mUI	---
From L2	4 mUI	1 mUI	---	19 mUI
From L1	2 mUI	---	12 mUI	0 mUI
From L0	---	18 mUI	11 mUI	6 mUI



TP0d Conditions:

80GHz 4BT

Direct Measurement from outputs of generator's remote head

Explicit clocking (no CDR, results in slightly higher Jitter values)

Instrument grade TX with 700mV SE/ 1.4V Differential

7 TAP FFE (3 Pre)

Alternative Option: CDR bandwidth

- ❖ If linearly scaling jitter parameters are preferred, an alternative option to alleviate implementation challenge due to jitter is to raise CDR bandwidth.
 - TX jitter measurement is lower with higher-bandwidth CDR.
 - TX implementation cost is relaxed by raising CDR bandwidth from 4MHz to a higher value such as 8MHz to keep the same CDR bandwidth to Baud rate ratio.

- ❖ This is a viable option.
 - CDR loop SNR is maintained by keeping CDR bandwidth to Baud rate Ratio.
 - CDR circuit can run faster than previous generations.
 - 4MHz is quite low compared to other recent standards:
 - ✓ PCIe Gen5 (32Gbps, NRZ) specifies CDR bandwidth of 20MHz.
 - ✓ PCIe Gen6 (64Gbps, PAM4) specifies CDR bandwidth of 10MHz.

Summary

- ❖ Propose σ_{RJ} and A_{DD} to be 15mUI and 20mUI for 200G KR/CR COM analysis. The complete proposal with TX jitter parameters is in page 12.
 - TX J_{RMS} spec is 25.7mUI. TX J_{3u} spec is 163mUI for CR, and 150mUI for KR.
 - COM difference between 10mUI and 15mUI σ_{RJ} is only 0.18dB COM.

- ❖ An alternative option to effectively overcome TX jitter challenge is to raise CDR bandwidth to 8 MHz. It keeps jitter parameters the same in the standard and reduces residual jitters after tracking.

Back up

COM parameters for KR/CR channels (package A w/o MLSE)

Table 93A-1 parameters			
Parameter	Setting	Units	Information
f_b	106.25	GBd	
f_min	0.05	GHz	
Delta_f	0.01	GHz	
C_d	[0.4e-4 0.9e-4 1.1e-4 0.4e-4 0.9e-4 1.1e-4]	nF	[TX RX]
L_s	[0.13 0.15 0.14; 0.13 0.15 0.14]	nH	[TX RX]
C_b	[0.3e-4 0.3e-4]	nF	[TX RX]
R_0	50	Ohm	
R_d	[45 45]	Ohm	[TX RX]
A_v	0.386	V	vp/vf=
A_fe	0.386	V	vp/vf=
A_ne	0.6	V	
L	4		
M	32		
filter and Eq			
f_r	0.58	*fb	
c(0)	0.55		min
c(-1)	[-0.3;0.05;0]		[min;step;max]
c(-2)	[0.05;0.1]		[min;step;max]
c(-3)	0		[min;step;max]
c(-4)	0		[min;step;max]
c(1)	0		[min;step;max]
N_b	1	UI	
b_max(1)	0.75		As/dffe1
b_max(2..N_b)	0.3		As/dfe2..N_b
b_min(1)	0		As/dffe1
b_min(2..N_b)	-0.15	S	As/dfe2..N_b
g_DC	[-15;1;-3]	dB	[min;step;max]
f_z	25.16	GHz	
f_p1	40.00	GHz	
f_p2	56.00	GHz	
g_DC_HP	[-5;1;0]		[min;step;max]
f_HP_PZ	1.328125	GHz	

I/O control		
DIAGNOSTICS	1	logical
DISPLAY_WINDOW	0	logical
CSV_REPORT	1	logical
RESULT_DIR	.\results\CACR_set1_(date)\	
SAVE FIGURES	0	logical
Port Order	[1 3 2 4]	
RUNTAG	KR_set1_eval_	
COM_CONTRIBUTION	1	logical
TDR and ERL options		
TDR	1	logical
ERL	1	logical
ERL_ONLY	0	ns
TR_TDR	0.01	
N	4000	logical
TDR Butterworth	1	
beta_x	0	
rho_x	0.618	
TDR_W_TXPKG	0	UI
N_bx	20	
fixture delay time	[0 0]	
Tukey_Window	1	
Noise, jitter		
sigma_RJ	0.01	UI
A_DD	0.02	V ² /GHz
eta_0	4.00E-09	dB
SNR_TX	33	
R_LM	0.95	
benartsj_3df_01a_2211		
mli_3df_02_220316		

Table 93A-3 parameters			
Parameter	Setting	Units	Information
package_tl_gamma0_a1_a2	[5e-4 8.9e-4 2e-4]		
package_tl_tau	0.006141	ns/mm	
package_Z_c	[87.5 87.5 ; 92.5 92.5 ; 100 100 ; 100 100]	Ohm	
z_p select	[2]		[test cases to run]
z_p (TX)	[12 33 ; 1.8 1.8 ; 0.0 ; 0.0]	mm	[test cases]
z_p (NEXT)	[12 31 ; 1.8 1.8 ; 0.0 ; 0.0]	mm	[test cases]
z_p (FEXT)	[12 33 ; 1.8 1.8 ; 0.0 ; 0.0]	mm	[test cases]
z_p (RX)	[12 31 ; 1.8 1.8 ; 0.0 ; 0.0]	mm	[test cases]
C_p	[0.4e-4 0.4e-4]	nF	[TX RX]
Filter: Rx FFE			
ffe_pre_tap_len	6	UI	
ffe_post_tap_len	45	UI	
ffe_tap_step_size	0		
ffe_main_cursor_min	0.7		
ffe_pre_tap1_max	0.7		
ffe_post_tap1_max	0.7		
ffe_tapn_max	0.7		
Operational			
ERL Pass threshold	10	dB	
COM Pass threshold	3	dB	
DER_0	1.00E-04		
T_r	0.00400	ns	
FORCE_TR	1	logical	
PMD_type	C2C		
EW	1		
MLSE	0	logical	
ts_anchor	1		
sample_adjustment	[- 8 8]		
Local Search	2		

SAVE_CONFIG2MAT		
0		
Receiver testing		
RX_CALIBRATION	0	logical
Sigma BBN step	5.00E-03	V
ICN parameters		
f_v	0.278	Fb
f_f	0.278	Fb
f_n	0.278	Fb
f_2	61.625	GHz
A_ft	0.450	V
A_nt	0.450	V
Parameter Setting		
board_tl_gamma0_a1_a2	6.44084e-4 3.6036e-0	1.4 db/in @ 53.125G
board_tl_tau	5.790E-03	ns/mm
board_Z_c	100	Ohm
z_bp (TX)	32	mm
z_bp (NEXT)	32	mm
z_bp (FEXT)	32	mm
z_bp (RX)	32	mm
C_0	[0.2e-4 0]	nF
C_1	[0.2e-4 0]	nF
Include PCB	0	logical
Seletions (rectangle, gaussian, dual rayleigh, triangle)		
Histogram_Window_Weight	gaussian	selection
Or	0.02	UI
Floating Tap Control		
N_bg	0	0 1 2 or 3 groups
N_bf	4	taps per group
N_f	80	UI span for floating taps
bmaxg	0.2	max DFE value for floating taps
B_float_RSS_MAX	0.1	rss tail tap limit
N_tail_start	25	(UI) start of tail taps limit

COM parameters for KR/CR channels (package B w/ MLSE)

Table 93A-1 parameters			
Parameter	Setting	Units	Information
f_b	106.25	Gbd	
f_min	0.05	GHz	
Delta_f	0.01	GHz	
C_d	[0.4e-4 0.9e-4 1.1e-4;0.4e-4 0.9e-4 1.1e-4]	nF	[TX RX]
L_s	[0.13 0.15 0.14; 0.13 0.15 0.14]	nH	[TX RX]
C_b	[0.3e-4 0.3e-4]	nF	[TX RX]
R_0	50	Ohm	
R_d	[45 45]	Ohm	[TX RX]
A_v	0.386	V	vp/vf=
A_fe	0.386	V	vp/vf=
A_ne	0.6	V	
L	4		
M	32		
filter and Eq			
f_r	0.58	*fb	
c(0)	0.55		min
c(-1)	[-0.3;0.05;0]		[min:step:max]
c(-2)	[0.05;0.1]		[min:step:max]
c(-3)	0		[min:step:max]
c(-4)	0		[min:step:max]
c(1)	0		[min:step:max]
N_b	1	UI	
b_max(1)	0.75		As/dffe1
b_max(2..N_b)	0.3		As/dfe2..N_b
b_min(1)	0		As/dffe1
b_min(2..N_b)	-0.15	S	As/dfe2..N_b
g_DC	[-15;1;-3]	dB	[min:step:max]
f_z	25.16	GHz	
f_p1	40.00	GHz	
f_p2	56.00	GHz	
g_DC_HP	[-5;1;0]		[min:step:max]
f_HP_PZ	1.328125	GHz	

I/O control		
DIAGNOSTICS	1	logical
DISPLAY_WINDOW	0	logical
CSV_REPORT	1	logical
RESULT_DIR	.\results\CACR_set1_(date)\	
SAVE_FIGURES	0	logical
Port Order	[1 3 2 4]	
RUNTAG	KR_set1_eval_	
COM_CONTRIBUTION	1	logical
TDR and ERL options		
TDR	1	logical
ERL	1	logical
ERL_ONLY	0	ns
TR_TDR	0.01	
N	4000	logical
TDR_Butterworth	1	
beta_x	0	
rho_x	0.618	
TDR_W_TXPKG	0	UI
N_bx	20	
fixture delay time	[0 0]	
Tukey_Window	1	
Noise, jitter		
sigma_RJ	0.01	UI
A_DD	0.02	V ² /GHz
eta_0	4.00E-09	dB
SNR_TX	33	
R_LM	0.95	
benartsi_3df_01a_2211		
mli_3df_02_220316		

Table 93A-3 parameters			
Parameter	Setting	Units	Information
package_tl_gamma0_a1_a2	[5e-4 6.5e-4 3e-4]		
package_tl_tau	0.006141	ns/mm	
package_Z_c	[92 92; 70 70; 80 80; 100 100]	Ohm	
z_p select	[4]		[test cases to run]
z_p (TX)	[8 24 30 45; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
z_p (NEXT)	[8 22 28 43; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
z_p (FEXT)	[8 24 30 45; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
z_p (RX)	[8 22 28 43; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
C_p	[0.4e-4 0.4e-4]	nF	[TX RX]
Filter: Rx FFE			
ffe_pre_tap_len	6	UI	
ffe_post_tap_len	45	UI	
ffe_tap_step_size	0		
ffe_main_cursor_min	0.7		
ffe_pre_tap1_max	0.7		
ffe_post_tap1_max	0.7		
ffe_tapn_max	0.7		
Operational			
ERL Pass threshold	10	dB	
COM Pass threshold	3	db	
DER_0	1.00E-04		
T_r	0.00400	ns	
FORCE_TR	1	logical	
PMD_type	C2C		
EW	1		
MLSE	1	logical	
ts_anchor	1		
sample_adjustment	[-8 8]		
Local Search	2		

SAVE_CONFIG2MAT	0	
Receiver testing		
RX_CALIBRATION	0	logical
Sigma BBN step	5.00E-03	V
ICN parameters		
f_v	0.278	Fb
f_f	0.278	Fb
f_n	0.278	Fb
f_2	61.625	GHz
A_ft	0.450	V
A_nt	0.450	V
Parameter Setting		
board_tl_gamma0_a1_a2	[6.44084e-4 3.6036e-04]	1.4 db/in @ 53.125G
board_tl_tau	5.790E-03	ns/mm
board_Z_c	100	Ohm
z_bp (TX)	32	mm
z_bp (NEXT)	32	mm
z_bp (FEXT)	32	mm
z_bp (RX)	32	mm
C_0	[0.2e-4 0]	nF
C_1	[0.2e-4 0]	nF
Include PCB	0	logical
Selections (rectangle, gaussian, dual, rayleigh, triangle)		
Histogram_Window_Weight	gaussian	selection
Qr	0.02	UI
Floating Tap Control		
N_bg	0	0 1 2 or 3 groups
N_bf	4	taps per group
N_f	80	UI span for floating taps
bmaxg	0.2	max DFE value for floating taps
B_float_RSS_MAX	0.1	rss tail tap limit
N_tail_start	25	(UI) start of tail taps limit

Results of sweep 1 with $A_v=0.413V$, $R_d=45ohm$

- ❖ Average COM difference between $\sigma_{RJ}=10mUI$ and $15mUI$
 - 0.20028dB with PKG A without MLSE
 - 0.17787dB with PKG B with MLSE

❖ Detail results with PKG A (without MLSE)

Results with $A_v=0.413V$ for PKG A without MLSE

σ_{RJ}	A_DD		CH01	CH02	CH03	CH04	CH05	CH06	CH07	CH08	CH09	CH10	CH11	CH12	CH13	CH14	CH15	CH16	CH17	CH18	CH19	CH20	CH21	CH22	CH23	CH24	CH25	CH26	AVG
10mUI	20mUI	COM	4.2152	3.8685	3.8791	5.1968	3.9857	3.9445	3.7551	4.7486	4.3688	3.9172	3.8493	5.9451	4.1802	3.8764	3.4526	4.6866	4.0408	3.8493	2.9504	3.2989	2.0915	2.7217	2.6507	2.6154	2.0915	1.9927	3.698946
15mUI	20mUI	COM	3.986	3.6913	3.6797	4.9196	3.7819	3.7284	3.5697	4.5046	4.1507	3.715	3.6487	5.6485	3.9582	3.6619	3.2609	4.4515	3.8223	3.6355	2.7813	3.1229	1.9491	2.5569	2.4872	2.4526	1.9491	1.8518	3.498665
		dCOM	-0.2292	-0.1772	-0.1994	-0.2772	-0.2038	-0.2161	-0.1854	-0.244	-0.2181	-0.2022	-0.2006	-0.2966	-0.222	-0.2145	-0.1917	-0.2351	-0.2185	-0.2138	-0.1691	-0.176	-0.1424	-0.1648	-0.1635	-0.1628	-0.1424	-0.1409	-0.20028

❖ Detail results with PKG B (with MLSE)

Results with $A_v=0.413V$ for PKG B with MLSE

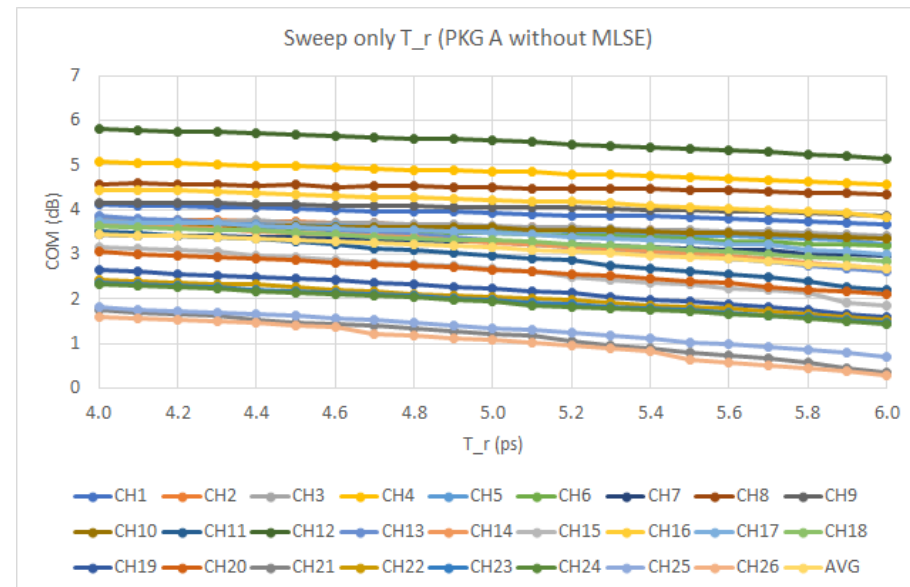
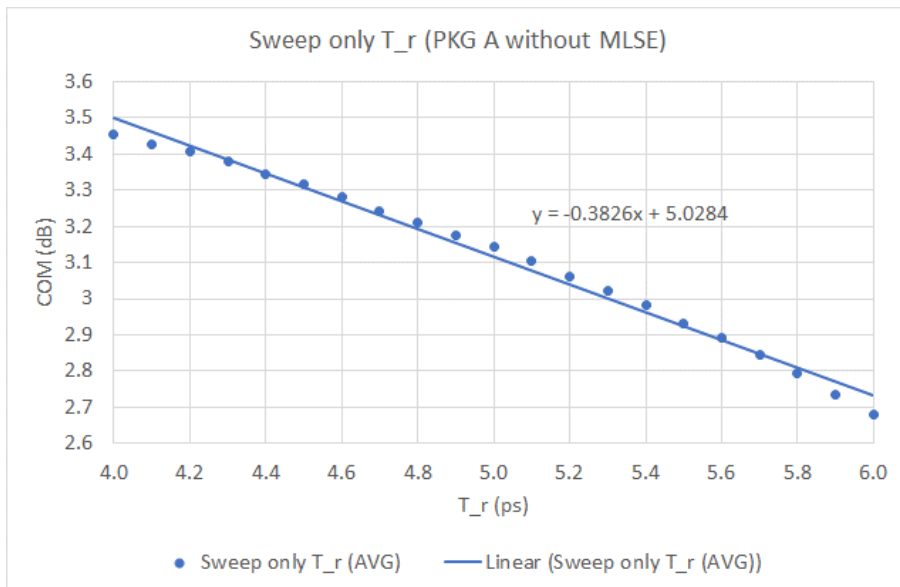
σ_{RJ}	A_DD		CH01	CH02	CH03	CH04	CH05	CH06	CH07	CH08	CH09	CH12	CH16	AVG
10mUI	20mUI	COM	5.1249	4.4556	4.4789	5.8034	4.6207	4.5294	4.3301	5.547	4.9694	6.049	5.0298	4.994382
15mUI	20mUI	COM	4.9044	4.2941	4.317	5.5984	4.4577	4.3728	4.1777	5.3544	4.801	5.8451	4.859	4.816509
		dCOM	-0.2205	-0.1615	-0.1619	-0.205	-0.163	-0.1566	-0.1524	-0.1926	-0.1684	-0.2039	-0.1708	-0.17787

Additional Sweep Conditions

- ❖ Sweep 3 : Sweep only Tr
 - Sweep Tr from 4.0ps to 6.0ps in 0.1ps step

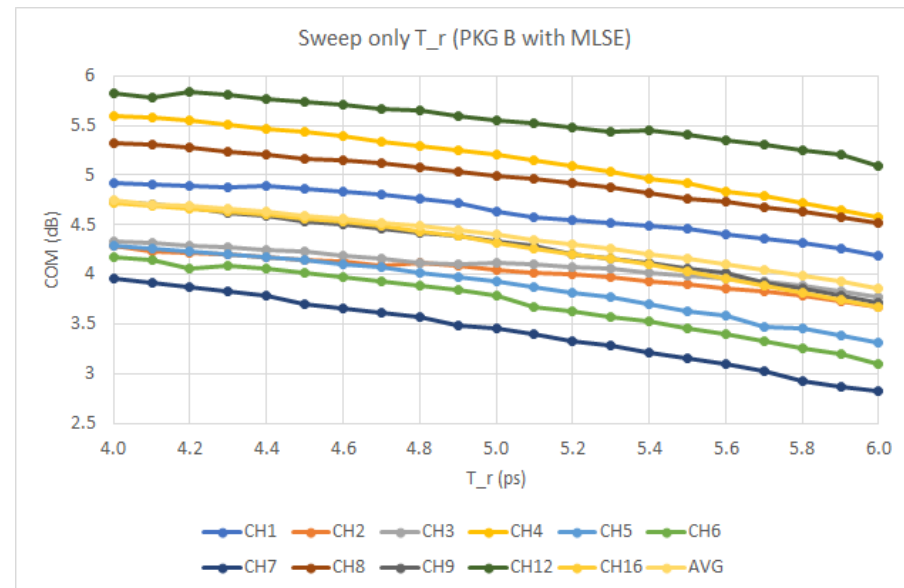
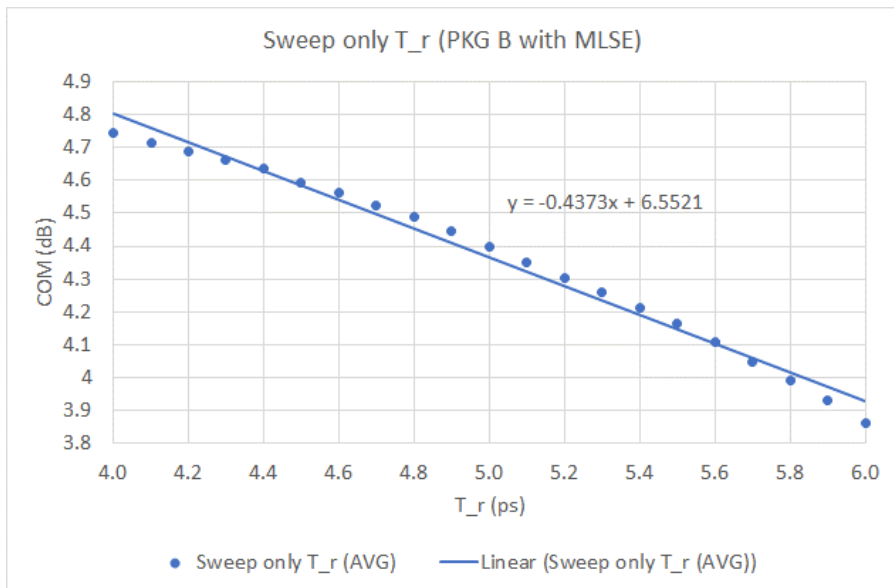
Results of Sweep 3 w/ PKG A (without MLSE)

❖ Sweeping only Tr degrades COM rather significantly



Results of Sweep 3 w/ PKG B (with MLSE)

❖ Sweeping only Tr degrades COM rather significantly



Summary of Results of Sweep 3

- ❖ More study is needed to determine Tr for 200G KR/CR COM analysis.
 - Changing Tr from 4ps to 4.6ps has a minor impact of 0.18dB COM.
 - Implementing Tr=4.6ps over PVT might still be a challenge.

Sweep only T_r						
T_r	COM (dB)			dCOM		
ps	PKG A	PKG B	AVG	PKG A	PKG B	AVG
	w/o MLSE	w/ MLSE		w/o MLSE	w/ MLSE	
	26 CHs	11 CHs		26 CHs	11 CHs	
4.0	3.452912	4.742791	4.097851	0	0	0
4.1	3.426023	4.712482	4.069252	-0.02689	-0.03031	-0.0286
4.2	3.405231	4.688118	4.046674	-0.04768	-0.05467	-0.05118
4.3	3.377885	4.6612	4.019542	-0.07503	-0.08159	-0.07831
4.4	3.344554	4.633909	3.989231	-0.10836	-0.10888	-0.10862
4.5	3.315277	4.592536	3.953907	-0.13763	-0.15025	-0.14394
4.6	3.281058	4.562464	3.921761	-0.17185	-0.18033	-0.17609
4.7	3.243115	4.525191	3.884153	-0.2098	-0.2176	-0.2137
4.8	3.210946	4.486718	3.848832	-0.24197	-0.25607	-0.24902
4.9	3.176635	4.444527	3.810581	-0.27628	-0.29826	-0.28727
5.0	3.145323	4.399191	3.772257	-0.30759	-0.3436	-0.32559
5.1	3.104046	4.349836	3.726941	-0.34887	-0.39295	-0.37091
5.2	3.061775	4.302673	3.682224	-0.39114	-0.44012	-0.41563
5.3	3.023071	4.258155	3.640613	-0.42984	-0.48464	-0.45724
5.4	2.981498	4.211536	3.596517	-0.47141	-0.53125	-0.50133
5.5	2.932187	4.163745	3.547966	-0.52072	-0.57905	-0.54988
5.6	2.890078	4.109364	3.499721	-0.56283	-0.63343	-0.59813
5.7	2.84614	4.048673	3.447406	-0.60677	-0.69412	-0.65045
5.8	2.791844	3.991636	3.39174	-0.66107	-0.75115	-0.70611
5.9	2.736114	3.931245	3.33368	-0.7168	-0.81155	-0.76417
6.0	2.677969	3.860518	3.269244	-0.77494	-0.88227	-0.82861

COM parameters for C2M channels (No MLSE, host package A, module 8mm package)

Table 93A-1 parameters			
Parameter	Setting	Units	Information
f_b	106.25	Gbd	
f_min	0.05	GHz	
Delta_f	0.01	GHz	
C_d	[0.4e-4 0.9e-4 1.1e-4;0.4e-4 0.9e-4 1.1e-4]	nF	[TX RX]
L_s	[0.13 0.15 0.14; 0.13 0.15 0.14]	nH	[TX RX]
C_b	[0.3e-4 0.3e-4]	nF	[TX RX]
R_0	50	Ohm	
R_d	[45 45]	Ohm	[TX RX]
A_v	0.386	V	vp/vf=
A_fe	0.386	V	vp/vf=
A_ne	0.6	V	
L	4		
M	32		
filter and Eq			
f_r	0.58	*fb	
c(0)	0.55		min
c(-1)	[-0.3;0.05;0]		[min:step:max]
c(-2)	[0.05;0.1]		[min:step:max]
c(-3)	0		[min:step:max]
c(-4)	0		[min:step:max]
c(1)	0		[min:step:max]
N_b	1	UI	
b_max(1)	0.75		As/dffe1
b_max(2..N_b)	0.3		As/dfe2..N_b
b_min(1)	0		As/dffe1
b_min(2..N_b)	-0.15	S	As/dfe2..N_b
g_DC	[-15;1;-3]	dB	[min:step:max]
f_z	25.16	GHz	
f_p1	40.00	GHz	
f_p2	56.00	GHz	
g_DC_HP	[-5;1;0]		[min:step:max]
f_HP_PZ	1.328125	GHz	

I/O control		
DIAGNOSTICS	1	logical
DISPLAY_WINDOW	0	logical
CSV_REPORT	1	logical
RESULT_DIR	.\results\C2M_(date)\	
SAVE_FIGURES	0	logical
Port Order	[1 3 2 4]	
RUNTAG	C2M TP1a_COM_model	
COM_CONTRIBUTION	1	logical
TDR and ERL options		
TDR	1	logical
ERL	1	logical
ERL_ONLY	0	ns
TR_TDR	0.01	
N	3000	logical
TDR_Butterworth	1	
beta_x	0	
rho_x	0.618	
TDR_W_TXPKG	0	UI
N_bx	0	
fixture delay time	[0 0]	
Tukey_Window	1	
Noise, jitter		
sigma_RJ	0.01	UI
A_DD	0.02	UI
eta_0	6.00E-09	V ² /GHz
SNR_TX	33	dB
R_LM	0.95	

Table 93A-3 parameters			
Parameter	Setting	Units	Information
package_tl_gammaa0_a1_a2	[5e-4 8.9e-4 2e-4]		
package_tl_tau	0.006141	ns/mm	
package_Z_c	[87.5 87.5 ; 92.5 92.5; 100 100; 100 100]	Ohm	
z_p select	[2]		[test cases to run]
z_p (TX)	[12 33 ; 1.8 1.8 ; 0 0 ; 0 0]	mm	[test cases]
z_p (NEXT)	[8 8 ; 0 0 ; 0 0 ; 0 0]	mm	[test cases]
z_p (FEXT)	[12 33 ; 1.8 1.8 ; 0 0 ; 0 0]	mm	[test cases]
z_p (RX)	[8 8 ; 0 0 ; 0 0 ; 0 0]	mm	[test cases]
C_p	[0.4e-4 0.4e-4]	nF	[TX RX]
Filter: Rx FFE			
ffe_pre_tap_len	6	UI	
ffe_post_tap_len	25	UI	
ffe_tap_step_size	0		
ffe_main_cursor_min	0.7		
ffe_pre_tap1_max	0.7		
ffe_post_tap1_max	0.7		
ffe_tapn_max	0.7		
Operational			
ERL Pass threshold	10	dB	
COM Pass threshold	3	db	
VEC Pass threshold	10	db	
DER_0	2.00E-05		
T_r	4.00E-03	ns	
FORCE_TR	1	logical	
Min_VEO_Test	0	mV	
PMD_type	C2C		
EH_min	5	Value	
EH_max	1000	Value	
T_O	50	mUI	
samples_for_C2M	100	samples/UI	
ts_anchor	1		
sample_adjustment	[- 8 12]		
EW	1		
MLSE	0		
Local Search	2		

SAVE_CONFIG2MAT		
	0	
Receiver testing		
RX_CALIBRATION	0	logical
Sigma BBN step	5.00E-03	V
ICN parameters		
f_v	0.588	Fb
f_f	0.278	Fb
f_n	0.278	Fb
f_2	61.625	GHz
A_ft	0.450	V
A_nt	0.450	V
Parameter Setting		
board_tl_gammaa0_a1_a2	[0 6.44084e-4 3.6036e-05]	1.4 db/in @ 53.125G
board_tl_tau	5.790E-03	ns/mm
board_Z_c	100	Ohm
z_bp (TX)	32	mm
z_bp (NEXT)	32	mm
z_bp (FEXT)	32	mm
z_bp (RX)	32	mm
C_0	[0.2e-4 0]	nF
C_1	[0.2e-4 0]	nF
Include PCB	0	logical
Seletions (rectangle, gaussian, dual_rayleigh, triangle)		
Histogram_Window_Weight	gaussian	selection
Qr	0.02	UI
Floating Tap Control		
N_bg	0	0 1 2 or 3 groups
N_bf	4	taps per group
N_f	80	UI span for floating taps
bmaxg	0.2	max DFE value for floating taps
B_float_RSS_MAX	0.1	rss tail tap limit
N_tail_start	16	(UI) start of tail taps limit
benartsi_3df_01a_2211		
mli_3df_02_220316		
ran_3dj_elec_02_230622		

COM parameters for C2M channels (No MLSE, host package B, module 8mm package)

Table 93A-1 parameters			
Parameter	Setting	Units	Information
f_b	106.25	Gbd	
f_min	0.05	GHz	
Delta_f	0.01	GHz	
C_d	[0.4e-4 0.9e-4 1.1e-4;0.4e-4 0.9e-4 1.1e-4]	nF	[TX RX]
L_s	[0.13 0.15 0.14; 0.13 0.15 0.14]	nH	[TX RX]
C_b	[0.3e-4 0.3e-4]	nF	[TX RX]
R_o	50	Ohm	
R_d	[45 45]	Ohm	[TX RX]
A_v	0.386	V	vp/vf=
A_fe	0.386	V	vp/vf=
A_ne	0.6	V	
L	4		
M	32		
filter and Eq			
f_r	0.58	*fb	
c(0)	0.55		min
c(-1)	[-0.3;0.05;0]		[min:step:max]
c(-2)	[0.05;0.1]		[min:step:max]
c(-3)	0		[min:step:max]
c(-4)	0		[min:step:max]
c(1)	0		[min:step:max]
N_b	1	UI	
b_max(1)	0.75		As/dffe1
b_max(2..N_b)	0.3		As/dfe2..N_b
b_min(1)	0		As/dffe1
b_min(2..N_b)	-0.15	S	As/dfe2..N_b
g_DC	[-15;1;-3]	dB	[min:step:max]
f_z	25.16	GHz	
f_p1	40.00	GHz	
f_p2	56.00	GHz	
g_DC_HP	[-5;1;0]		[min:step:max]
f_HP_PZ	1.328125	GHz	

I/O control		
DIAGNOSTICS	1	logical
DISPLAY_WINDOW	0	logical
CSV_REPORT	1	logical
RESULT_DIR	.\results\C2M_(date)\	
SAVE_FIGURES	0	logical
Port Order	[1 3 2 4]	
RUNTAG	C2M TP1a_COM_model	
COM_CONTRIBUTION	1	logical
TDR and ERL options		
TDR	1	logical
ERL	1	logical
ERL_ONLY	0	ns
TR_TDR	0.01	
N	3000	logical
TDR_Butterworth	1	
beta_x	0	
rho_x	0.618	
TDR_W_TXPKG	0	UI
N_bx	0	
fixture delay time	[0 0]	
Tukey_Window	1	
Noise, jitter		
sigma_RJ	0.01	UI
A_DD	0.02	UI
eta_0	6.00E-09	V ² /GHz
SNR_TX	33	dB
R_LM	0.95	

Table 93A-3 parameters			
Parameter	Setting	Units	Information
package_tl_gammaa0_a1_a2	[5e-4 6.5e-4 3e-4]		
package_tl_tau	0.006141	ns/mm	
package_Z_c	[92 92; 70 70; 80 80; 100 100]	Ohm	
z_p select	[4]		[test cases to run]
z_p (TX)	[8 24 30 45; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
z_p (NEXT)	[8 8 8 8; 0 0 0 0; 0 0 0 0; 0 0 0 0]	mm	[test cases]
z_p (FEXT)	[8 24 30 45; 1 1 1 1; 1 1 1 1; 0.5 0.5 0.5 0.5]	mm	[test cases]
z_p (RX)	[8 8 8 8; 0 0 0 0; 0 0 0 0; 0 0 0 0]	mm	[test cases]
C_p	[0.4e-4 0.4e-4]	nF	[TX RX]
Filter: Rx FFE			
ffe_pre_tap_len	6	UI	
ffe_post_tap_len	25	UI	
ffe_tap_step_size	0		
ffe_main_cursor_min	0.7		
ffe_pre_tap1_max	0.7		
ffe_post_tap1_max	0.7		
ffe_tapn_max	0.7		
Operational			
ERL Pass threshold	10	dB	
COM Pass threshold	3	dB	
VEC Pass threshold	10	dB	
DER_0	2.00E-05		
T_r	4.00E-03	ns	
FORCE_TR	1	logical	
Min_VEO_Test	0	mV	
PMD_type	C2C		
EH_min	5	Value	
EH_max	1000	Value	
T_o	50	mUI	
samples_for_C2M	100	samples/UI	
ts_anchor	1		
sample_adjustment	[- 8 12]		
EW	1		
MLSE	0		
Local Search	2		

SAVE_CONFIG2MAT		
	0	
Receiver testing		
RX_CALIBRATION	0	logical
Sigma BBN step	5.00E-03	V
ICN parameters		
f_v	0.588	Fb
f_f	0.278	Fb
f_n	0.278	Fb
f_2	61.625	GHz
A_ft	0.450	V
A_nt	0.450	V
Parameter Setting		
board_tl_gammaa0_a1_a2	[0 6.44084e-4 3.6036e-05]	1.4 db/in @ 53.125G
board_tl_tau	5.790E-03	ns/mm
board_Z_c	100	Ohm
z_bp (TX)	32	mm
z_bp (NEXT)	32	mm
z_bp (FEXT)	32	mm
z_bp (RX)	32	mm
C_0	[0.2e-4 0]	nF
C_1	[0.2e-4 0]	nF
Include PCB	0	logical
Seletions (rectangle, gaussian, dual_rayleigh, triangle)		
Histogram_Window_Weigh	gaussian	selection
Qr	0.02	UI
Floating Tap Control		
N_bg	0	0 1 2 or 3 groups
N_bf	4	taps per group
N_f	80	UI span for floating taps
bmaxg	0.2	max DFE value for floating taps
B_float_RSS_MAX	0.1	rss tail tap limit
N_tail_start	16	(UI) start of tail taps limit
benartsi_3df_01a_2211		
mli_3df_02_220316		
ran_3dj_elec_02_230622		

Results of Sweep 1&2 for C2M channels

- ✓ Used 26 C2M channels selected in lusted_3dj_02a_2309, slide 12
- ❖ Sweeping only σ_{RJ} degrades COM only moderately
- ❖ Sweeping σ_{RJ} and A_{DD} results in larger COM degradation

