ILT Send Training Timer

Comment # 269

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Background and problem statement

- The training control state diagram defines the operation of ILT for AUI components and PMDs.

- For avoiding unexpectedly long time consumed by ILT, training_timer was proposed to start in TRAIN_LOCAL state in ran_3dj_02a_2505.

- However, a risk of being trapped in SEND_TRAINING state exists if either local_tf_lock or remote_tf_lock is false for long. Need to resolve this!
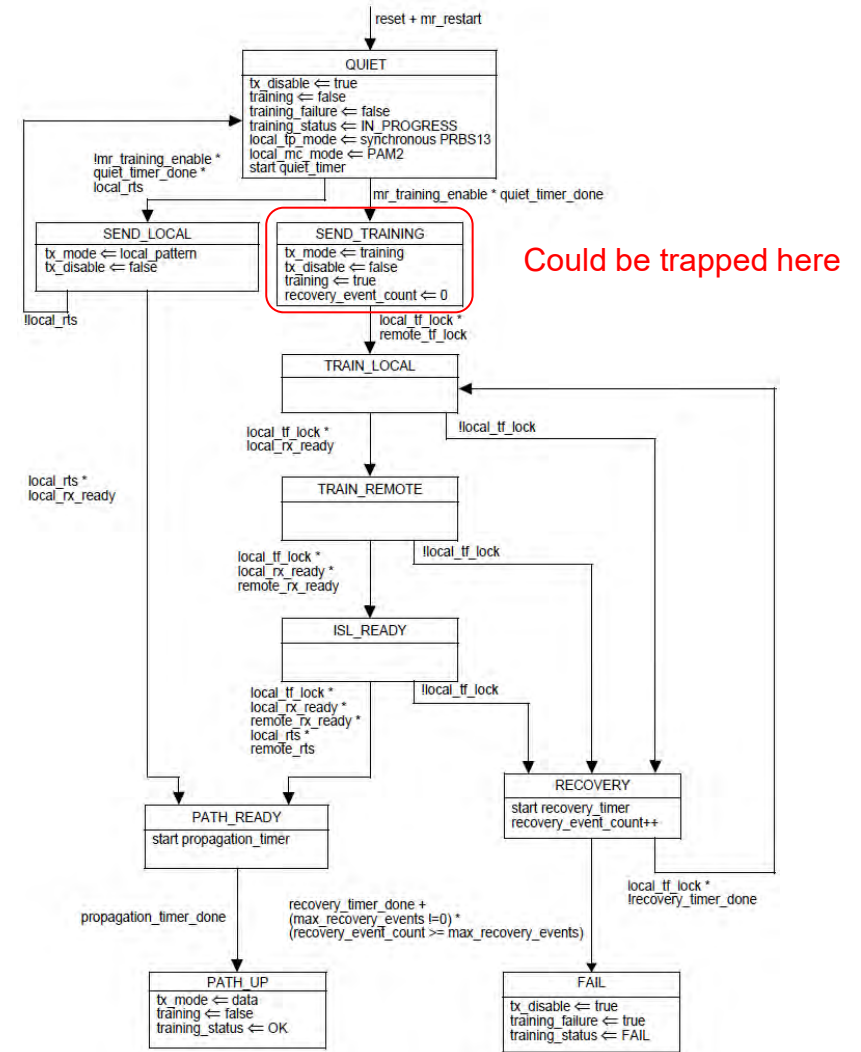

Figure 178B–8—Training control state diagram

Proposed change

178B.14.3.3 Timers

quiet_timer

This timer is started when the training control state diagram on a lane enters the QUIET state (see Figure 178B–8). The terminal count of this timer is between 100 ms and 200 ms.

propagation_timer

This timer is started when the training control state diagram on a lane enters the PATH_READY state (see Figure 178B–8). The terminal count of this timer is between 100 ms and 200 ms.

recovery_timer

This timer is started when the training control state diagram on a lane enters the RECOVERY state (see Figure 178B–8). The terminal count of this timer is between 20 ms and 30 ms.

send_training_timer

This timer is started when the training control state diagram on a lane enters the SEND_TRAINING state (see Figure 178B-8). The terminal count of this timer is between 100 ms and 200 ms.
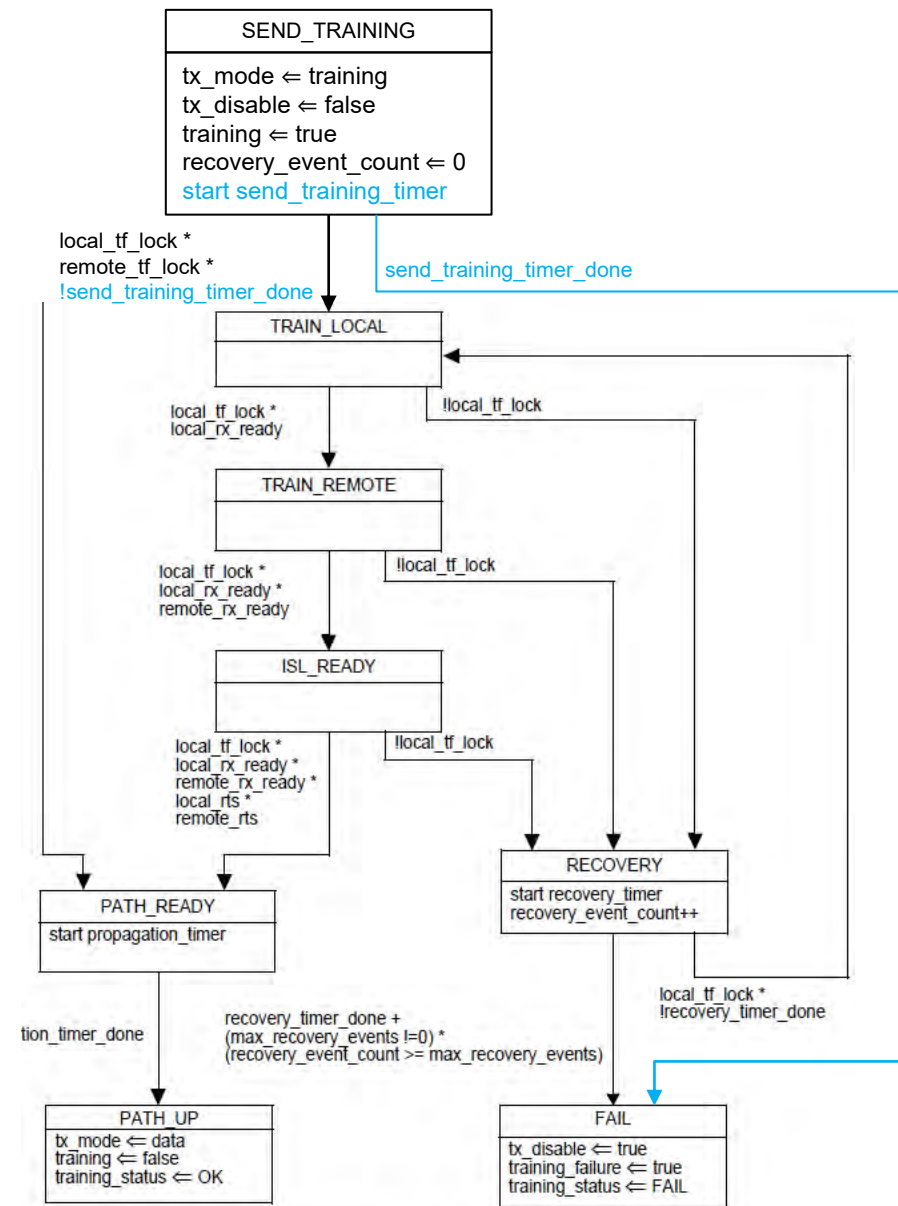

Figure 178B–8—Training control state diagram

Summary

- To avoid being trapped in the SEND_TRAINING state for long, send_training_timer is proposed to use.

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Questions?

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