

Response to 802.3dj D2.3 Comment #44 Test Vectors for Clause 186 FEC (800GBASE-ER1, ER1-20)

Mike A. Sluyski, Gary Nicholl (Cisco Systems)
Haim Ben Amram, Aharon Roni El-Bahar (Retym)

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Supporters:

Tom Williams – Cisco Systems

Jean-Michel Caia – CICT

802.3dj D2.3 Comment #44

CI 186A SC 186A

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Brown, Matt

Alphawave Semi

Comment Type TR

Comment Status X

Draft 2.2 comment #152 requested that test vectors be incorporated into the draft. The response pointed out that relevant test vectors are provided publicly by OIF but need some exceptions. During comment resolution, one participant offered to provide appropriate text vectors.

Suggested Remedy

Provide text vectors for Clause 186 FEC or reference the OIF test vectors noting exceptions.

Proposed Response

Response Status O

1 x 800GE Test Vectors

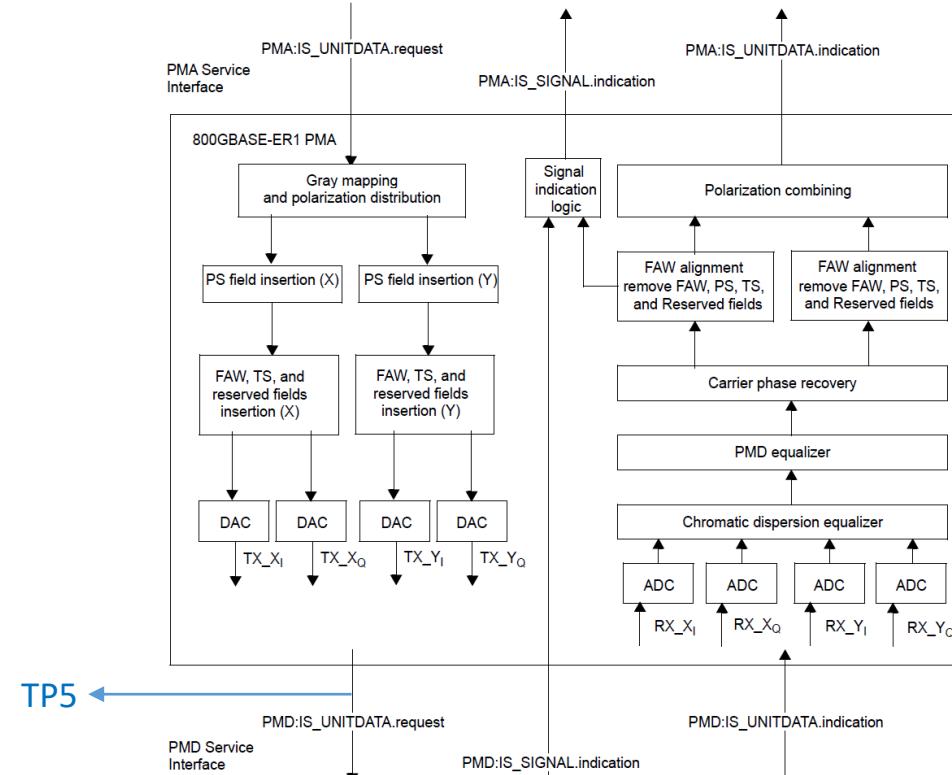
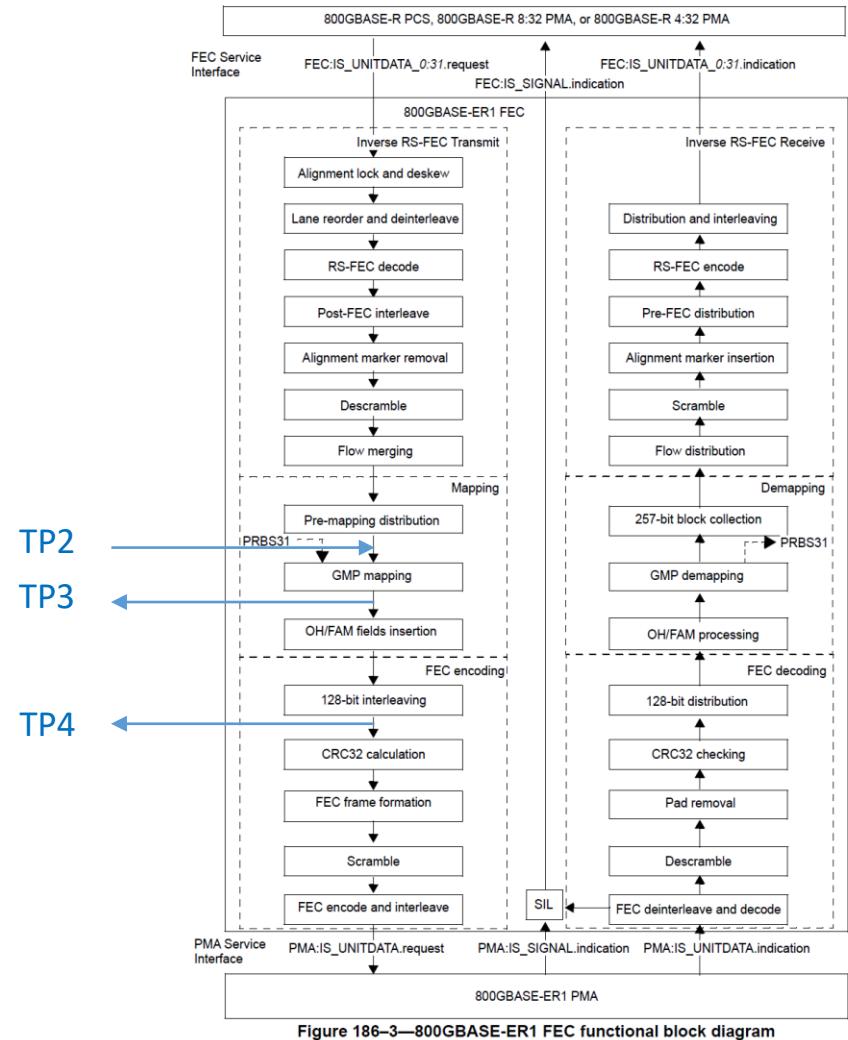
- Payload bits are a specific test pattern of Ethernet packets encoded and transcoded per IEEE 802.3 Clauses 172.2.4.1 to 172.2.4.4 for 800GBASE-R.
- Idles have been added between packets to generate average IPG=12 and to diversify the cases for the 64B/66B->256B/257B transcoder.
- Output symbols for this test are included.

Note: using a transcoded Idle test pattern (because unscrambled in the case of 800GE client GMP mapping) would not provide sufficient randomization of the data and test coverage: i.e. the GMP payload would be mostly zeros in this case).

1 x 800GE Test Vector - continued

- These test vectors do not support the optional alignment marker location feature – Users of these vectors should assume that FEC status variable: FEC_alignment_marker_location_ability, and/or FEC control variable: FEC_alignment_marker_location_enable are configured to zero. Under these conditions, the value of the AML overhead == all-zeros.
- Test Vector files:
 - Input reference point: TP2
 - Monitoring point: TP3-TP5

800GBASE-ER1/ER1-20 - Test Vector Points.



Recommendation:

- Adopt the test vectors contained in Sluyski_3dj_02_2601 in response to 802.3dj D2.3 Comment #44, as the test vectors for Clause 186A.