

IEEE P802.3dj Task Force – Joint Electrical, Optical and Logic Ad hoc meeting

30 October 2025 Ad hoc Electronic Teleconference Meeting

Unapproved Meeting Minutes, Prepared by Gary Nicholl, Kent Lusted and Mark Nowell

Meeting called to order at 10:00 am (all times ET) by Kent Lusted, who was chairing the meeting.

The chair reminded participants to declare their affiliation in the online meeting tool. Failure to do so would result in expulsion from the meeting.

Presentation #1	Agenda and General Information
Presenter	Kent Lusted
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/agenda_3dj_optx_logic_elec_01a_251030.pdf

The chair asked if there were any modifications to the agenda (See slide #2) – there were was a request to update the title of the presentation from Michael He.

There were no objections to the approval of the agenda, and it was considered approved by unanimous consent.

The chair reviewed meeting decorum (See Slide #3). Chair reminded participants to follow the decorum rules. Chair reminded participants of compliance with the IEEE Code of Ethics required by all participants.

The chair asked if any clarification is needed on IEEE SA Policies. There was none.

Slide #6 - Chair noted that the information regarding the IEEE SA Policies had been sent out via the Task Force reflector , and requested that individuals review the following IEEE SA policies prior to the interim meeting –

- IEEE SA Patent policy
- IEEE SA Copyright Policy
- IEEE SA Participation Policy and IEEE Code of Ethics

Chair asked if anyone needed to review the policies at that time – there were no requests to do so from in-person nor remote attendees.

Chair presented the third slide (See Slide #23) of the IEEE SA Patent Policy slides. Chair did call for Potentially Essential Patents, and no one came forward.

Chair presented the second slide (See Slide #28) of the IEEE SA Copyright Policy slides. Chair noted – “By participating in this activity, you agree to comply with the IEEE Code of Ethics, all applicable laws, and all IEEE policies and procedures including, but not limited to, the IEEE SA Copyright Policy.”

Chair reminded participants of the IEEE Code of Ethics and Conduct. (See slide #31) He noted “All participants in IEEE-SA activities are expected to adhere to the core principles underlying the IEEE Code of Ethics and IEEE Code of Conduct”.

Chair presented the second slide (See Slide #32) of the IEEE SA Participation Policy slides. Chair noted – “Participants in the IEEE-SA “individual process” shall act independently of others, including employers. By participating in standards activities using the “individual process”, you are deemed to accept these requirements; if you are unable to satisfy these requirements then you shall immediately cease any participation.”

The chair reminded participants to declare their affiliation in the online meeting tool.

The chair noted that individuals should fill out IMAT information for attendance.

Presentation #2	Moving toward an ERL CC, DC, and CC specification
Presenter	Rich Mellitz
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/mellitz_3dj_adhoc_01a_251030.pdf

Author noted several typos and would provide an updated version 01a for posting. The material was reviewed and discussed.

Presentation #3	Reconciling the SMF IMDD transmitter quality metric test methods
Presenter	Mark Nowell
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/nowell_3dj_adhoc_01a_251030.pdf

Author noted several typos and would provide an updated version 01a for posting. The material was reviewed and discussed.

Presentation #4	Method for measuring the intrinsic noise and distortion factors NSRrx and ECRx of the ETCC reference receiver
Presenter	Joerg Pfeifle
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/pfeifle_3dj_adhoc_01_251030.pdf

The material was reviewed and discussed.

Presentation #5	Transmitter Spec. with Tx_DP
Presenter	Michael He
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/he_m_3dj_adhoc_01_251030.pdf

The material was reviewed and discussed.

Presentation #6	Acronyms: Block Error Ratio & Path Start Up
Presenter	Kent Lusted
URL	https://www.ieee802.org/3/dj/public/adhoc/electrical/25_1030/lusted_3dj_adhoc_01a_251030.pdf

The material was reviewed and discussed.

Straw Poll #1

For the acronym of “Block Error Ratio”, I prefer to use:

- A) KER
 - B) BLER
 - C) BKER
 - D) don’t care
- (choose one)

Results: A: 6 B: 19 C: 5 D:3

Straw Poll #2

For the acronym used for Annex 178B”, I prefer to use:

- A. APSU, e.g. “Autonomous Path Start Up”
 - B. LTPSU e.g. “LT Path Start Up”
 - C. something else
 - D. don’t care
- (choose one)

Results (all): A: 11 B: 6 C: 9 D: 3

Straw Poll #3

For the acronym used for “Annex 178B”, I would support using APSU, e.g. “Autonomous Path Start Up”

Results: Y: 19 N: 6 A: 6

Chair reminded participants of the presentation request and presentation submission deadline. (see: <https://www.ieee802.org/3/B400G/email/msg01695.html>)

Meeting adjourned at 12:55 pm ET.

Attendees (per IMAT):

Name	Affiliation	Employer
Akinwale, Oluwafemi	Intel Corporation	Intel Corporation
Alloin, Laurent	Ciena Corporation	
Bruckman, Leon	NVIDIA	NVIDIA
Calvin, John	Keysight Technologies	Keysight Technologies
D'Ambrosia, John	Futurewei Technologies, U.S. Subsidiary of Huawei	Futurewei Technologies, U.S. Subsidiary of Huawei
Dawe, Piers J G	Nvidia	NVIDIA
de Koos, Andras	Microchip Technology, Inc.	Microchip Technology Inc
Dsilva, Hansel	Amphenol Corporation	
El-Chayeb, Ahmad	Keysight Technologies Inc	Keysight Technologies Inc
Galan, Jose	MaxLinear, Inc.	MaxLinear, Inc.
Ghiasi, Ali	Ghiasi Quantum LLC, MARVELL	Ghiasi Quantum LLC
He, Xiang	Huawei Technologies Co., Ltd	Huawei Technologies Co., Ltd
Healey, Adam	Broadcom Inc.	Broadcom Inc.
Heck, Howard	Intel Corporation	TE Connectivity
Hidaka, Yasuo	Credo Semiconductor	Credo Semiconductor
Hong, Paul	Maxlinear, Inc	

Name	Affiliation	Employer
Johnson, John	Broadcom	Broadcom Corporation
Kim, Kihong/Joshua	Hirose Electric (USA), Inc.	Hirose Electric (USA), Inc.
Kimber, Eric	Semtech Ltd	Semtech Ltd
Landry, Gary	Texas Instruments	Texas Instruments Inc.
Li, Pei-Rong	MediaTek Inc.	MediaTek Inc.
Little, Terrance	Foxconn Electronics Inc.	Foxconn Electronics Inc.
Lusted, Kent	Synopsys, Inc.	Synopsys, Inc.
Malicoat, David	Malicoat Networking Solutions; SENKO Advanced Components	Malicoat Networking Solutions
Marris, Arthur	Self	Cadence Design Systems, Inc.
Mascitto, Marco	Nokia	Infinera Canada Inc.
Mellitz, Richard	Samtec, Inc.	Samtec, Inc.
Moorwood, Charles	Keysight Technologies	Keysight Technologies
Muhigana, Ernest	Lumentum	MACOM Technology Solutions Holdings, Inc.
Muth, Karlheinz	Broadcom Corporation	Broadcom Corporation
Nowell, Mark	Cisco Systems, Inc.	Cisco Systems, Inc.
Opsasnick, Eugene	Broadcom Inc.	Broadcom Inc.

Name	Affiliation	Employer
Parsons, Earl	CommScope, Inc.	CommScope, Inc.
Pfeifle, Joerg	Keysight Technologies	Keysight Technologies
Rabinovich, Rick	Keysight Technologies	Keysight Technologies
Rodes, Roberto	Coherent	II-VI
Sakai, Toshiaki	Socionext	Socionext Inc.
Sekel, Steve	Wilder Technologies	Wilder Technologies
Shakiba, Mohammad	Huawei Technologies Canada; Huawei Technologies Co., Ltd	Huawei Technologies Canada Co., Ltd.
She, Qingya	FUJITSU	Fujitsu Network Communications
Simms, William	NVIDIA Corporation	NVIDIA Corporation
Spruit, Hans		TRUMPF
Swenson, Norman	Norman Swenson Consulting; Point2 Technology Inc., Nokia	Norman Swenson Consulting
WANG, Xuebo	Huawei Technologies Co., Ltd	
Weaver, James	Arista Networks	Arista Networks
Yu, Rang-Chen	Innolight Technology Corproation	Innolight Technology Corproation