

# Comment 205: Figure 201-7 and Related Comment 46: Figure 201-11

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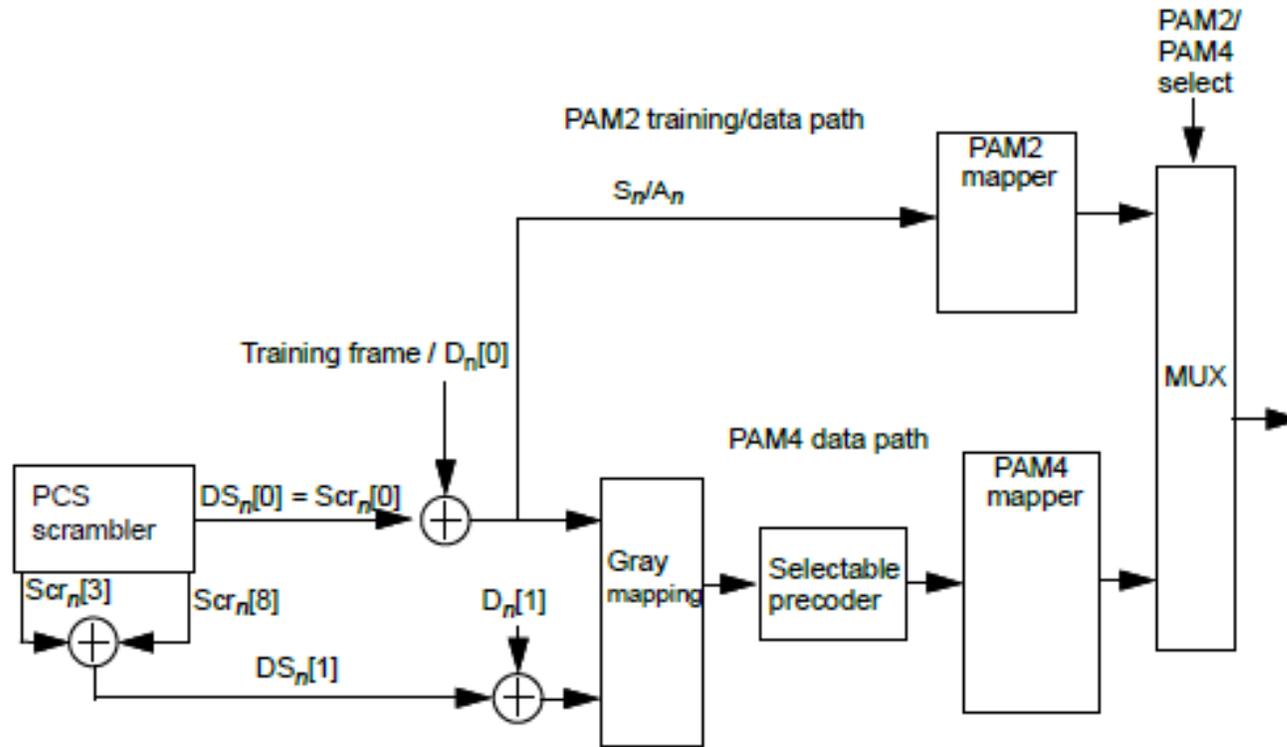
# Problem: Figure 201-7

The fact that Clause 201 has both PAM2 and PAM4 PHYs creates some inconsistencies reusing Clause 149 figures for 2.5G/5G PHY Types

Existing Figure 201-7 has a number of issues.

- It mixes 2.5G/5G and 10G PCS features, which are different (2.5G/5G does not have the PAM4 path at all...),
- It is missing interfaces where signals come from
- The output is not labeled to reference other diagrams
- The Figure claims to be a 'block diagram' of the PCS Transmit, but is really only a subset of the PCS Transmit, and omits the RS-FEC encoder, and the PCS Transmit process which generates the 65B blocks from XGMII

# Existing Diagram

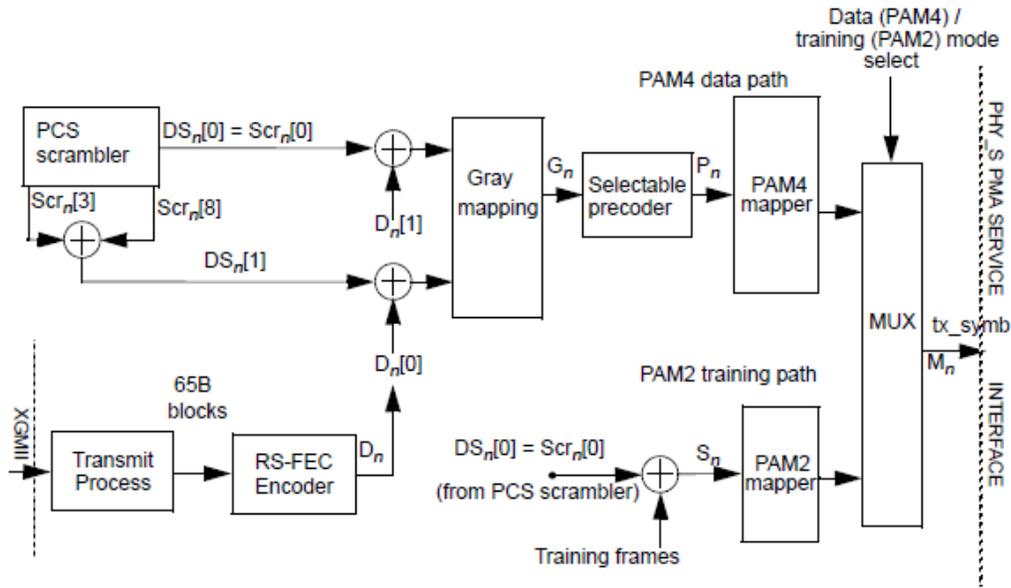


NOTE 1—The  $S_n$  are from the training frame, and the  $A_n$  are from the 2.5G and 5G HS\_PATH when the PAM2 mapper is used.

Figure 201-7—HS\_TX PCS Transmit function block diagram

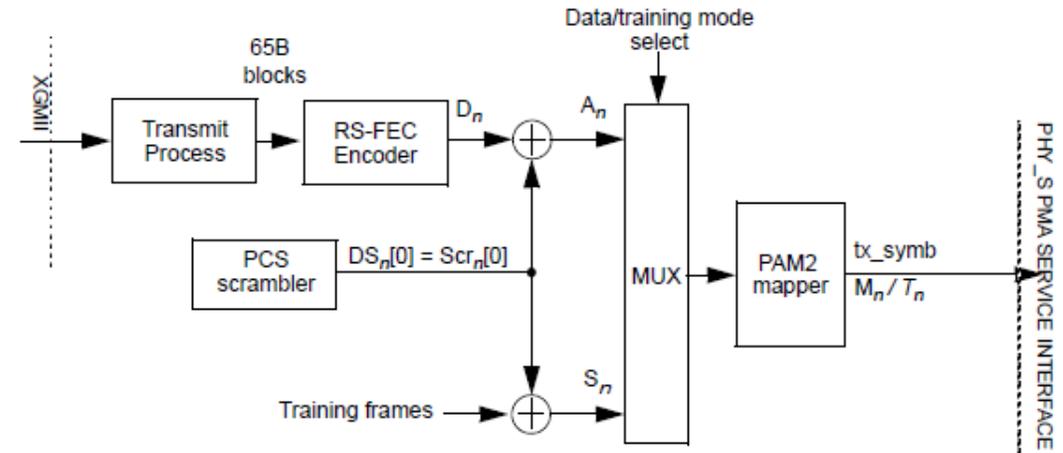
# Proposed New Figures

The block diagram of HS\_TX PCS Transmit functions is shown in Figure 201-7a and Figure 201-7b, replacing Figure 149-5. .



NOTE —The  $S_n$  are from the training frame, and the MUX selects the PAM2 path while in PAM2 training states.

Figure 201-7a—10Gbps HS\_TX PCS Transmit function block diagram



NOTE 1 —The  $S_n$  are from the training frame, and the  $A_n$  are for the 2.5G and 5G HS\_PATH.  
NOTE 2 — The output of the PAM2 mapper is  $M_n$  in data mode and  $T_n$  in training mode.

Figure 201-7b—2.5Gbps and 5 Gbps HS\_TX PCS Transmit function block diagram

# Related comment: Comment 46

ID	Commenter	Affiliation	Subclause	Page	Line	Comment	Suggested Remedy
46	Jonsson, Ragnar	Infineon	201.4.2.3	97	32	HS_PATH in Figure 201-7	Change the handling of the "Training frame" in the figure to be consistent with how it is done in Figure 201-7

## Proposed

### Existing: Draft 0b

A block diagram of the low speed path PCS Transmit functions is shown in Figure 201-11.

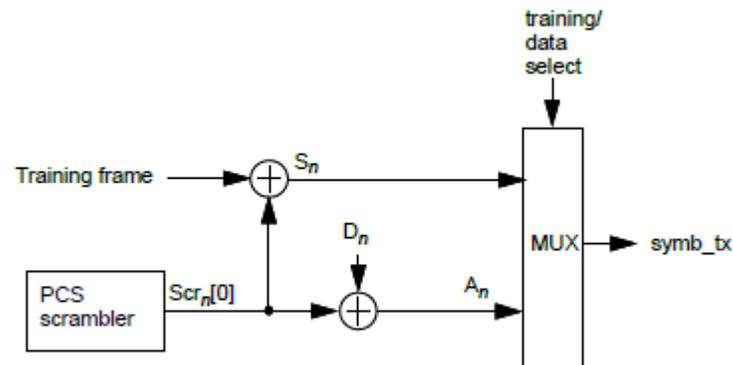


Figure 201-11—PCS Transmit function block diagram for low speed path

A block diagram of the low speed path PCS Transmit functions is shown in Figure 201-11.

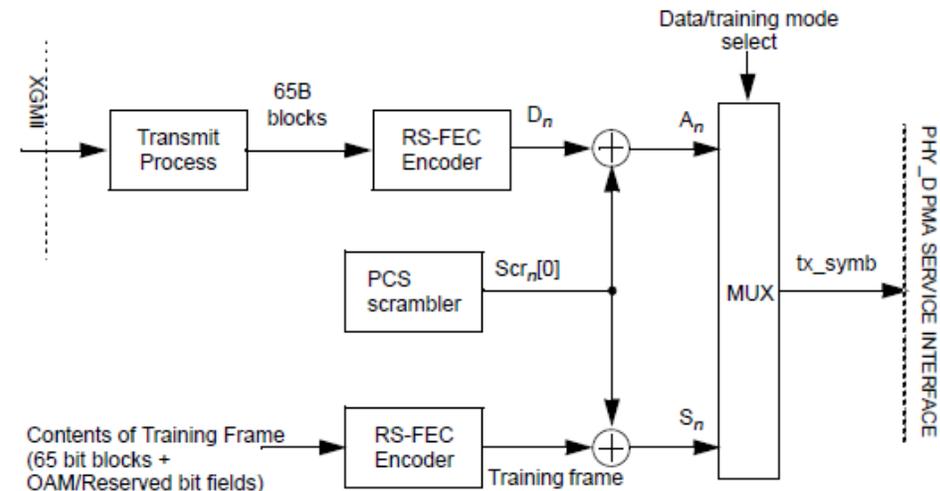


Figure 201-11—PCS Transmit function block diagram for low speed path

Questions?  
Thank you.