

Meeting Minutes: IEEE P802.3dq Pin-Optimized PHY Interface (POPI) Task Force
May 14, 2026
Interim

Prepared by Chad Jones

IEEE 802.3 Pin-Optimized PHY Interface (POPI) Study Group meeting was convened at 13:20 CEST, Thursday, May 14, 2026, by Jason Potterf, IEEE 802.3 POPI Task Force Chair

All times are in CEST

Attendance is listed in Appendix A

Mr. Potterf reminded the group of the IEEE SA individual participation policy, and front matter and asked if everyone had reviewed the materials as was requested in the meeting announcement. There were affirmative responses, and no questions.

Presentation: POPI_Agenda_2026-05-14_v01.pdf

Presenter: Jason Potterf, Chair.

13:23 The Chair reviewed the agenda. Mr. Potterf turned to the presentation and reviewed the agenda. Mr. Potterf asked if there were any modifications to the agenda, none responded, and he noted that he considered the agenda approved.

The Chair then resumed the review of POPI_Agenda_2026-05-14_v01.pdf:

- Mr. Potterf asked if anyone was attending from the press including those who would run a public blog on this meeting – none responded.
- Mr. Potterf noted that there should be no recording or photography without permission.

Mr. Potterf continued review of the presentation including access to the reflector and website, and ground rules.

13:26 Approval of past meeting minutes. Motion 1

Move to approve the final Study Group and initial Task Group meeting minutes shown on slide 6 of https://www.ieee802.org/3/dq/public/2026-05-14/POPI_Agenda_2026-05-14_v02.pdf

M: Brian Murray

S: Peter Jones

Motion approved by unanimous consent with opposition

Mr. Potterf advised the group of the IEEE meeting attendance tool and procedures.

13:29 Mr. Potterf reviewed the IEEE Structure and where to find the governing (IEEE-SA and below) rules and procedures documents.

IEEE Patent Policy, Mr. Potterf showed the patent slides and made the request for potentially essential patents at 13:30. None responded. Mr. Potterf completed reading the patent slides.

13:33 Mr. Potterf showed the IEEE-SA Copyright policy, Participant Behavior, Individual Process, “Fair and Equitable Consideration”, SA meeting rules, and the Ethics reporting line slides in the agenda deck and asked if there were any questions. No one responded.

Mr. Potterf reviewed the standards development process for IEEE and where this Task Force is in the process.

Mr. Potterf then moved to reviewing the Objectives and noted that these will need to be taken back to the IEEE 802.3 Working Group for approval as some changes have been made since they were approved previously.

Mr. Potterf noted that the timeline shown on slide 40 was out of date and needed to be updated.

Future Meetings:

- May IEEE 802.3 plenary in Montreal, Canada
- POPI Meeting Timeslot (8 am Pacific US for 2 hours) on Tuesday ??, 2026

PRESENTATIONS

13:40

Title: POPI Requirements for 10/100M PHYs

URL: https://www.ieee802.org/3/dq/public/2026-05-14/Murray_3dq_01a_05142026.pdf

Presenters: Brian Murray, Jacobo Riesco – Analog Devices

14:18 Discussion: general questions were asked and answered

14:27 The Chair asked George Zimmerman to chair the meeting while he presented.

14:29

Title: POPI Sideband MicroFrame Format Proposal

URL: https://www.ieee802.org/3/dq/public/2026-05-14/POPI_Potterf_Expanded_Sideband_Microframe_Format_Proposal_2026-05-14.pdf

Presenter: Jason Potterf – Cisco Systems

14:46 Discussion: General questions were asked and answered

14:48 Mr. Potterf resumed as Chair.

The chair then moved into a discussion of modification and updates to the objectives. The plan is to present these for WG approval in July.

15:15 break, reconvene at 15:40

Discussion of the objectives continues.

16:03 The chair discussed future meetings and asked for presentations for the plenary meeting in Montreal in July.

The Chair indicated that the agenda had been exhausted and adjourned the meeting at 16:04 CEST.

Appendix A: Attendees at the IEEE P802.3dq Pin-Optimized PHY Interface (POPI) Task Force Meeting, May 14, 2026.

Normalized attendee	Affiliation
Ajeya Gupta	General Motors Company
Anton Schedl	BMW Group
Brian Mowad	Cisco
Brian Murray	Analog Devices Inc.
Chad Jones	Cisco Systems, Inc.
Daniel Hopf	AUMOVIO
David Ofelt	Hewlett Packard Enterprise
Dokyun Kim	LG ELECTRONICS
George Zimmerman	CME Consulting/Analog Devices, APL Group, Cisco, Infineon, OnSemi, Sony
Guy Hutchison	Aviva Links Inc; Aviva Links Inc.
Heiko Strohmeier	Robert Bosch GmbH
Jason Potterf	Cisco Systems, Inc.
Jonas Edele	BMW AG; BMW Group
Markus Jochim	General Motors Company
Mary Sue Haydt	Microchip Technology, Inc.
Michal Brychta	Analog Devices Inc.
Natalie Wienckowski	IVN Solutions LLC; Ethernovia, Bosch
Paul Vanderlaan	Panduit Corp.
Peter Jones	Cisco Systems, Inc.
Ramanjit Ahuja	ON Semiconductor
Robert Voss	Panduit Corp.
Scott Muma	Microchip Technology, Inc.
Sophie Laperche	CetraC
Steffen Graber	self
Thomas Hogenmueller	Robert Bosch GmbH
Tim Baggett	Microchip Technology, Inc.
Tingting Zhang	Huawei Technologies Co., Ltd
TJ Houck	Infineon Technologies
Ulf Parkholm	Ericsson AB
Valerie Maguire	Copperopolis (affl with CME Consulting, Cisco, Dell, Microchip, and NXP)
Xuebo Wang	Huawei Technologies Co., Ltd
Yan Zhuang	Huawei
Yuxuan Tan	Motorcomm