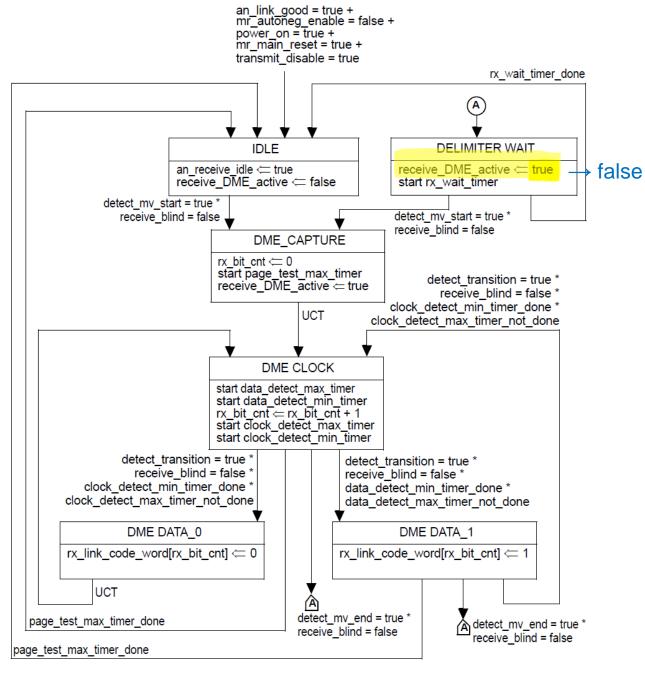
receive\_DME\_active in
DELIMITER WAIT state of
Figure 98-9 Receive state diagram

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#### Overview



- DELIMITER WAIT state is entered after successful completion of reception of DME page from the link partner (condition detect\_mv\_end = true).
- No DME page reception is ongoing in DELIMITER WAIT
  - receive\_DME\_active should be assigned to false
  - But Figure 98-9 currently shows it as assigned to true
- Description of variable from subclause 98.5.1:

receive\_DME\_active

Status indicating whether or not a DME page reception is in progress.

Values:

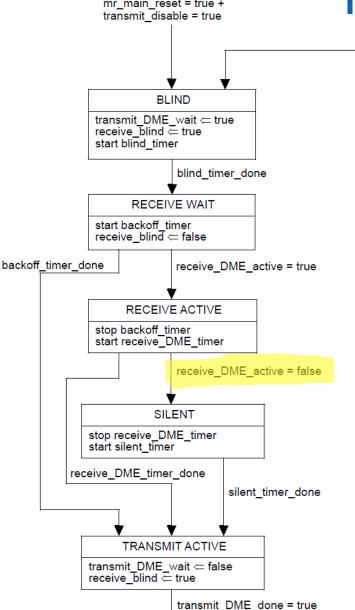
true: DME page reception in progress

false: DME page reception completed

# an\_link\_good = true + mr\_autoneg\_enable = false + power\_on = true + mr\_main\_reset = true + transmit\_disable = true

# Impact on Half-duplex operation (1)





- Figure 98-10 Half-duplex state diagram
- The intended flow here is straightforward, i.e. to transition through each state in turn, and repeat.
- Purpose of receive\_DME\_active is to keep Half-duplex state diagram synchronized with receive activity.
- When DME page reception is ongoing, this state machine will be in the RECEIVE ACTIVE state
  - Waiting for completion of DME page reception, i.e. the condition receive\_DME\_active = false.
- Once the DME page reception has completed, the Receive state machine will enter DELIMITER WAIT, but currently it does not assert receive\_DME\_active = false there.
- Exit from RECEIVE ACTIVE then relies on receive\_DME\_timer\_done, and the transition skips SILENT
  - This timer is defined in subclause 98.5.2
  - Timer for the maximum amount of time to receive a complete page before timeout.

### Impact on Half-duplex operation (2)



- Half-duplex state diagram intended to operate as follows:
  - Once either PHY successfully receives a DME page, then both PHYs will fall into a pattern of alternating DME page transmission and reception. One PHY will transmit and the other PHY receives, and then the other PHY transmits and the one PHY receives.
  - Auto-Negotiation (i.e. Arbitration state diagram) will then proceed to completion quickly.
- Issue with receive\_DME\_active in DELIMITER WAIT means that Half-duplex state diagram and Receive state diagram do not remain in sync.
  - Transition from RECEIVE WAIT to RECEIVE ACTIVE state occurs on receive\_DME\_active = true, but the Receive state diagram is holding this at true, so this transition does not occur with start of DME page reception (detect\_mv\_start in Receive state diagram).
- Lack of synchronization can give rise to DME page collision:
  - A PHY might commence DME page transmission before ongoing reception of a DME page from the link partner has completed.
  - The link partner might commence DME page transmission before ongoing local DME page transmission has completed.
- DME page collision will likely provoke rx\_wait\_timer\_done (in Receive state diagram):
  - Causes transition back to IDLE, assertion of an\_receive\_idle = true, and Arbitration transition back to TRANSMIT DISABLE (restart of Auto-Negotiation).

## Origin of problem



- Problem appears to have been introduced in IEEE 802.3bp standardization (1000BASE-T1 and Auto-Negotiation) when diagrams were redrafted for IEEE P802.3bp/D2.1.
- Some presentations give some insight into original intent and evolution of Clause 98 Auto-Negotiation, and all include versions of the Receive state diagram, and all show receive\_DME\_active = false in DELIMITER WAIT:
  - http://www.ieee802.org/3/bp/public/mar14/Lo\_3bp\_04\_0314.pdf; see slide 11.
  - http://www.ieee802.org/3/bp/public/jul14/Lo 3bp 02a 0714.pdf; see slide 10.
  - http://www.ieee802.org/3/bp/public/jan15/McClellan\_3bp\_05\_0115.pdf; see slide 2.
- Also baseline text for Clause 98 Auto-Negotiation:
  - http://www.ieee802.org/3/bp/public/nov14/mcclellan\_3bp\_03\_1114\_%20Autoneg\_baseline\_text\_proposal\_v0 p4.pdf; see page 26.
- Comments received against D2.0 here: http://www.ieee802.org/3/bp/comments/8023bp\_D20\_approved.pdf
  - See comments #6, #7, #8, #121. #316, #29 concerning diagram clarity and legibility.
  - Problem appears to have been introduced inadvertently, when the diagrams were redrafted to address these comments.