**IEEE Std 802.3-2015, Section 1**

| Page | Subclause | D2.0 page | D2.0 line | "Deprecated"? | PICS? | Proposed change |
|------|-----------|-----------|-----------|---------------|-------|-----------------|
| 197 | 7.4.2.1 | 198 | 7 | No | No | NOTE—The specified threshold levels do not take precedence over the duty cycle and jitter tolerance specified elsewhere. Both sets of specifications ~~must~~ have to be met. |
| 220 | 8.3.1.5 | 221 | 30 | Yes | | None. |
| 221 | 8.4.1.1 | 222 | 47 | Yes | | None. |
| 226 | 8.5.3.1 | 227 | 3 | Yes | | None. |
| 269 | 9.9.2.8 | 270 | 19 | Yes | | None. |
| 357 | 12.7.3.3 | 358 | 6 | Yes | | None. |
| 381 | 14.2.1.8 | 382 | 13 | No | No | NOTE—Since there is no way to communicate the half or full duplex abilities between a DTE or repeater and a MAU separated by an AUI, caution ~~must~~should be used when connecting full duplex capable DTEs or MAUs that are manually configured. |
| 390 | 14.3.1.2 | 391 | 22 | No | No | NOTE–Care ~~must~~should be taken that layout and parasitics do not exceed R, C, and L tolerance values. |
| 394 | 14.3.1.2.4 | 395 | 19 | No | No | NOTE—The balance of the test equipment (such as the matching of the 147 Ω resistors) ~~must~~should exceed that required of the transmitter. |
| 441 | 15.6.1 | 442 | 34 | No | No | NOTE—While optical fibers are nonconducting, some fiber optic cables do contain metallic strength members or sheathing that ~~must~~should be considered during installation. However, since grounding of these metallic members does not involve the signal path, it is beyond the scope of this standard. |

**IEEE Std 802.3-2015, Section 2**

| Page | Subclause | D2.0 page | D2.0 line | "Deprecated"? | PICS? | Proposed change |
|------|-----------|-----------|-----------|---------------|-------|-----------------|
| 83 | 22.6.1 | 83 | 30 | No | No | NOTE—All MII conformance tests are performed at the mating surfaces of the MII connector at the Reconciliation sublayer end of the cable. If a PHY circuit assembly does not have a permanently attached cable, ~~the vendor must ensure that all of~~ the requirements of this clause ~~are~~ also <u>have to be</u> met when a cable that meets the requirements of 22.4.5 is used to attach the PHY circuit assembly to the circuit assembly that contains the Reconciliation sublayer. |
| 141 | 23.5.1.2.6 | 141 | 1 | Yes | | None. |
| 195 | 24.2.4.2 | 195 | 37 | No | n/a | Change the transition condition from IDLE to IDLE to "BackToIDLE (NOTE 1)". |
| 574 | 32.6.1.4.2 | 588 | 22 | Yes | | None. |

**IEEE Std 802.3-2015, Section 3**

| Page | Subclause | D2.0 page | D2.0 line | "Deprecated"? | PICS? | Proposed change |
|------|-----------|-----------|-----------|---------------|-------|-----------------|
| 121 | 37.3.1.1 | 122 | 7 | No | No | NOTE—For the first setting of mr_page_rx, mr_lp_adv_ability is valid but need not be read as it is preserved through a Next Page operation. On subsequent settings of mr_page_rx, mr_lp_np_rx ~~must~~<u>should</u> be read prior to loading mr_np_tx register in order to avoid the overlay of Next Page information. |
| 267 | 40.8.3.3 | 269 | 37 | No | No | NOTE—The imbalance of the test equipment (such as the matching of the test resistors) ~~must~~<u>should</u> be insignificant relative to the balance requirements. |

| Page | Subclause | D2.0 page | D2.0 line | "Deprecated"? | PICS? | Proposed change |
|------|-----------|-----------|-----------|---------------|-------|-----------------|
| 436 | 50.3.2 | 529 | 44 | No | No | NOTE—Figures 50–8 and 50–7 are provided for informative purposes only. In the event of any discrepancy observed between the figures and the normative portions of ATIS-0600416.1999(R2010), the latter ~~shall~~ take_s_ precedence. |
| 440 | 50.3.2.4 | 533 | 29 | No | No | NOTE—Values of overhead fields encoded by the Transmit process within a WIS entity at one end of a link may be changed by intervening Line and Section equipment before it reaches the peer WIS entity at the other end of the link. In particular, the H1, H2, and H3 fields may take on values different from those originally encoded. The receive process ~~must be able to~~ handle_s_ these situations in accordance with ATIS-0600416.1999(R2010), and therefore ~~must~~ contain_s_ a pointer interpreter function compliant with the latter. Bits 5 and 6 of the H1 octet are ignored by the receiver. |
| 737 | 48B.3.1.1 | 830 | 51 | No | n/a | 26 Amplitude calibration should be done before addition of SJ, as SJ is designed to add margin into the design. ~~NOTE—~~This footnote is not referring to the suggestion of temporarily substituting SJ and RJ during amplitude calibration, but to the SJ that is required as part of the specifications. |