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| 8802-3/802.3 REVISION REQUEST 1102 |
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DATE: 25th October, 2002
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REQUESTED REVISION:
STANDARD: IEEE Std. 802.3ae-2002
CLAUSE NUMBER: 46.4
CLAUSE TITLE: XGMII electrical characteristics

PROPOSED REVISION TEXT:

It is stated in 46.4, page 293, line 18-19 (P802.3ae/D5.0), that the XGMII timing measurements should be measured at the XGMII driver output. Should the measured timing not be done with reference to the VOH(ac) and VOL(ac) signal levels?

RATIONALE FOR REVISION:

The JEDEC HSTL standard defines a VOH(ac) and VOL(ac). These are not used for timing reference in either the HSTL nor the XGMII standard (uses VIN(ac)).

IMPACT ON EXISTING NETWORKS:

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| Please attach supporting material, if any |
| Submit to:- Bob Grow, Chair IEEE 802.3 |
| E-Mail: Bob.Grow@intel.com |
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| +----- For official 802.3 use -----+ |
| REV REQ NUMBER: 1102 |
| DATE RECEIVED: 25th October, 2002 |
| EDITORIAL/TECHNICAL |
| ACCEPTED/DENIED |
| BALLOT REQ'D YES/NO |
| COMMENTS: 25-Oct-02 Ver: D1.0 Status: R |
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| For information about this Revision Request see - |
| http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1102 |
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