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+---------------------------------------------------------------------+     1
|                 8802-3/802.3 REVISION REQUEST 1135          |            2
+=====================================================================+     3
                                                                            4
DATE:                   29th Apr, 2004                                      5
NAME:                   Peter Bradshaw                                      6
COMPANY/AFFILIATION: BitBlitz Communications                                7
E-MAIL:                 pbradshaw@bitblitz.com                              8
                                                                            9
REQUESTED REVISION:                                                        10
    STANDARD:        IEEE Std 802.3ae-2002                                 11
    CLAUSE NUMBER:   45.5.5.3                                              12
    CLAUSE TITLE:    PMA/PMD management functions                         13
                                                                           14
PROPOSED REVISION TEXT:                                                    15
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In the PICS Item MM31, replace:-                                          17
                                                                           18
Single wavelength device uses lane 0 for transmit disable                 19
by                                                                         20
Single wavelength device uses bit 1.9.0 for transmit disable              21
                                                                           22
For details see attached draft.                                           23
                                                                           24
RATIONALE FOR REVISION:                                                    25
                                                                           26
The PICS item does NOT correspond to subclause 45.2.1.8 paragraph 2. Bit  27
1.9.0 is the global disable. For multiple wavelength or lane devices,     28
lane 0 is controlled by bit 1, for single wavelength devices, there is no 29
lane 0.                                                                    30
                                                                           31
                                                                           32
IMPACT ON EXISTING NETWORKS:                                              33
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It is unlikely that anyone designed a PMA/PMD based on the editorial      35
error in the PICS item, since the controlling subclause is clear.         36
                                                                           37
                                                                           38
+---------------------------------------------------------------------+    39
| Please attach supporting material, if any                           |   40
| Submit to:- Bob Grow, Chair IEEE 802.3                              |   41
|            E-Mail: Bob.Grow@intel.com                               |   42
|                                                                     |   43
|            +------- For official 802.3 use -----------+             |   44
|            | REV REQ NUMBER: 1135                      |             |   45
|            | DATE RECEIVED: 29th Apr, 2004             |             |   46
|            | EDITORIAL/TECHNICAL                       |             |   47
|            | ACCEPTED/DENIED                           |             |   48
|            | BALLOT REQ'D YES/NO                       |             |   49
|            | COMMENTS: 08-Mar-06 Ver: D3.0 Status: P  |             |   50
+------------+-------------------------------------------+------------+    51
| For information about this Revision Request see -                    |   52
|http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1135 |  53
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Change PICS entry MM31 found in subclause 45.5.5.3 as follows:

MM31	Single wavelength device uses <u>bit</u> ~~lane~~ zero for transmit disable	45.2.1.8		PMA*PTD:M	Yes [ ] N/A [ ]