DATE: 21st May, 2004
NAME: Thomas Mathey
COMPANY/AFFILIATION: Independent
E-MAIL: tmathey@concentric.net

REQUESTED REVISION:
STANDARD: IEEE Std 802.3-2002
CLAUSE NUMBER: Figure 7-6
CLAUSE TITLE: PLS Input and Data_Valid function

PROPOSED REVISION TEXT:
Change text from "SQE * input_idle" to "not SQE * input_idle". The not applies only to SQE.
For details see attached draft.

RATIONALE FOR REVISION:
Error when figure was converted from 1996 version.
For exit from state "BIT BUCKET" to state "INPUT IDLE", an inversion on signal SQE was dropped in edits.

IMPACT ON EXISTING NETWORKS:
Editorial change only.

Please attach supporting material, if any
Submit to:- Bob Grow, Chair IEEE 802.3
E-Mail: Bob.Grow@intel.com

For information about this Revision Request see -
http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1143
Replace Figure 7-6 with the following:

**Figure 7–6—PLS Input and Data_Valid function**

```
RESET
 DATA_VALID_STATUS = DATA_NOT_VALID

BIT BUCKET
 pls_reset

INPUT IDLE
 DATA_VALID_STATUS = DATA_NOT_VALID

CARRIER_ON

DISCARD TRASH
 [Discard the first 15 bits received]
 DATA_VALID_STATUS = DATA_NOT_VALID

Input_idle

INITIALIZE
 [N = 1]

Input (bit N)

INPUT HOLD

Input (bit N+1)

INPUT DATA
 Input-Unit (N)

INCREMENT
 [N = N + 1]

UCT

CARRIER_OFF

SQE

INPUT HOLD

INPUT DATA

UCT

Input (bit N)
```

**ISO/IEC 8802-3/IEEE Std.802.3 Revision Request. Subject to change.**