8802-3/802.3 REVISION REQUEST 1182

DATE: 19th Sep, 2006
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REQUESTED REVISION:
STANDARD: IEEE Std 802.3an-2006
CLAUSE NUMBER: 45.2.1.4
CLAUSE TITLE: PMA/PMD speed ability (Register 1.4)

PROPOSED REVISION TEXT:

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Table 45-6 is missing an entry for bit 1.4.3.
Add a row for bit 1.4.3 with name "Reserved"
and description "Value always 0, writes ignored" and R/W entry of "RO".

RATIONALE FOR REVISION:

Table 45-6 is missing an entry for bit 1.4.3.

IMPACT ON EXISTING NETWORKS:

None. This is an editorial change.

Please attach supporting material, if any
Submit to:- Bob Grow, Chair IEEE 802.3
E-Mail: Bob.Grow@intel.com

For information about this Revision Request see -
http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1182