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8	802-3/802.3 REVISION REQUEST 1286
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DATE:	11th September, 2015
NAME:	David Law
COMPANY/AFFILIATION:	HP Ltd
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REQUESTED REVISION:	
STANDARD:	IEEE Std 802.3-2015
CLAUSE NUMBER:	90.7
CLAUSE TITLE:	
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PROPOSED REVISION TE	XT:
[1] Change the toyt	in the first sentence of subclause 90.7 as follows
	tion by the PHY to the TX_{MDI} .
co teb presenta	
[2] Change the text	in the second sentence of subclause 90.7 as follows
-	he <u>RX</u> MDI to its presentation'.
	ence to the end of subclause 90.7 that reads 'In the
	PHY, the receive path data delay is measured from
_	inning of the SFD at the RX MDI on the lane with the
maximum media propag	ation delay.'.
RATIONALE FOR REVISI	ON •
RATIONALE FOR REVISI	ON:
Items [1] and [2] are	e to clarify which of the two MDIs, the TX or the RX,
	efer to in each case.
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Item [3] clarifies w	hich lane should be used as the timing reference
	eive path data delay register to in a multi-lane PHY.
	defined for the receive path is the input of the
	at the MDI, however in the case of a multi-lane PHYs
	nt media lane delays, resulting in differing arrival
times for the beginn	ing of the SFD on each lane.
Without a definition	of which lane of the MDI is used as the timing
	entire inter-lane skew may need to be accounted for
	um and receive maximum path data delay register
	on the timing accuracy that can be supported.
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IMPACT ON EXISTING N	ETWORKS:
None. This will supp	ort improved timing accuracy in the future.

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| Please attach supporting material, if any
| Submit to:- David Law, Chair IEEE 802.3
 and copy: - Adam Healey, Vice-Chair IEEE 802.3
          E-Mail: stds-802-3-maint-req@ieee.org
At:-
          +----- For official 802.3 use -----+
          REV REQ NUMBER: 1286
          DATE RECEIVED: 11th September, 2015
          | EDITORIAL/TECHNICAL
          ACCEPTED/DENIED
          | BALLOT REQ'D YES/NO
          | COMMENTS: XX-Xxx-XX Ver: D1.0 Status: R |
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For information about this Revision Request see -
| http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1286 |
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