```
2
               8802-3/802.3 REVISION REQUEST
3
    +-----
    DATE: January 26, 2016
5
    NAME: Yasuo Hidaka
6
    COMPANY/AFFILIATION: Fujitsu Laboratories of America, Inc.
7
   E-MAIL: yasuo.hidaka@us.fujitsu.com
8
9
   REQUESTED REVISION:
10
     STANDARD: 802.3-2015
11
     CLAUSE NUMBER: 55.1.3.1
12
    CLAUSE TITLE: Physical Coding Sublayer (PCS)
13
14
   PROPOSED REVISION TEXT:
15
16
    Change the sixth paragraph of clause 55.1.3.1 as follows:
17
18
    The DSQ128 constellation is partitioned into eight subsets, each
19
    subset containing 16 maximally spaced 2D symbols. The three uncoded
20
   bits of each 7-bit label select one DSQ128 subset, and the four
21
   LDPC-coded-bits of the label select one 2D symbol in this subset.
22
23
    RATIONALE FOR REVISION:
24
25
    The original text is not consistent with slide 9 of
26
    http://www.ieee802.org/3/an/public/sep04/ungerboeck 2 0904.pdf
27
    that is the basis of DSQ128 bit mapping described in the second
    paragraph of clause 53.3.2.2.19.
28
29
    In the above paragraph, the four LDPC-coded bits and three uncoded
30
   bits are swapped.
31
32
   A similar comment was made to P802.3bq D3.0 (comment i-38)
33
   and accepted.
34
35
    IMPACT ON EXISTING NETWORKS: None.
36
    +-----
37
38
   |Please attach supporting material, if any
   |Submit to:- David Law, Chair IEEE 802.3
39
40
   |and copy:- Adam Healey, Vice-Chair IEEE 802.3
41
42
   |At:- E-Mail: stds-802-3-maint-reg@ieee.org
43
44
               +----- For official 802.3 use -----+
45
               | REV REQ NUMBER: 1293
               | DATE RECEIVED: 26th January 2016
46
47
               | EDITORIAL/TECHNICAL
48
               | ACCEPTED/DENIED
   49
               | BALLOT REQ'D YES/NO
   50
               | COMMENTS:
    +-----+
51
52
   | For information about this Revision Request see -
   |http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1293 |
53
   +-----
54
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