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4  DATE: 01/14/2020
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8
9  REQUESTED REVISION:
10     STANDARD: 802.3cb-2018
11     CLAUSE NUMBER: 125.3
12     CLAUSE TITLE: Delay Constraints
13  PROPOSED REVISION TEXT:
14
15  Change Table 125-3 entries for 5GBASE-KR PMD
16  from "1024 1 102.4"
17  to "512 1 102.4"
18
19  Clause 130.4 page 128 change "1024 bit times" to "512 bit times"
20
21  RATIONALE FOR REVISION:
22  The current entries for 5GBASE-KR PMD Maximum (bit time) of 1024
23  does not correspond to the entries for Maximum (pause_quanta) of 1 and
24  Maximum (ns) of 102.4.
25
26  The maximum bit time of 512 for 5GBASE-KR PMD was adopted as a baseline
27  in motion #1 at the March 2016 plenary as a part of this draft text:
28  http://www.ieee802.org/3/cb/public/mar16/Lo_3cb_04a_0316.pdf
29  Later, draft text was added for 130.4 with a stated max delay of 1024
30  bits, conflicting with Table 125-3.
31
32  The 512 Maximum (bit time) was retained in Table 125-3 until comment
33  resolution on i-20 of draft 3.0 adopted the incorrect value of 1024
34  from 130.4 into Table 125-3 without changing the associated
35  Maximum (pause_quanta) and Maximum (ns).
36
37  IMPACT ON EXISTING NETWORKS:
38  None.
39
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41  |Please attach supporting material, if any
42  |Submit to:-   David Law, Chair IEEE 802.3
43  |and copy:-   Adam Healey, Vice-Chair IEEE 802.3
44  |
45  |At:-         E-Mail: stds-802-3-maint-req@ieee.org
46  |
47  |             +----- For official use -----+
48  |             | REV REQ NUMBER: 1358
49  |             | DATE RECEIVED: 17 July, 2020
50  |             | EDITORIAL/TECHNICAL
51  |             | ACCEPTED/DENIED
52  |             | BALLOT REQ'D   YES/NO
53  |             | COMMENTS:
54  +-----+
55  | For information about this Revision Request see -
56  |http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1358
57  +-----+

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