REVISION REQUEST

DATE: May 9, 2023
NAME: Andras de Koos
COMPANY/AFFILIATION: Microchip Technology Inc.
E-MAIL: andras.dekoos@microchip.com

REQUESTED REVISION:

STANDARD: 802.3-2022
CLAUSE NUMBER: 119
CLAUSE TITLE: Physical Coding Sublayer (PCS) for 64B/66B, type 200GBASE-R and 400GBASE-R

PROPOSED REVISION TEXT:

In "Table 119–5—MDIO/PCS status variable mapping", the row for "Lane x aligned" refers to the PCS variable "am_lock<x>". However, no such variable exists in Clause 119. It should map to the "amps_lock<x>" variable in clause 119. "am_lock<x>" exists for clauses 82/91.

Note that the same typo exists in the MII-Extender Clause:
Table 118–2—MDIO PHY XS to Clause 119 status variable mapping
Table 118–4—MDIO DTE XS to Clause 119 status variable mapping

RATIONALE FOR REVISION:

Seems like a typo, and an obvious one at that. But doing a text search for amps_lock<x>, one could mistakenly conclude that the clause 119 amps_lock<x> is not actually mapped to MDIO registers.

IMPACT ON EXISTING NETWORKS:
None.

| Please attach supporting material, if any |
| Submit to:-    David Law, Chair IEEE 802.3 |
| and copy:-     Adam Healey, Vice-Chair IEEE 802.3 |
| At:-           E-Mail: stds-802-3-maint-req@ieee.org |

For information about this Revision Request see -
http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1403