

IEEE P802.3de Time Synchronization for Point-to-Point Single Pair Ethernet Task Force Report

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Electronic Plenary

January 2022

IEEE P802.3de Project Information

Task Force Organization

George Zimmerman, IEEE 802.3de Task Force Chair & Editor

Task Force web and reflector information

Reflector information: <https://www.ieee802.org/3/de/reflector.html>

Note – project uses same reflector as GT10Mb/s SPE SG, please put 802.3de in the Subject line

Home page: <https://www.ieee802.org/3/de/index.html>

Private area: <http://www.ieee802.org/3/de/private/index.html>

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PAR: https://www.ieee802.org/3/de/P802d3de_PAR.pdf

CSD: <https://mentor.ieee.org/802-ec/dcn/21/ec-21-0197-00-ACSD-p802-3de.pdf>

Objectives: https://www.ieee802.org/3/de/objectives_07212021.pdf

IEEE P802.3de Task Force

Activities since November 2021 plenary

Initial working group ballot

Comment resolution meeting (January 17, 2022)

Recirculation working group ballot chartered.

Ballot Results

	D2p0	D2p0%
Voters	248	
Return	165	66.5%
Approve	134	97.1%
Disapprove w/comment	4	2.9%
Disapprove w/o comment	0	0.0%
Abstain	27	16.4%
Comments	64	
Comments w/floor	64	
Comments w/floor & late	64	

64 comments, highly repetitive

52 related to revision alignment or minor editorial issues

12 relate to wording vs. PAR scope, or technical issues in half-duplex

Topics:

16 Alignment with Revision

9 Editorial

10 EZ (typos, obvious errors)

12 related to Figure NOTE

2 related to PICS

10 related to half-duplex

2 related to PAR scope

Substantial ballot issue

- Project objectives were adopted on the basis that including point-to-point half duplex would be a minimal and well understood addition
- Commenters have pointed out that this is not the case, and consensus is that there is work to be done
 - Plan is to recirculate the ballot and evaluate the state of this work at comment resolution for the recirculation
 - Draft 2.1 will bear the following note:

99.4 MAC Merge sublayer operation

Editor's Note (to be resolved prior to close of WG ballot):

Issues have been raised with regards to the need for additional changes in Transmit and Receive Processing for 10BASE-T1S point-to-point half duplex (other half-duplex PHYs being out of project scope). See comments i-58 and i-59 for more detail. Comments do not provide sufficient detail to resolve. Presentations and further comments to with specific changes to resolve issues are solicited.

IEEE P802.3de Timeline Update

Paced by revision

✓ Initial WG ballot started after November Plenary

802.3de to SA ballot after initial SA ballot of Revision

x Best case – SA ballot in early January 2022

| Slower case – March 2022

Presubmit for end of April 2022 (best) or June (slower) Revcom

~~May 2022/~~ July 2022 Standard

IEEE P802.3de Objectives

Enable use of the 10BASE-T1L point-to-point full-duplex PHY with Clause 99 MACMERGE

Require no changes to PHYs, Clause 4/Annex 4A MAC, or MAC/PLS Interface

Enable use of the 10BASE-T1S PHY with Clause 99 MAC Merge in both half-duplex and full-duplex point-to-point modes.

Request may be to delete the highlighted text in March, based on the state of analysis

Questions?

Thank you!