



presentation as a baseline specification for the IEEE P802.3cg 1,000m PHY objective:  
[http://www.ieee802.org/3/cg/public/May2017/diminico\\_01\\_0517.pdf](http://www.ieee802.org/3/cg/public/May2017/diminico_01_0517.pdf).

We have also adopted a DC Loop Resistance limit described in slide 19 of the following presentation as a baseline specification for the IEEE P802.3cg 1,000m PHY objective:  
[http://www.ieee802.org/3/cg/public/May2017/diminico\\_02\\_0517.pdf](http://www.ieee802.org/3/cg/public/May2017/diminico_02_0517.pdf).

We would welcome any questions or comments you may have on this.

Sincerely,

David Law

Chair, IEEE 802.3 Ethernet Working Group

DRAFT