## Call for Interest Improving PTP Timestamping Accuracy on Ethernet Interfaces Opening Report

Steve Gorshe
Microchip Technology
Vienna, Austria
July 2019

## CFI Request

- Applications in which Precision Time Protocol (PTP) (e.g., IEEE Std 1588 or IEEE Std 802.1AS) is carried over Ethernet interfaces have become increasingly important. A growing number of applications benefit from being able to communicate time synchronization information over Ethernet. Emerging important potential Ethernet applications such as 5G mobile radio access networks require increased PTP accuracy.
- Unfortunately, the increased complexity of several recent IEEE Std 802.3
   PHYs has inadvertently introduced limitations or degradations of the potential PTP accuracy. In some cases, the decreased PTP accuracy is related to a discrepancy between IEEE Std 802.3 and IEEE Std 802.1AS/1588 reference points. Addressing these issues will increase the scope of applications in which Ethernet can be deployed.
- This Call for Interest is to assess the support for the formation of the "Improving PTP Timestamping Accuracy on Ethernet Interfaces" Study Group in IEEE 802.3 to consider the development of a PAR and CSD to address high accuracy time transport for IEEE 802.3 Ethernet.

## Logistics

- An overview presentation session will be given to support consensus building:
  - Date Tuesday, July 16, 2019
  - Time 19:30 to 20:30
  - Location Hall E1 Level 0
- CFI deck: <a href="http://ieee802.org/3/cfi/0719">http://ieee802.org/3/cfi/0719</a> 2/CFI 02 0719.pdf

 The request to form a Study Group will occur during the closing IEEE 802.3 WG Plenary on Thursday

## Questions?

Does anyone have any questions?

- Contact Info:
  - Steve.Gorshe@microchip.com
  - Richard.Tse@microchip.com