

IEEE 802.3 Ethernet Working Group
DRAFT Liaison Communication

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From: David Law, Chair, IEEE 802.3 Ethernet Working Group
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Subject: Status of IEEE P802.3ck Task Force

Approval: Agreed to at IEEE 802.3 Plenary meeting, Vienna, Austria, July 18, 2019

Dear Mr. Otto and members of the OIF,

Thank you for your liaison letter providing the status of the OIF CEI-112G projects along with the draft Implementation Agreements oif2017.346.09 and oif2018.212.05.

IEEE P802.3ck (100 Gb/s, 200 Gb/s, and 400 Gb/s Electrical Interfaces Task Force) is currently developing 100 Gb/s per lane electrical interfaces for chip-to-module, chip-to-chip, electrical backplane, and copper cable for 100 Gb/s, 200 Gb/s, and 400 Gb/s Ethernet. A number of baseline documents have been adopted and are available at <http://www.ieee802.org/3/ck/public/baselines/index.html>. The meeting materials from our July, 2019 plenary meeting can be found at: http://www.ieee802.org/3/ck/public/19_07/index.html.

We intend to communicate our future progress to you.

¹ This document solely represents the views of the IEEE 802.3 Working Group, and does not necessarily represent a position of the IEEE, the IEEE Standards Association, or IEEE 802

Sincerely,
David Law
Chair, IEEE 802.3 Ethernet Working Group

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