



May 19, 2020

To: David Law and members of the IEEE 802.3 Ethernet Working Group
cc: John D'Ambrosia – IEEE P802.3cw Task Force Chair

Subject: 400ZR Interop Project

From: Ed Frlan, OIF Technical Committee Chair (efrlan@semtech.com)

Dear Mr. Law and participants of IEEE 802.3 Working Group,

The OIF Q2 Technical and MA&E Committees meetings were held virtually from May 11th through May 15th. Since our previous meeting held in February the 400ZR IA candidate (oif2017.245.15) was approved by principal member ballot and published to the OIF website as OIF-400ZR-01.0. See https://www.oiforum.com/wp-content/uploads/OIF-400ZR-01.0_reduced2.pdf.

During these sessions, a new project start was approved to address the known deficiencies in OIF400ZR-01.0 relative to updated market requirements. The scope of this new project will include documenting the requirements for 400ZR operation on a 75GHz grid. It also intends to integrate Error Vector Magnitude (EVM) testing methodology in the normative sections of the IA.

We very much appreciate our ability to liaise with the IEEE P802.3cw and look forward to sharing information with the Task Force as we further develop the 400ZR IA. We would appreciate any comments and information you can share with us on your P802.3cw Task Force progress. Our next meeting is scheduled for Aug 3rd – 7th 2020 in Toronto ON, Canada.

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Sincerely,

Ed Frlan,
OIF Technical Committee Chair (efrlan@semtech.com)