Dear Mr Otto and members of the OIF,

Thank you for your liaison letter “800LR IA Project Update”, dated 16 May 2023.

As the IEEE P802.3df and IEEE P802.3dj projects are both addressing 800 Gb/s Ethernet, status updates for both projects are provided.

The IEEE P802.3dj Task Force is in the midst of selecting baselines. As previously communicated, one of the objectives of this Task Force is to define a physical layer specification that supports 800 Gb/s operation over one wavelength over a single SMF in each direction with lengths up to at least 10 km. At the IEEE 802.3 May 2023 Interim meeting DP-16QAM modulation was selected for this interface.
The IEEE P802.3dj Task Force met at the IEEE 802 July 2023 plenary. At the meeting the following decisions were made:

1. A baseline was adopted that specifies the use of concatenated FEC based on RS(544,514) and BCH(126,110). Discussion continues on whether to use a wavelength in the O band or C band.

2. The task force adopted a DER.0 value of 2.67e-5 for AUIs within a PHY using 200 Gb/s PAM4 signaling. BER division between chip-to-chip and chip-to-module AUIs is a topic for further discussion.

For further information on task force activities, please see https://www.ieee802.org/3/dj/index.html.

The IEEE P802.3df Task Force also met at the IEEE 802 July 2023 Plenary, addressed comments submitted against IEEE P802.3df D2.1, and approved the generation of IEEE P802.3df D3.0 (attached). Additionally, the draft was approved to progress to the next level of balloting.

For further information please see https://www.ieee802.org/3/df/index.html.

We look forward to the continued collaboration between our two groups.

Sincerely,

David Law
Chair, IEEE 802.3 Ethernet Working Group