To: Mr Al Yanes, Chair, PCI-SIG <ayanes@us.ibm.com>
CC: Mr Paul Nikolich, Chair, IEEE 802 LMSC <p.nikolich@ieee.org>
    Mr John D’Ambrosia, Chair IEEE 802.3 BW Assessment Ad Hoc <jdambrosia@ieee.org>
    Mr Wael Diab, Vice-Chair, IEEE 802.3 Working Group <wdiab@broadcom.com>
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From: IEEE 802.3 Ethernet Working Group

Subject: Liaison letter to PCI-SIG on the Ethernet Bandwidth Assessment Ad Hoc

Date: 17th March 2011

Approval: Agreed to at IEEE 802.3 Plenary meeting, Singapore, 17th March 2011

Dear Mr. Yanes and members of PCI-SIG,

The IEEE 802.3 Ethernet Working Group would like to inform PCI-SIG that a new activity has been started under the IEEE Industry Connections Program titled “IEEE 802.3 Industry Connections Ethernet Bandwidth Assessment Ad Hoc”.


This IEEE Industry Connections activity will focus on gathering information that will enable an evaluation of the bandwidth needs for Ethernet wireline applications, including, but not limited to, core networking and computing. This evaluation will then enable the generation of material that can be used for future reference by an appropriate related standards activity. It should be noted that the role of this ad hoc is to gather information, not make recommendations or initiate a new project within the IEEE.

The Ad Hoc will operate using both face-to-face and teleconference meetings, at which the Ad Hoc encourages individuals from the PCI-SIG community to participate and provide input. Any data regarding prior or future data throughput trends that PCI-SIG or its members may be able to share with us would be gratefully received.

Sincerely,

David J Law
Chair, IEEE 802.3 Ethernet Working Group
<dlaw@hp.com>

1 This document solely represents the views of the IEEE 802.3 Working Group, and does not necessarily represent a position of the IEEE, the IEEE Standards Association, or IEEE 802.