IEEE P802.3cy Greater than 10 Gb/s Electrical Automotive Ethernet PHY Report

Steve Carlson High Speed Design, Inc. Robert Bosch GmbH Ethernovia Teleconference March 8, 2020

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IEEE P802.3cy Greater than 10 Gb/s Electrical Automotive Ethernet PHY TF Project information

Task Force Organization

Steve Carlson, Chair

Marek Hajduczenia, Chief Editor

Natalie Wienckowski, Ad Hoc Chair

Task Force web and reflector information

Reflector information: http://www.ieee802.org/3/B10GAUTO/reflector.html

Home page: http://ieee802.org/3/cy/index.html

PAR http://ieee802.org/3/cy/P802d3cy_PAR.pdf

CSD https://mentor.ieee.org/802-ec/dcn/20/ec-20-0094-00-ACSD-p802-3cy.pdf

Objectives http://grouper.ieee.org/groups/802/3/cy/P802d3cy_OBJ_WG_0520.pdf

Approved Timeline: https://grouper.ieee.org/groups/802/3/cy/P802_3cy_timeline_01_22_21.pdf

Private area: <u>http://ieee802.org/3/cy/private/index.html</u>

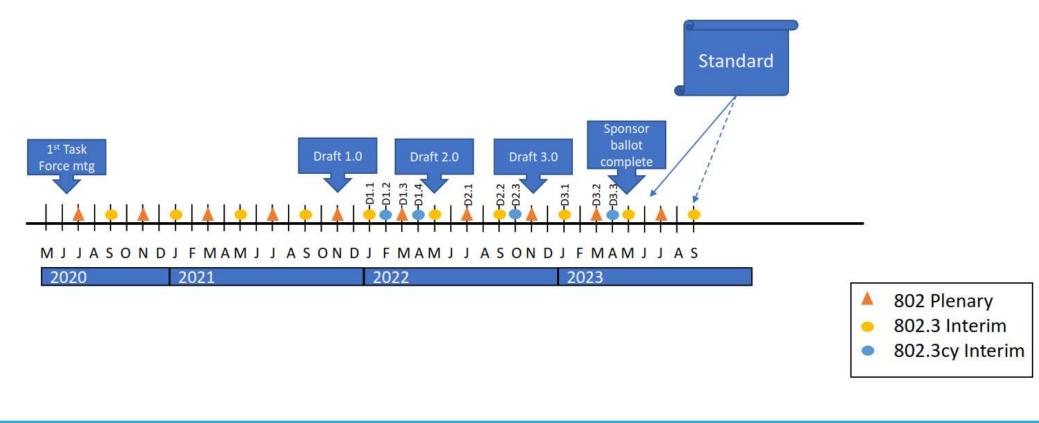
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IEEE P802.3 IEEE P802.3cy Greater than 10 Gb/s Electrical Automotive Ethernet PHY TF Activities since November 2020 update

- Ad hoc teleconferences -
 - 1 December, 8 December, 15 December, 5 January, 12 January, 19 January (2), 9 February, 16 February, 23 February, 1 March
 - Link segment/channel model
 - Simulation tool for PHY/link segment trade-off
 - Reports on connector/cables up to 10 Ghz; test methodologies
 - IL and RL
 - EMC and PCB
 - Initial discussion on how to lane
 - Work continues
- 26 January Interim teleconference decisions made
 - Adopted timeline: <u>https://grouper.ieee.org/groups/802/3/cy/P802_3cy_timeline_01_22_21.pdf</u>

IEEE P802.3cy B10GAUTO Task Force Adopted timeline



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Meeting plan

Goals for the meeting

Continue work on baselines for link segment/channel model

Meet 15 -16 March15h00-UTC

Ad hocs scheduled weekly on Tuesday

Interims as needed

Big ticket items

Link segment/channel model Channel model/link segment definition Link segment vs. PHY complexity tradeoffs Multi-lane architecture Build on 802.3ch experience

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Questions?



Thank you!

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