## IEEE 802.3 Ethernet Working Group Liaison Communication

Source: IEEE 802.3 Working Group<sup>1</sup>

To: Klaus-Holger Otto Technical Committee Chair, OIF

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CC: Konstantinos Karachalios Secretary, IEEE-SA Standards Board

Secretary, IEEE-SA Board of Governors

Paul Nikolich Chair, IEEE 802 LMSC

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John D'Ambrosia Chair, IEEE P802.3dj Task Force

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From: David Law Chair, IEEE 802.3 Ethernet Working Group

Subject: Request for information on CMIS-LT project

Approval: Agreed at IEEE 802.3 plenary meeting, Atlanta, GA, USA, 16 March 2023

Dear Mr Otto and members of the OIF,

The IEEE P802.3dj project includes objectives targeting 200 Gb/s Ethernet, 400 Gb/s Ethernet, 800 Gb/s Ethernet, and 1.6 Tb/s Ethernet chip-to-module electrical interfaces (referred to within IEEE 802.3 as "AUI"s) based on 200 Gb/s per lane signaling. The Task Force is currently in the process of selecting baseline proposals, and both in-band and out-of-band link training have been discussed for these interfaces.

The Task Force is interested in learning more about the OIF CMIS-LT project, which defines out-of-band link training, and would like to request that the OIF share any relevant information going forward.

We look forward to the continued collaboration between our two groups.

Sincerely, David Law

Chair, IEEE 802.3 Ethernet Working Group

<sup>&</sup>lt;sup>1</sup> This document solely represents the views of the IEEE 802.3 Working Group and does not necessarily represent a position of the IEEE, the IEEE Standards Association, or IEEE 802.