

# IEEE 802.3 Pin-Optimized PHY Interface Task Force Closing Report

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# IEEE 802.3dq Pin-Optimized PHY Interface Task Force

## Task Force information

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### Task Force Organization

Jason Potterf, IEEE 802.3dq Pin-Optimized PHY Interface Task Force Chair

Jon Lewis, IEEE 802.3dq Pin-Optimized PHY Interface Task Force Secretary

### Task Force charter

Move that the IEEE 802.3 Working Group request the formation of a Task Force to develop a Project Authorization Request (PAR) and Criteria for Standards Development (CSD) responses for an Ethernet Media Independent Interface (MII) optimized for an exposed interconnect.

### Task Force web and reflector information

Reflector information: <https://www.ieee802.org/3/POPI/reflector.html>

Home page: <https://www.ieee802.org/3/dq/index.html>

# IEEE 802.3 Pin-Optimized PHY Interface Task Force Activities This Week

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Met Tuesday afternoon during plenary

- Saw two presentations on architectural direction
- Productive discussion of open issues and work items

# IEEE 802.3 Pin-Optimized PHY Interface Task Force

## Next Steps

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Continue architectural discussion in order to move towards baseline text

Next Meeting:

POPI Meeting Timeslot:

8 AM Pacific US / 10 AM Central US / 11 AM Eastern US / 4 PM GMT for 2 hours

Next Virtual Instance(s):

Presentation Request Deadline	Presentation Submission Deadline	Meeting Date
Friday April 3, 2026	Sunday, April 5, 2026	Tuesday, April 7, 2026

Questions?

Thank You!