

# IEEE P802.3dq Pin-Optimized PHY Interface Task Force Report

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# IEEE P802.3dq Pin-Optimized PHY Interface Task Force

## Project information

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### Task Force Organization

Jason Potterf, IEEE P802.3dq Task Force Chair

Jon Lewis, IEEE P802.3dq Task Force Secretary

TBD, IEEE P802.3dq Task Force Chief Editor

### Task force web and reflector information

Reflector information: <http://www.ieee802.org/3/POPI/reflector.html>

Home page: <http://ieee802.org/3/dq/index.html>

PAR: [https://www.ieee802.org/3/dq/P802.3dq\\_PAR.pdf](https://www.ieee802.org/3/dq/P802.3dq_PAR.pdf)

CSD: <https://mentor.ieee.org/802-ec/dcn/25/ec-25-0128-02-LMSC-ieee-p802-3dq-draft-csd.pdf>

Objectives: [https://www.ieee802.org/3/dq/POPI\\_DRAFT\\_Objectives\\_2025-06-18\\_v01.pdf](https://www.ieee802.org/3/dq/POPI_DRAFT_Objectives_2025-06-18_v01.pdf)

# IEEE P802.3dq Pin-Optimized PHY Interface Task Force Activities since P802.3dq PAR Approval

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Held one Interim meeting in January of 2026

Major items discussed, decisions made and actions

- Heard presentation on POPI Requirements for a 100M Single PHY

- Developed rough draft work item list

Current status

- Refining Work Items

- Considering Architectural Proposals

# IEEE P802.3dq Pin-Optimized PHY Interface Task Force Plans

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Task Force will meet during the March 2026 Plenary but will not be making any decisions

## Big ticket items

- Architectural proposals to move towards baseline text

## Future

- Meetings will continue between plenaries to advance towards a draft standard

Questions?

Thank you!