Closing Report

IEEE P802.3bs 400GbE Task Force

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IEEE 802 Nov 2014 Plenary

Reflector and Web

- To subscribe to the 400G reflector, send an email to:

  ListServ@ieee.org

  with the following in the body of the message (do not include “<>”):

  subscribe stds-802-3-400G <yourfirstname> <yourlastname>

  end

- Send 400G reflector messages to:

  STDS-802-3-400G@listserv.ieee.org

- Task Force web page URL:

  http://www.ieee802.org/3/bs/index.html

- Ad hoc area URL:

  http://www.ieee802.org/3/bs/public/adhoc/index.shtml

Private Area

- URL: http://www.ieee802.org/3/bs/private/index.html
  - Username: xxxxxx
  - Password: xxxxxx

- Write it down…

- Note - The draft, and any other content, is posted for your review only, and neither the content nor access information should be copied or redistributed to others in violation of document copyrights.

This Week

- ≈ 135 Attendees
- Communications from OIF and CDFP MSA. Responses deferred until Jan 2015
- Reviewed 46 Technical Presentations
  - Initial proposals for all identified areas received
- Discussed 50GbE / 200GbE
- Motions:
  - Move to adopt the proposal in slides 6 to 16 in king_3bs_02_1114.pdf as the baseline proposal for the P802.3bs objective to “provide physical layer specifications which support link distances of at least 100 m of MMF” (400GBASE-SR16)Results: All y/n/a: 105/0/18
- Straw polls – see next slides

Straw Polls (1of 2)

1	I support channel equations in Goergen_3bs_01a_1114.pdf slide 15 "blue curves" for C2C and C2M as a target for further evaluation • Yes • No • Undecided • Abstaining	 45 3 26 39
2	For chip-to-module interconnect: I support the following chip-to-module ELECTRICAL interconnect modulation for 400GbE (a) NRZ for 50 Gb/s (b) PAM4 for 50 Gb/s (c) undecided (d) abstaining	 20 40 36 24
3	For chip-to-chip interconnect: I support the following chip-to-chip ELECTRICAL interconnect modulation for 400GbE: (a) NRZ for 50 Gb/s (b) PAM4 for 50 Gb/s (c) undecided (d) abstaining	 15 41 33 28

Straw Polls (2 of 2)

1	Should 802.3 standardize 50Gb/s Ethernet? (a) No (b) Yes, as part of 802.3bs Project (P802.3bs PAR modification or new PAR by same Task Force), pending approval of new CFI in Mar 2015. (c) Yes, in a separate, new 802.3 Project. (d) No opinion at this time, don't care, abstain.	 0 8 86 20

Moving Forward

- Consideration of new material presented at this week
- Offline consensus building
- Ad Hoc Meetings

- Decisions are supposed to made at the next meeting per the adopted schedule.

Future Meetings

- See: http://www.ieee802.org/3/interims/index.html
- Jan 2015 Interim
  - Week of Jan 12
  - Hyatt Regency, Atlanta, GA, USA
  - Hosted by IEEE 802
- Mar 2015 Plenary
  - Week of Mar 8
  - Estrel Hotel and Convention Center, Berlin, Germany
- May 2015 Interim
  - Week of May 18, 2015
  - Omni William Penn Hotel, Pittsburgh, PA, USA
  - Hosted by Ethernet Alliance
- July 2015 Plenary
  - Week of July 12
  - Hilton Waikoloa Village, Waikoloa, HI
- Sept 2015 Interim
  - Week of Sept 14
  - Hyatt Regency Coconut Point, Bonita Springs, FL
  - Hosted by NEC

- Anyone interested in hosting a meeting or webex contact me.

Future Ad Hoc Calls (Tenative)

- Nov 18 Tuesday SMF 7:30am to 9:30am PST
- Nov 20 Thursday Electrical 7:30am to 9am PST
- Dec 2 Tuesday Logic 8:00am to 10:00am PST
- Dec 4 Thursday Electrical 7:30am to 9am PST
- Dec 9 Tuesday SMF 7:30am to 9:30am PST
- Dec 16 Tuesday Logic 8:00am to 10:00am PST
- Dec 18 Thursday Electrical 7:30am to 9am PST
- Jan 2 Friday Electrical 7:30am to 9am PST

Note –Dates / times listed are subject to change.

Thank You!

Backup

Task Force Team

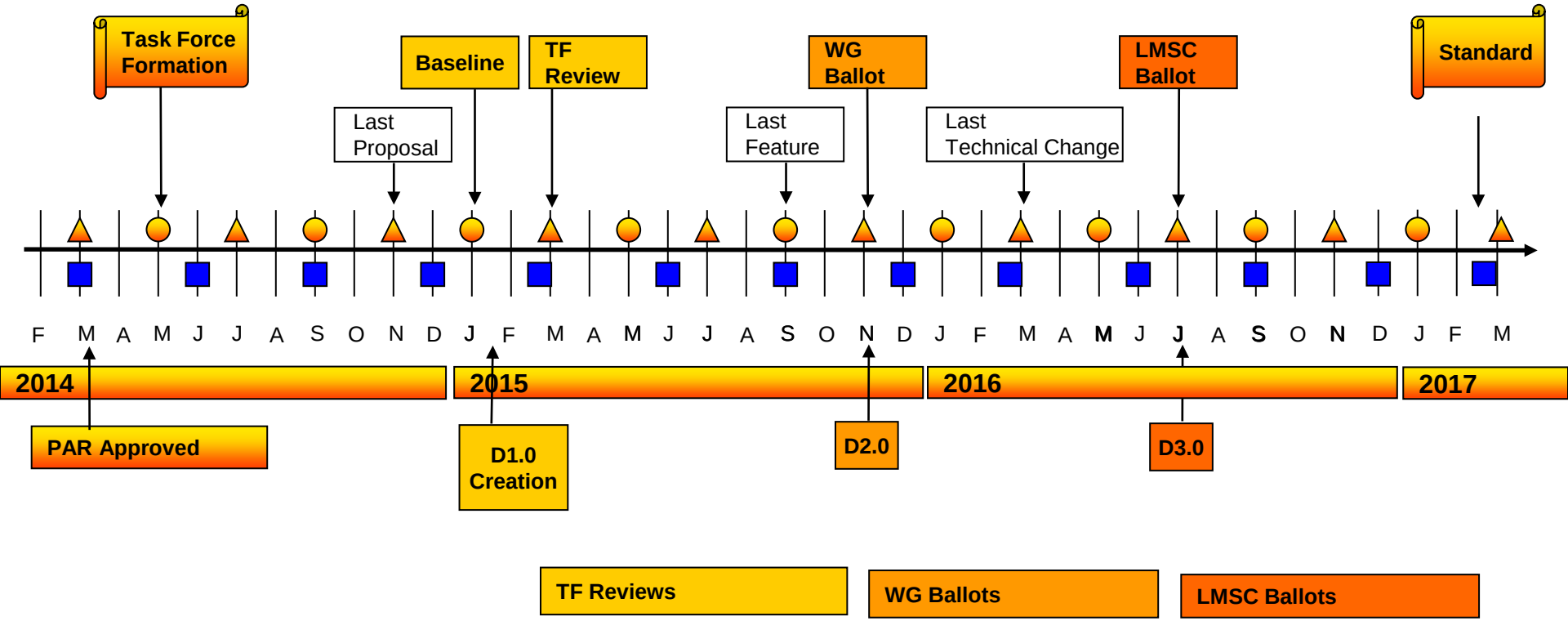
- John D'Ambrosia: Chair
- Pete Anslow:
  - Chief Editor
  - SMF Ad hoc Chair
- Mark Gustlin, Logic Ad Hoc Chair
- Jonathan King: MMF Ad Hoc Chair
- Gary Nicholl: Use Case Ad Hoc Chair
- Joel Goergen / Vasu Parthasarathy: Electrical Interface Ad Hoc Co Chairs

Project Objectives

- Support a MAC data rate of 400 Gb/s
- Support a BER of better than or equal to 10^{-13} at the MAC/PLS service interface (or the frame loss ratio equivalent)
- Support full-duplex operation only
- Preserve the Ethernet frame format utilizing the Ethernet MAC
- Preserve minimum and maximum FrameSize of current Ethernet standard
- Provide appropriate support for OTN
- Specify optional Energy Efficient Ethernet (EEE) capability for 400 Gb/s PHYs
- Support optional 400 Gb/s Attachment Unit Interfaces for chip-to-chip and chip-to-module applications
- Provide physical layer specifications which support link distances of:
  - At least 100 m over MMF
  - At least 500 m over SMF
  - At least 2 km over SMF
  - At least 10 km over SMF

* Approved by IEEE 802.3 WG, 20-Mar-2014

IEEE P802.3bs 400GbE Adopted Timeline



Adopted by IEEE P802.3bs 400GbE Task Force, May 2014 Interim.