

IEEE 802.3ch Multi-Gig Automotive Ethernet PHY Task Force Opening Report

Steve Carlson

High Speed Design, Inc./Robert Bosch GmbH/Marvell

Bangkok, Thailand

November 12, 2018

IEEE 802.3 Multi-Gig Automotive Ethernet PHY Task Force information

Task Force Organization

Steve Carlson, Chair

Natalie Wienckowski, Chief Editor, Curtis Donahue, PICS Editor

George Zimmerman, Ad Hoc Chair

Task Force web and reflector information

Reflector information:

<http://www.ieee802.org/3/NGAUTO/reflector.html>

Home page: <http://ieee802.org/3/ch/index.html>

PAR

<http://ieee802.org/3/ch/P802.3ch.pdf>

5 Criteria

<https://mentor.ieee.org/802-ec/dcn/17/ec-17-0069-00-ACSD-802-3ch.pdf>

Objectives

http://ieee802.org/3/ch/0317_approved_objectives_3NGAUTO.pdf

Private area: <http://ieee802.org/3/ch/private/index.html>

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IEEE 802.3 Multi-Gig Automotive Ethernet PHY Task Force

Activities since July 2018 plenary

7 ad hoc calls on link segment, PoDL, Reed-Solomon-FEC, OAM, EEE, asymmetry, link topologies, and more!

Met at September interim for 2 days

Major items discussed, decisions made and actions

1 straw poll, 23 motions, 18 presentations

Adopted PCS block coding 64/65B; RS FEC ($N=360$, $K=326$, $m=10$), consisting of 50 PCS 64B/65B blocks, a 10-bit field formerly known as OAM, and 34 parity symbols, as the baseline; PAM 2 for training and side-stream; data mode scrambler polynomials; PAM4 mapping; interleaver; selectable fixed pre-coder; primitive polynomial

Comment resolution to D0.5; updated draft to D0.6

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Meeting week plan

Goals for the meeting

Continue filling in holes to move to Task Force review D1.0

Big ticket items

PoDL

OAM

EMC

Adopt additional draft text and create D1.0 and start review

Timeline: on schedule to produce D1.0 out of November plenary

Questions?



Thank you!