

IEEE P802.3dq Pin-Optimized PHY Interface Task Force Report

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IEEE P802.3dq Pin-Optimized PHY Interface Task Force

Project information

Task Force Organization

Jason Potterf, IEEE P802.3dq Task Force Chair

Jon Lewis, IEEE P802.3dq Task Force Secretary

TBD, IEEE P802.3dq Task Force Chief Editor

Task force web and reflector information

Reflector information: <http://www.ieee802.org/3/POPI/reflector.html>

Home page: <http://ieee802.org/3/dq/index.html>

PAR: https://www.ieee802.org/3/dq/P802.3dq_PAR.pdf

CSD: <https://mentor.ieee.org/802-ec/dcn/25/ec-25-0128-02-LMSC-ieee-p802-3dq-draft-csd.pdf>

Objectives: https://www.ieee802.org/3/dq/POPI_DRAFT_Objectives_2025-06-18_v01.pdf

IEEE P802.3dq Pin-Optimized PHY Interface Task Force Activities since P802.3dq PAR Approval

Held one Telephonic Interim and one AdHoc in October of 2025

Major items discussed, decisions made and actions

- Jason Potterf confirmed as Chair by Task Force

- Objectives reviewed and refinements suggested

Current status

- Finalizing Objectives and Considering Architectural Proposals

IEEE P802.3dq Pin-Optimized PHY Interface Task Force Plans

Task Force will not during the November 2025 Plenary

Task Force will Meet Next on November 25, 2025 at 11 AM Eastern US / 4 PM GMT

Big ticket items

- Architectural Proposals to Help Develop Objectives and Timeline

- Objectives

- Timeline

Future

- Meetings will continue between plenaries to advance towards a draft standard

Questions?

Thank you!