IEEE P802.3dw FMP Ethernet Powering Techniques Closing Report

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IEEE P802.3dw FMP Ethernet Powering Techniques Project information

Task Force Organization

Bob Voss, IEEE P802.3dw Task Force Chair

Task force web and reflector information

Reflector information: http://www.ieee802.org/3/dw/reflector.html

Home page: http://ieee802.org/3/<<*xx*>>/index.html

PAR: https://www.ieee802.org/3/EFMP/public/260715/P802.3dw_PAR_150726.pdf

CSD: https://www.ieee802.org/3/EFMP/public/260715/CSD_EFMP_20260715.pdf

Objectives: https://www.ieee802.org/3/EFMP/public/260715/cmjones_EFMP_0726_objectives_V2.pdf

IEEE P802.3dw FMP Ethernet Powering Techniques Activities during November 2026 plenary

- Met during the November 2026 plenary meeting series
  - Major items discussed
    - Architectural Considerations for Ethernet Fault Managed Power
    - Link Segment Considerations
  - Current status
    - Contributions sought to address project objectives

IEEE P802.3dw FMP Ethernet Powering Techniques
Adopted timeline

Not Established Yet

Questions?

Thank you!