IEEE P802.3dq Pin-Optimized PHY Interface Task Force Report

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2026-11-09

IEEE P802.3dq Pin-Optimized PHY Interface Task Force
Project information

Task Force Organization

- Jason Potterf, IEEE P802.3dq Task Force Chair
- Jon Lewis, IEEE P802.3dq Task Force Secretary

Task force web and reflector information

- Reflector information: http://www.ieee802.org/3/POPI/reflector.html
- Home page: http://ieee802.org/3/dq/index.html
  - PAR: https://www.ieee802.org/3/dq/P802.3dq_PAR.pdf
  - *CSD*: https://mentor.ieee.org/802-ec/dcn/25/ec-25-0128-02-LMSC-ieee-p802-3dq-draft-csd.pdf
  - Objectives: https://www.ieee802.org/3/dq/POPI_DRAFT_Objectives_2025-06-18_v01.pdf

IEEE P802.3dq Pin-Optimized PHY Interface Task Force Activities since September Interim in Lisbon

None

IEEE P802.3dq Pin-Optimized PHY Interface Task Force Plans

- Task Force will meet during the November 2026 Plenary
- Big ticket items
  - Architectural proposals to move towards baseline text
- Future
  - Meetings will continue at plenaries to advance towards a draft standard
  - Meetings between plenaries will only be scheduled for committed contributions

Questions?

Thank you!