



August 20, 2018

To: David Law and participants of the IEEE 802.3 Working Group
cc: Peter Anslow, 802.3 WG Secretary
John D'Ambrosia – Beyond 10km Optical PHYs Study Group Chair

Subject: 400ZR Interop Project

From: Klaus-Holger Otto, OIF Technical Committee Chair (klaus-holger.otto@nokia.com)

Dear Mr. Law and participants of IEEE 802.3 Working Group,

The OIF Q2 Technical and MA&E Committees meetings were held in Vancouver BC Canada. On Monday July 30th, 2018 the OIF PLL WG – Optical track held an interim meeting, followed by several working sessions from July 31st through August 2nd.

The 400ZR Implementation Agreement (IA) now includes a complete definition of the digital format and processing, along with some basic analog parameters for the network interface. The TX/RX data paths also include a set of test vectors to ensure DSP design compatibility. Full interoperability of implementations at the optical interface will require additional parameters to be specified and measurements of real implementations and devices.

A parallel work group completed work on a contribution to the QSFP-DD MIS Advisors recommending an update to the memory map in support of Coherent modules. The framework of this work will be captured in a future version of the 400ZR IA

We would be very interested in receiving an update from the IEEE 802.3 task forces, specifically progress on P802.3cd and the beyond 10K Study Group. We would also appreciate any comments and feedback you may have on the IA # OIF-400ZR 0.9 Draft (oif2017.245.09).

We will continue to update the IEEE 802.3 Working Group on the progress of the 400ZR IA. Our next meeting in Sydney, Australia, October 29th – November 1st, 2018.

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Sincerely,
Klaus-Holger Otto,
OIF Technical Committee Chair

cc: David Stauffer, OIF PLL WG Chair; Karl Gass, OIF PLL WG Optical Vice Chair
Attachment: oif2017.245.09 OIF-400ZR 0.9 Draft