



PRELIMINARY MAKE/BREAK UNDERLOAD TESTING

- PRODUCT - AMP SHIELDED RJ45 SMT JACK
- SET-UPS:
 - 48 Vdc, 312 mA
 - 12 Vdc, 1.25 A
 - CENTER PAIR (POSITIONS 4 & 5) ENERGIZED
 - 750 MATING CYCLES UNDERLOAD
 - CONTACT RESISTANCE AND VISUAL EXAMINATION COMPLETED AT THE FOLLOWING INTERVALS; 50, 100, 250, 500 & 750 CYCLES.



PRELIMINARY FINDINGS

■ 48Vdc TEST

- PRELIMINARY VISUAL EXAMINATIONS & CONTACT RESISTANCE MEASUREMENTS DID NOT APPEAR TO SHOW ANY ADVERSE EFFECTS UP TO 750 CYCLES.

■ 12Vdc TEST

- PRELIMINARY VISUAL EXAMINATIONS & CONTACT RESISTANCE MEASUREMENTS DID SHOW SOME POTENTIAL EFFECTS ON THE CONTACT INTERFACE.
- SCANNING ELECTRON MICROSCOPE (SEM) ANALYSIS OF CONTACT INTERFACES NEEDS TO BE COMPLETED TO CONFIRM THE IMPACT OF MATING UNDERLOAD AT THIS CIRCUIT LEVEL.



FUTURE INVESTIGATIONS (IF REQUIRED)

- LARGER TEST GROUPS
- ENERGIZE ALL 4 PAIRS
- ALTERNATE CIRCUIT CONDITIONS