

1. Station Tables

Comparison: (FR_BN, FR_CT, FR_RP, FR_SMP) versus (FR_MAC).

9.2 Station Operating Table Extraction: JS=SLT			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JB0	3124	FR_BN(SA<>MA) & JS=SLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JB0	3125	FR_CT(SA<>MA) & JS=SLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JB0	3127	FR_RP(SA<>MA) & JS=SLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JB0	3165	FR_SMP(SA<>MA) & JS=SLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JB0	3103	FR_MAC(SA<>MA & SA<>SUA & VC=00) & JS=SLT	JS=BP
JB0	3107	FR_MAC(SA<>SUA & VC=03) & JS=SLT	JS=BP

Comparison: (FR_PHB, FR_SMP) versus (FR_MAC).

9.2 Station Operating Table Extraction: JS=SDAC			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JC0	3136	FR_PHB(SA<>SUA) & JS=SDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JC0	3162	FR_SMP(SA<>MA) & JS=SDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JC0	3112	FR_MAC(SA<>MA & SA<>SUA & VC=00) & JS=SDAC	JS=BP
JC0	3137	FR_MAC(SA<>SUA & VC=03) & JS=SDAC	JS=BP

Comparison: (FR_PHB) versus (FR_MAC).

9.2 Station Operating Table Extraction: JS=SJC			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JD0A	3152	FR_PHB(SA<>SUA) & FSMRO<2 & JS=SJC	JS=BP; Remove_station Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
	3184	FR_PHB(SA<>SUA) & FSMRO>1 & JS=SJC	CSRAT=n8; TSRAP=R; TXI_RMV_ALRT Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JD0A	3146	FR_MAC(SA<>SUA & VC=00) & FSMRO<2 & JS=SJC	JS=BP; Remove_station
JD0A	3148	FR_MAC(SA<>SUA & VC=03) & FSMRO<2 & JS=SJC	JS=BP; Remove_station
	3182	FR_MAC(SA<>SUA & VC=00) & FSMRO>1 & JS=SJC	CSRAT=n8; TSRAP=R; TXI_RMV_ALRT
	3183	FR_MAC(SA<>SUA & VC=03) & FSMRO>1 & JS=SJC	CSRAT=n8; TSRAP=R; TXI_RMV_ALRT

2. C-Port Tables

Comparison: (FR_BN, FR_CT, FR_RP, FR_SMP) versus (FR_MAC).

9.3 Port Operating Table Extraction: JS=PDAC			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JM0	1044	FR_AMP & JS=PDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JM0	1041	FR_BN & JS=PDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JM0	1043	FR_CT & JS=PDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JM0	1042	FR_RP & JS=PDAC	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JM0	1060	FR_MAC(DA=any_recognized_address & SA<>SUA & VC=00) & JS=PDAC	JS=BP
JM0	1082	FR_MAC(DA=any_recognized_address & SA<>SUA & VC=30) & JS=PDAC	JS=BP

Comparison: (FR_PHB, FR_SMP) versus (FR_MAC).

9.3 Port Operating Table Extraction: JS=PLT			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JL0	1030	FR_AMP & JS=PLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JL0	1027	FR_BN & JS=PLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JL0	1029	FR_CT & JS=PLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JL0	1028	FR_RP & JS=PLT	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JL0	1059	FR_MAC(DA=any_recognized_address & SA<>SUA & VC=00) & JS=PLT	JS=BP
JL0	1090	FR_MAC(DA=any_recognized_address & SA<>SUA & VC=30) & FPMRO<2 & JS=PLT	JS=BP
	1125	FR_MAC(DA=any_recognized_address & SA<>SUA & VC=30) & FPMRO>1 & JS=PLT	CPRAT=n9; TPRAP=R; TXI_RMV_ALRT

Comparison: (FR_PHB) versus (FR_MAC).

9.3 Port Operating Table Extraction: JS=PJCI			
S/T	REF	EVENT / EVENT & CONDITIONS	ACTIONS / OUTPUTS
JP0	1052	FR_AMP & JS=PJCI	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JP0	1053	FR_CT & FPMRO<2 & JS=PJCI	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
	1123	FR_CT & FPMRO>1 & JS=PJCI	CPRAT=n9; TPRAP=R; TXI_RMV_ALRT Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JP0	1054	FR_RP & FPMRO<2 & JS=PJCI	JS=BP Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
	1126	FR_RP & FPMRO>1 & JS=PJCI	CPRAT=n9; TPRAP=R; TXI_RMV_ALRT Works when: (SA=SUA & VC=00: valid frame) or (SA=SUA & VC=03: invalid frame)
JP0	1055	FR_MAC (DA=any_recognized_address & SA<>SUA & VC=00) & JS=PJCI	CPRAT=n9; TPRAP=R; TXI_RMV_ALRT
JP0	1084	FR_MAC (DA=any_recognized_address & SA<>SUA & VC=30) & FPMRO<2 & JS=PJCI	JS=BP
	1124	FR_MAC (DA=any_recognized_address & SA<>SUA & VC=30) & FPMRO>1 & JS=PJCI	CPRAT=n9; TPRAP=R; TXI_RMV_ALRT