



HSTR Speed Trade-up

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TXI Registration 4/16/100 Mbps

- **C-Port**
 - Parallel detection
 - 4/16/100 Mbps
- **Station**
 - **Port in Station Emulation**
 - Alternate TxDriver enable
 - JS=SREG 4/16 Mbps
 - JS=SREG 100 Mbps



TXI Registration, 16 Mbps

C-port		Station, C-port Station Emulation
JS=PREG, 100/16/4 Mbps		JS=SREG, 16/4 Mbps
JS=PREG, 16/4 Mbps		TXI(REG_REQ)
	← REG_REQ (TXI, 100 BaseTX capable)	
RX(REG_REQ)		
TXI(REG_RSP)		
	REG_RSP → (100 BaseTX capable)	
Delay 10-20 msec		RX(REG_RSP)
JS=PREG, 100/16/4 Mbps		JS=BP
		JS=SREG, 100 Mbps
JS=PREG, 100 Mbps		TXI(REG_REQ)



DTR add on

- AP_REQ subvector
- AP_RSP subvector
- New bit definition:
 - **X'0004' 100 BaseTX capability**